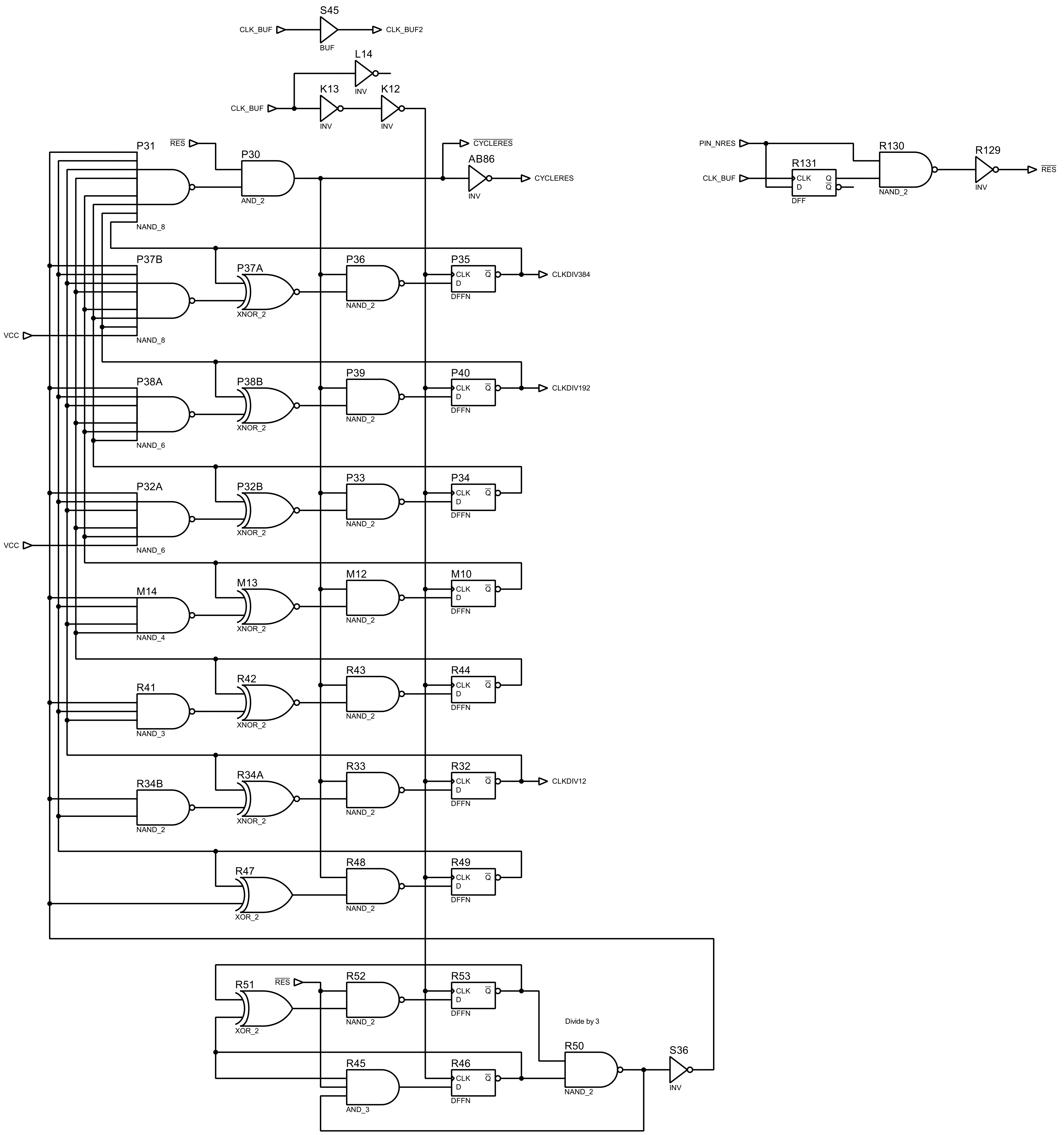

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Konami 054539
CLOCK DIV LOW

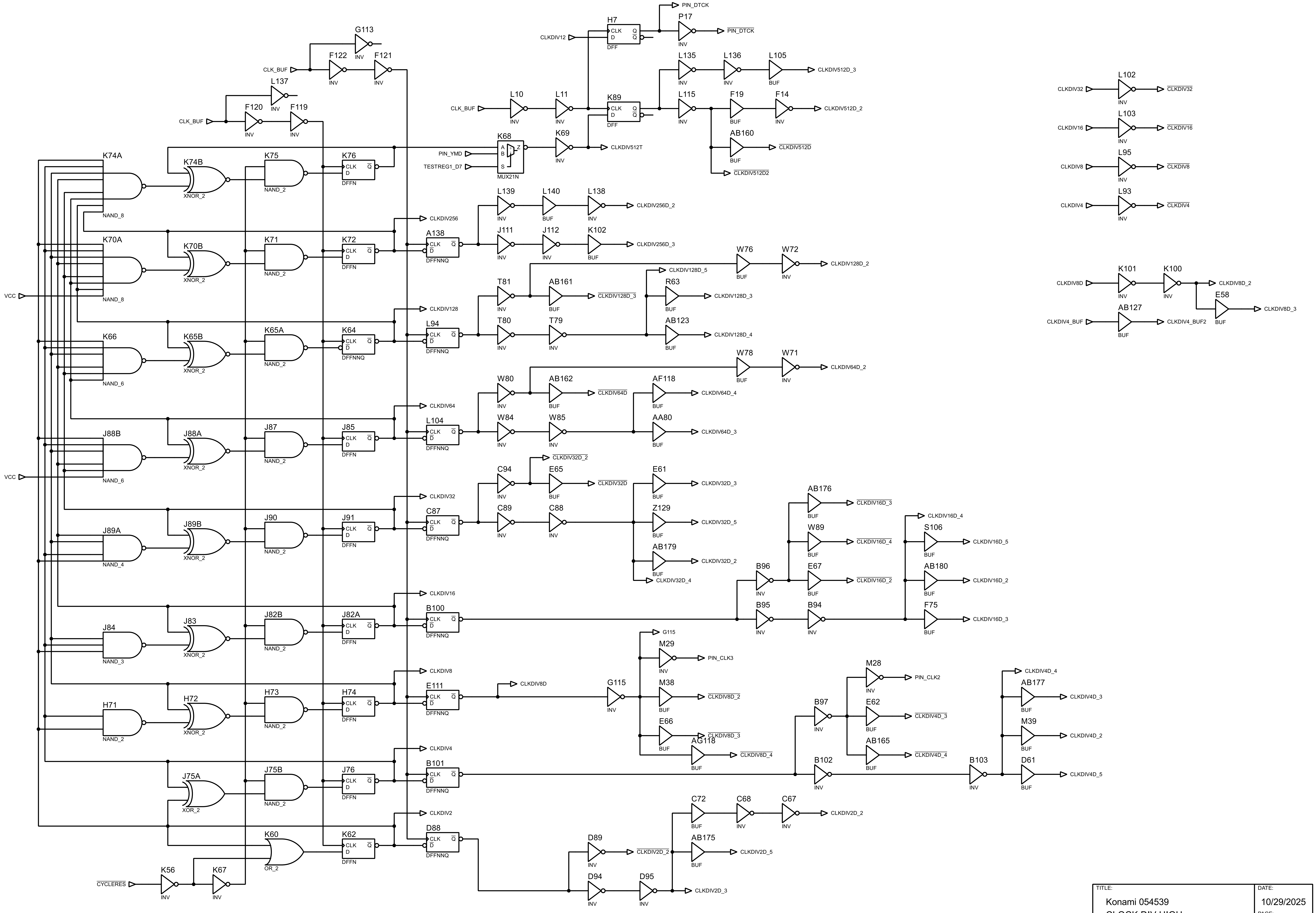
DATE:

10/29/2025

PAGE:

1/53

BY: Sean Gonsalves REV: @REV



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Konami 054539
CLOCK DIV HIGH

DATE:

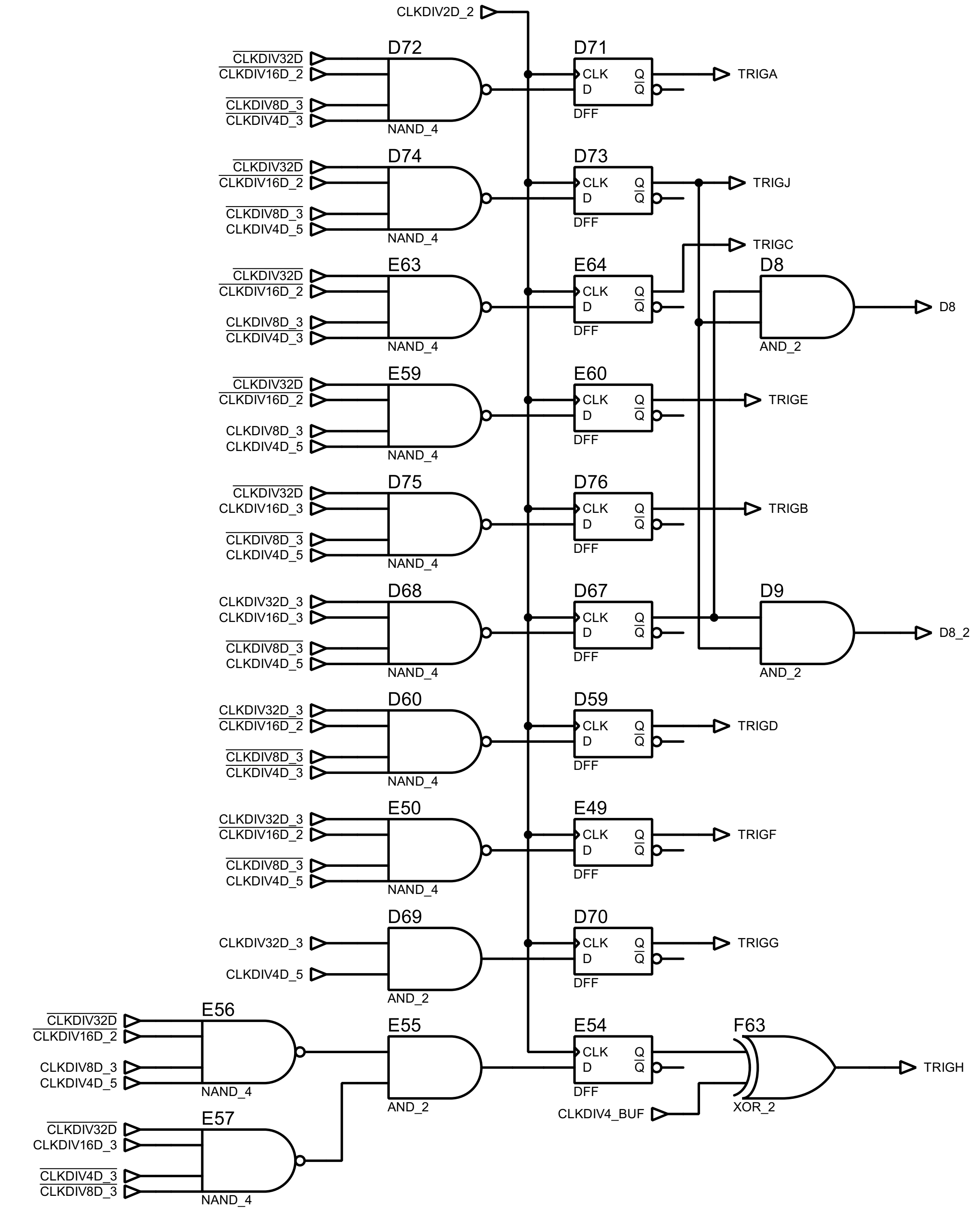
10/29/2025

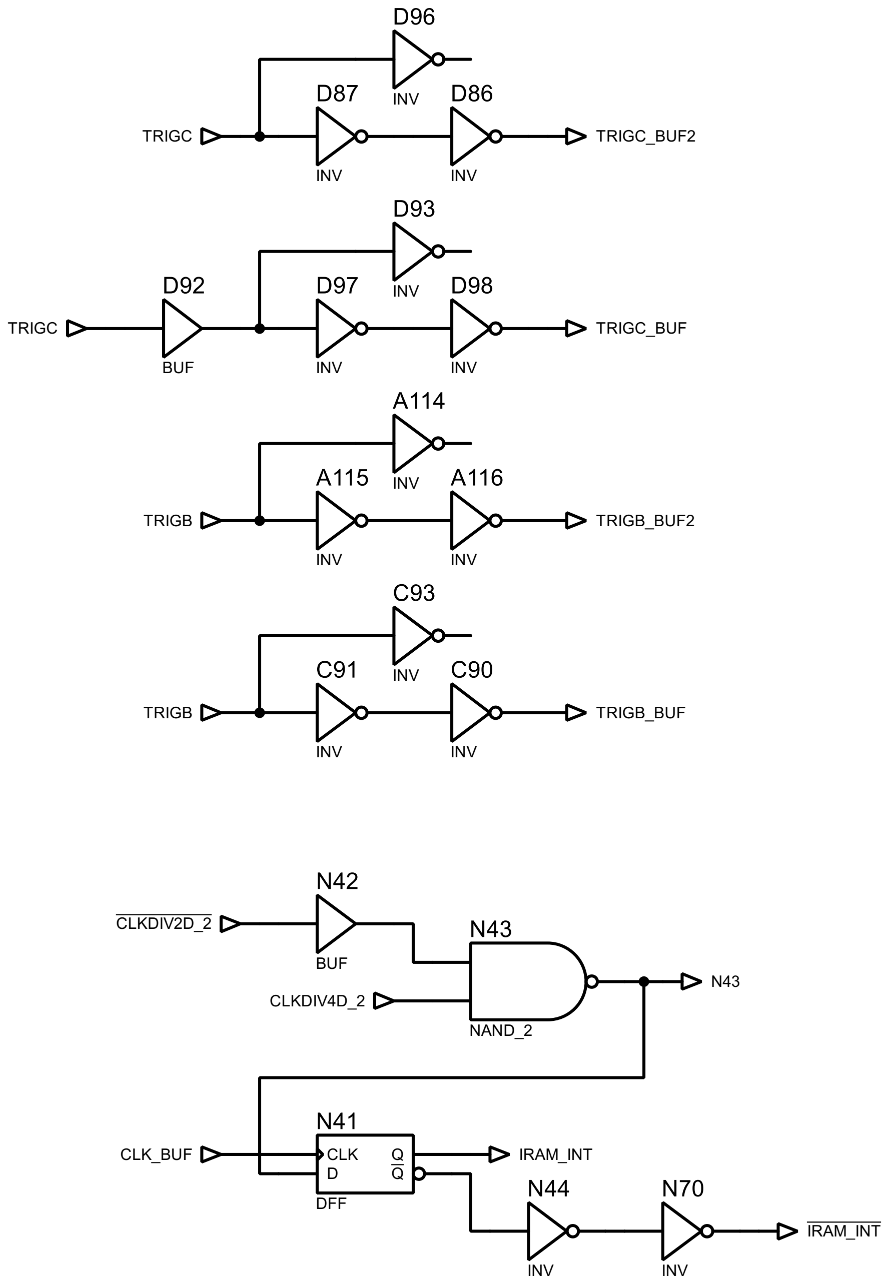
PAGE:

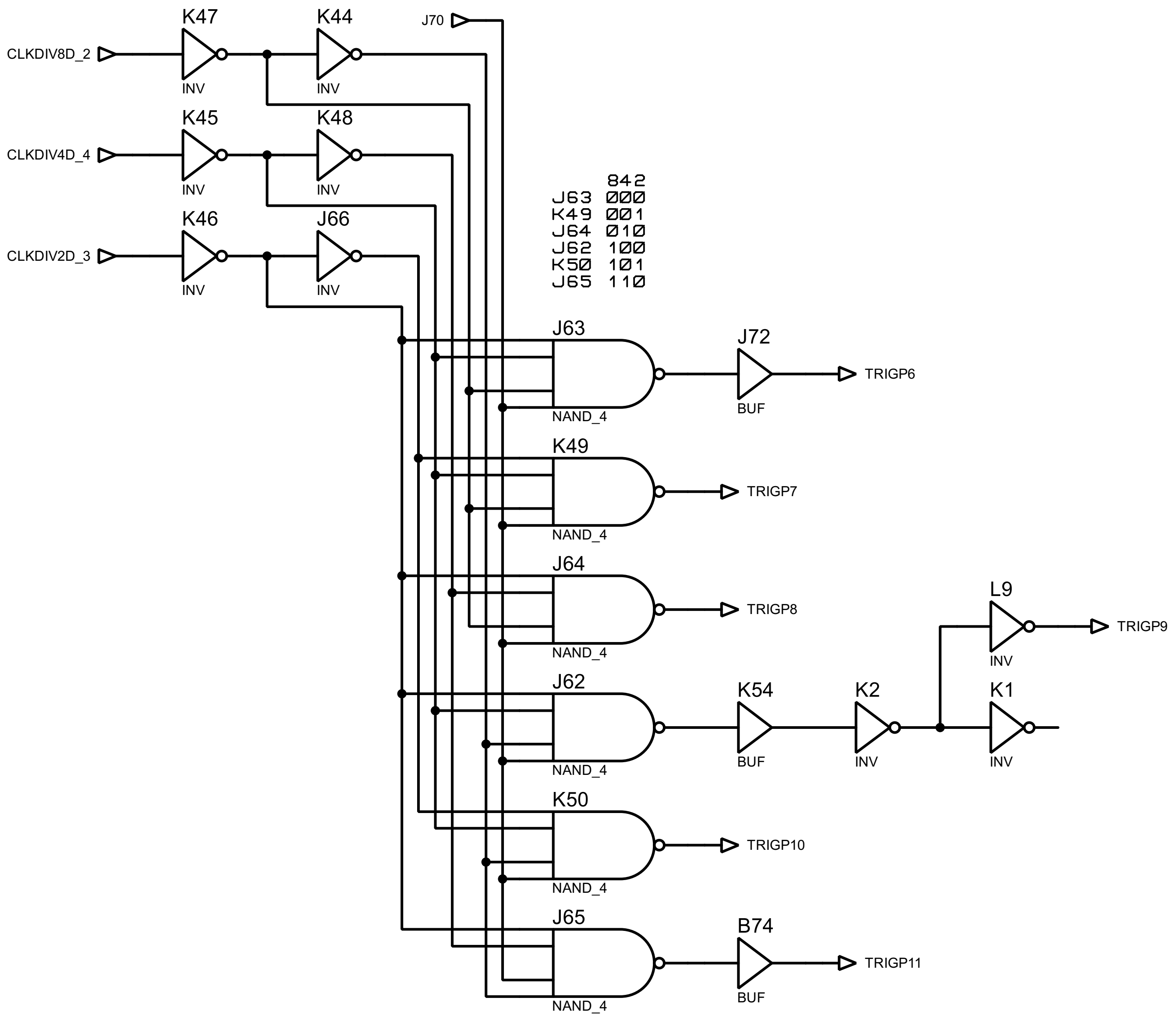
2/53

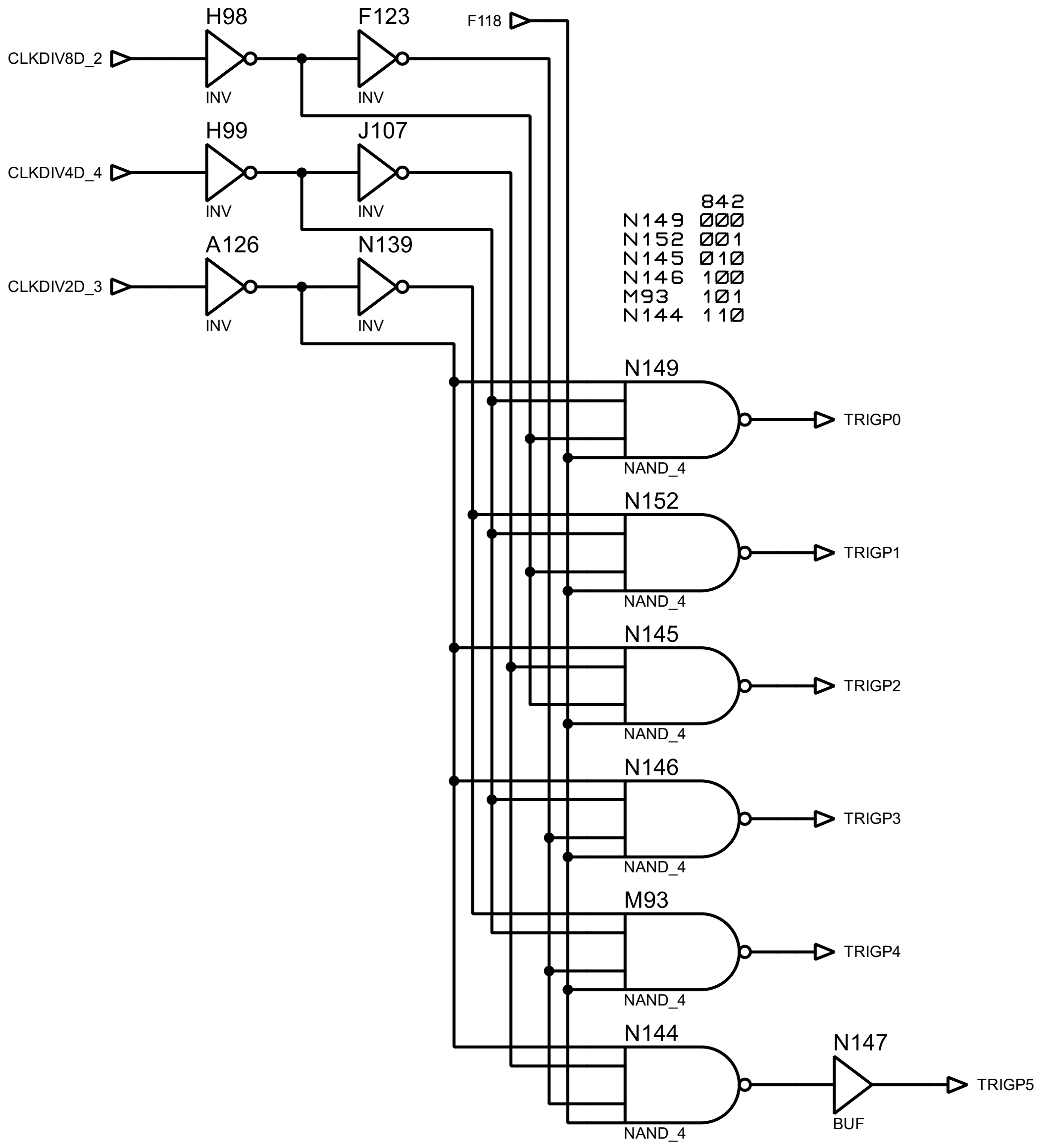
BY: Sean Gonsalves

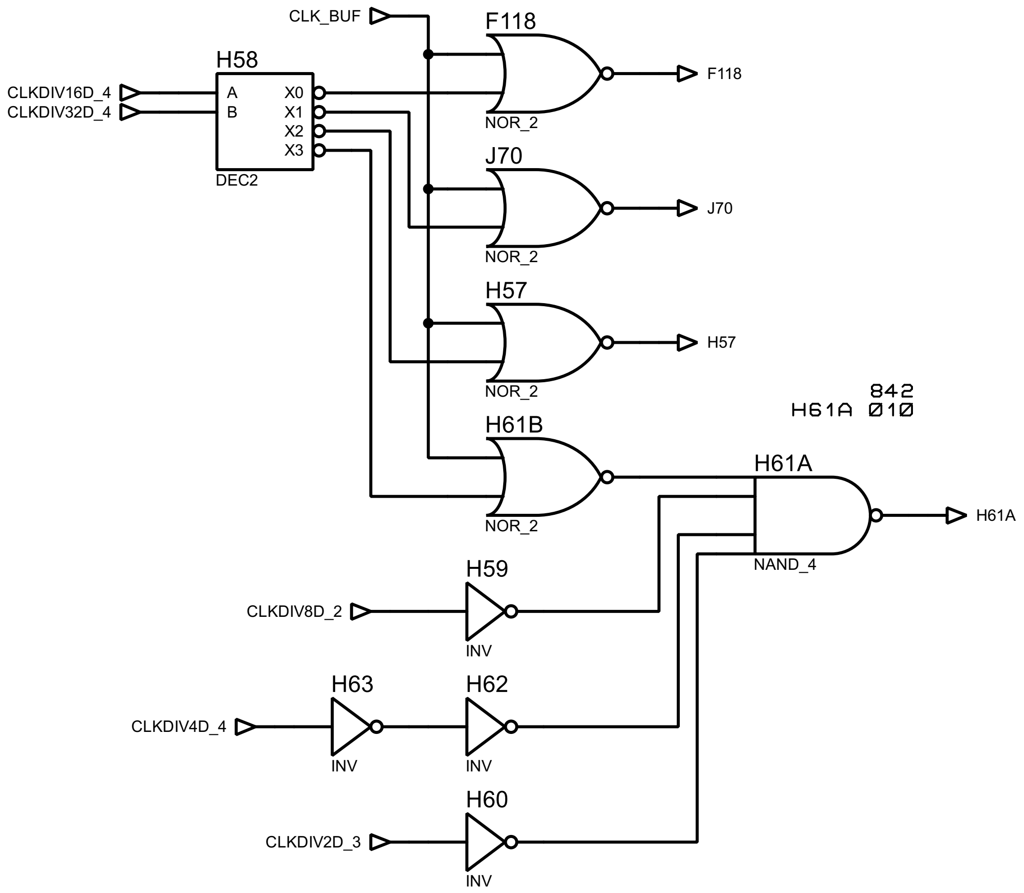
REV: @REV

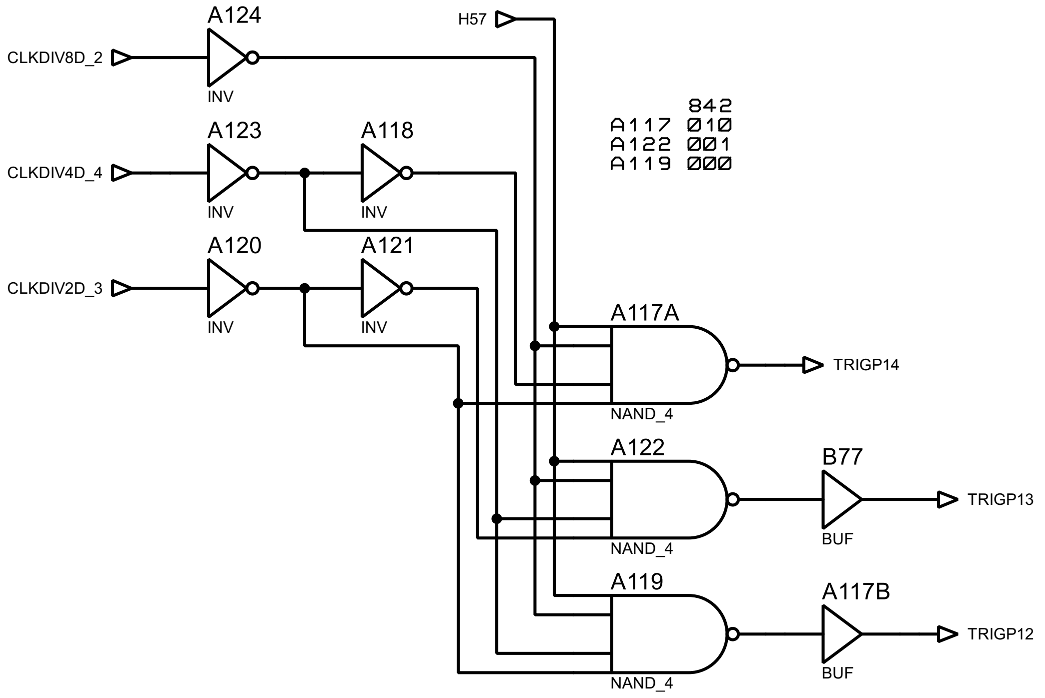










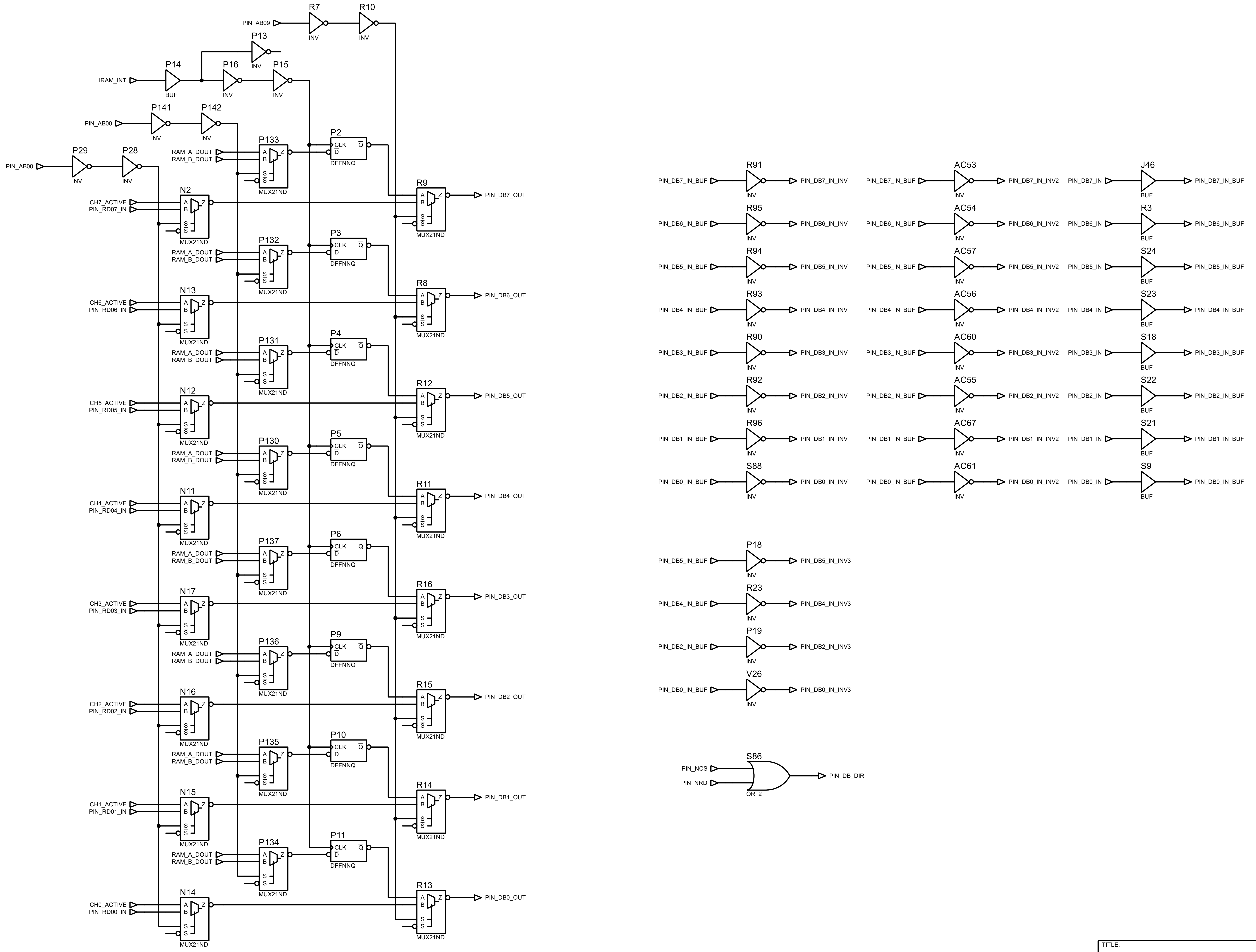


TITLE:
Konami 054539
TRIGGERS

DATE:
10/29/2025

PAGE:
3/53

BY: Sean Gonsalves
REV: @REV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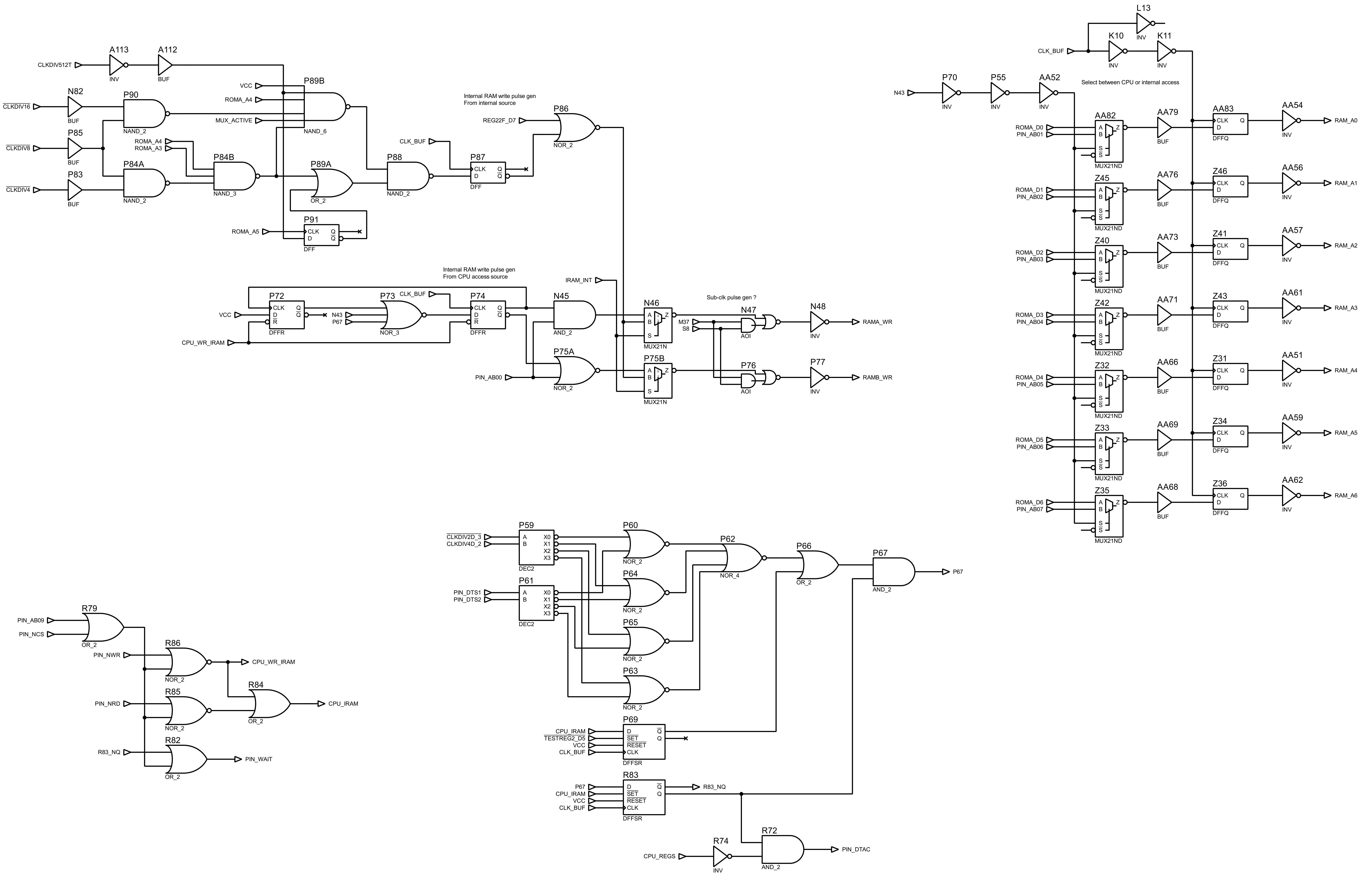
R7
INV
R10
INV
P13
INV
IRAM_INT
P14
BUF
P16
INV
P15
INV
PIN_AB00
P141
INV
P142
INV
PIN_AB00
P29
INV
P28
INV
RAM_A_DOUT
RAM_B_DOUT
P133
P132
P131
P130
P137
P136
P135
P134
MUX21ND
A
B
S
S̄
Z
P2
P3
P4
P5
P6
P9
P10
P11
CLK
D̄
Q̄
DFFNNQ
N2
N13
N12
N11
N17
N16
N15
N14
CH7_ACTIVE
PIN_RD07_IN
CH6_ACTIVE
PIN_RD06_IN
CH5_ACTIVE
PIN_RD05_IN
CH4_ACTIVE
PIN_RD04_IN
CH3_ACTIVE
PIN_RD03_IN
CH2_ACTIVE
PIN_RD02_IN
CH1_ACTIVE
PIN_RD01_IN
CH0_ACTIVE
PIN_RD00_IN
R9
PIN_DB7_OUT
R8
PIN_DB6_OUT
R12
PIN_DB5_OUT
R11
PIN_DB4_OUT
R16
PIN_DB3_OUT
R15
PIN_DB2_OUT
R14
PIN_DB1_OUT
R13
PIN_DB0_OUT
PIN_DB7_IN_BUF R91 INV PIN_DB7_IN_INV
PIN_DB6_IN_BUF R95 INV PIN_DB6_IN_INV
PIN_DB5_IN_BUF R94 INV PIN_DB5_IN_INV
PIN_DB4_IN_BUF R93 INV PIN_DB4_IN_INV
PIN_DB3_IN_BUF R90 INV PIN_DB3_IN_INV
PIN_DB2_IN_BUF R92 INV PIN_DB2_IN_INV
PIN_DB1_IN_BUF R96 INV PIN_DB1_IN_INV
PIN_DB0_IN_BUF S88 INV PIN_DB0_IN_INV
PIN_DB7_IN_BUF AC53 INV PIN_DB7_IN_INV2
PIN_DB6_IN_BUF AC54 INV PIN_DB6_IN_INV2
PIN_DB5_IN_BUF AC57 INV PIN_DB5_IN_INV2
PIN_DB4_IN_BUF AC56 INV PIN_DB4_IN_INV2
PIN_DB3_IN_BUF AC60 INV PIN_DB3_IN_INV2
PIN_DB2_IN_BUF AC55 INV PIN_DB2_IN_INV2
PIN_DB1_IN_BUF AC67 INV PIN_DB1_IN_INV2
PIN_DB0_IN_BUF AC61 INV PIN_DB0_IN_INV2
PIN_DB7_IN J46 BUF PIN_DB7_IN_BUF
PIN_DB6_IN R3 BUF PIN_DB6_IN_BUF
PIN_DB5_IN S24 BUF PIN_DB5_IN_BUF
PIN_DB4_IN S23 BUF PIN_DB4_IN_BUF
PIN_DB3_IN S18 BUF PIN_DB3_IN_BUF
PIN_DB2_IN S22 BUF PIN_DB2_IN_BUF
PIN_DB1_IN S21 BUF PIN_DB1_IN_BUF
PIN_DB0_IN S9 BUF PIN_DB0_IN_BUF
PIN_DB5_IN_BUF P18 INV PIN_DB5_IN_INV3
PIN_DB4_IN_BUF R23 INV PIN_DB4_IN_INV3
PIN_DB2_IN_BUF P19 INV PIN_DB2_IN_INV3
PIN_DB0_IN_BUF V26 INV PIN_DB0_IN_INV3
PIN_NCS
PIN_NRD
S86
OR_2
PIN_DB_DIR

TITLE:
Konami 054539
CPU DATA
BY: Sean Gonsalves
REV: @REV
DATE:
10/29/2025
PAGE:
4/53



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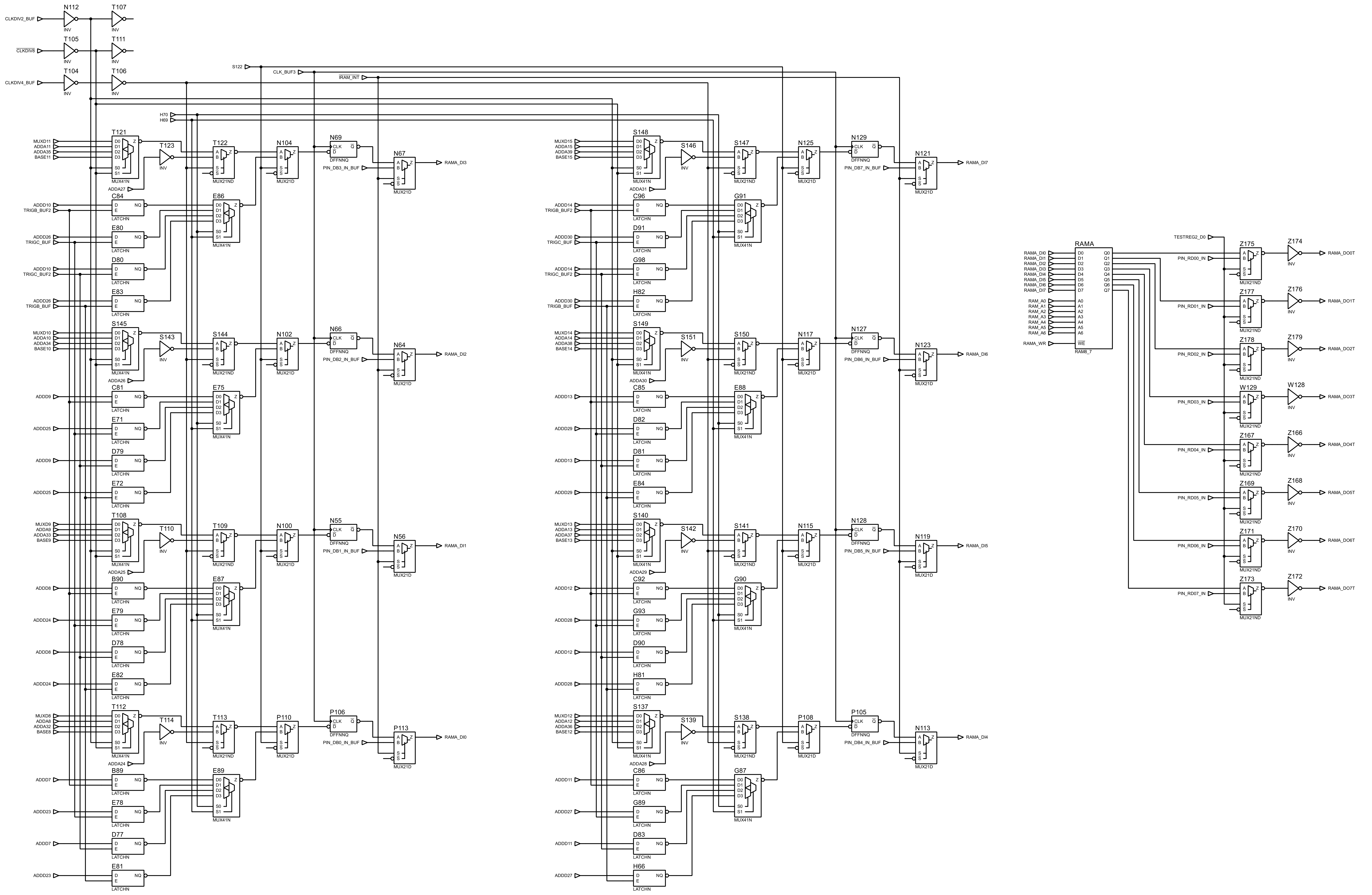
Konami 054539
INTERNAL RAM CONTROL

BY: Sean Gonsalves
REV: @REV

DATE:
10/29/2025

PAGE:
5/53



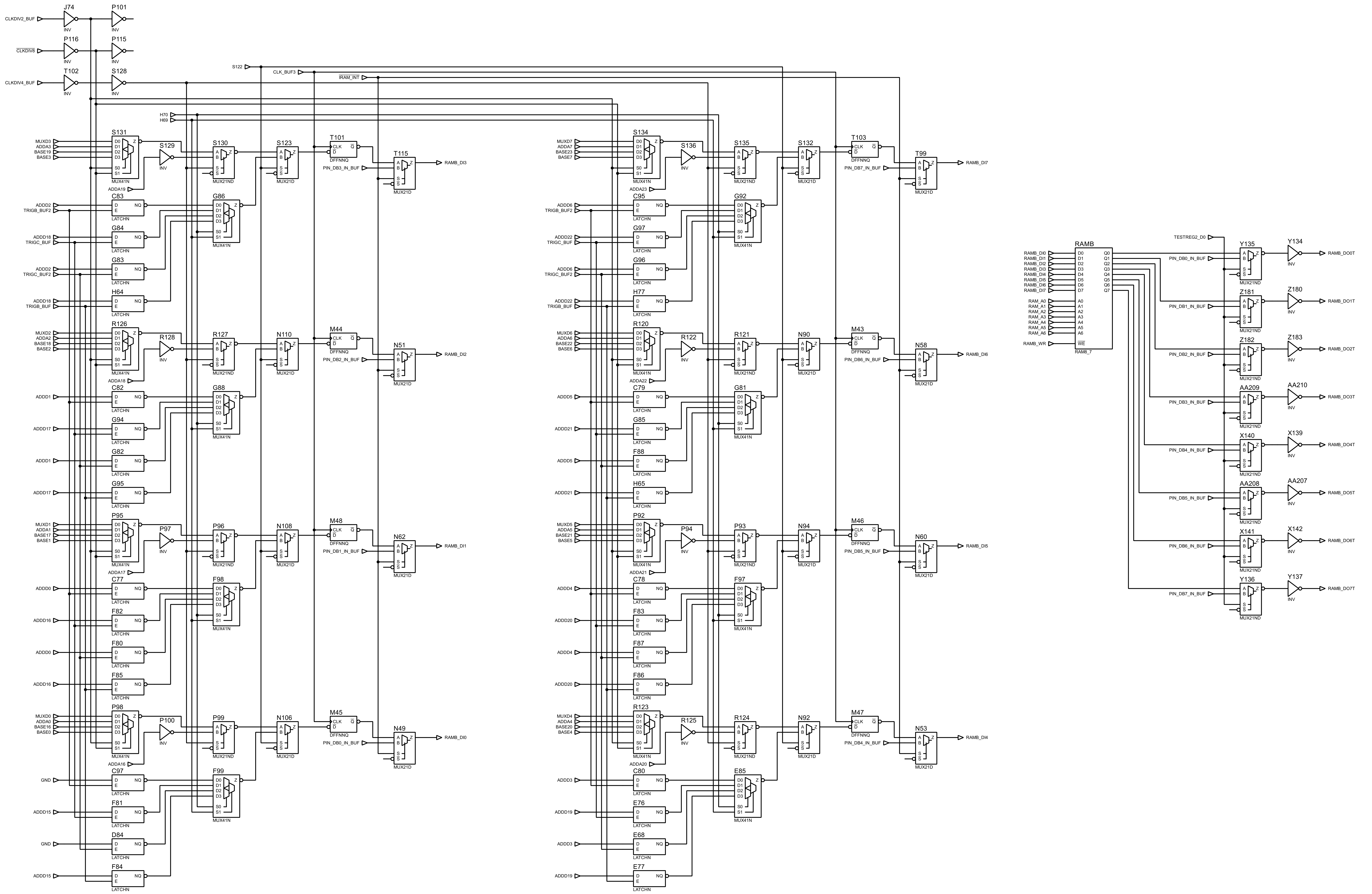
TITLE:
Konami 054539
RAM A DATA

BY: Sean Gonsalves

REV: @REV

DATE:
10/29/2025

PAGE:
6/53



TITLE:

Konami 054539
RAM B DATA

BY: Sean Gonsalves

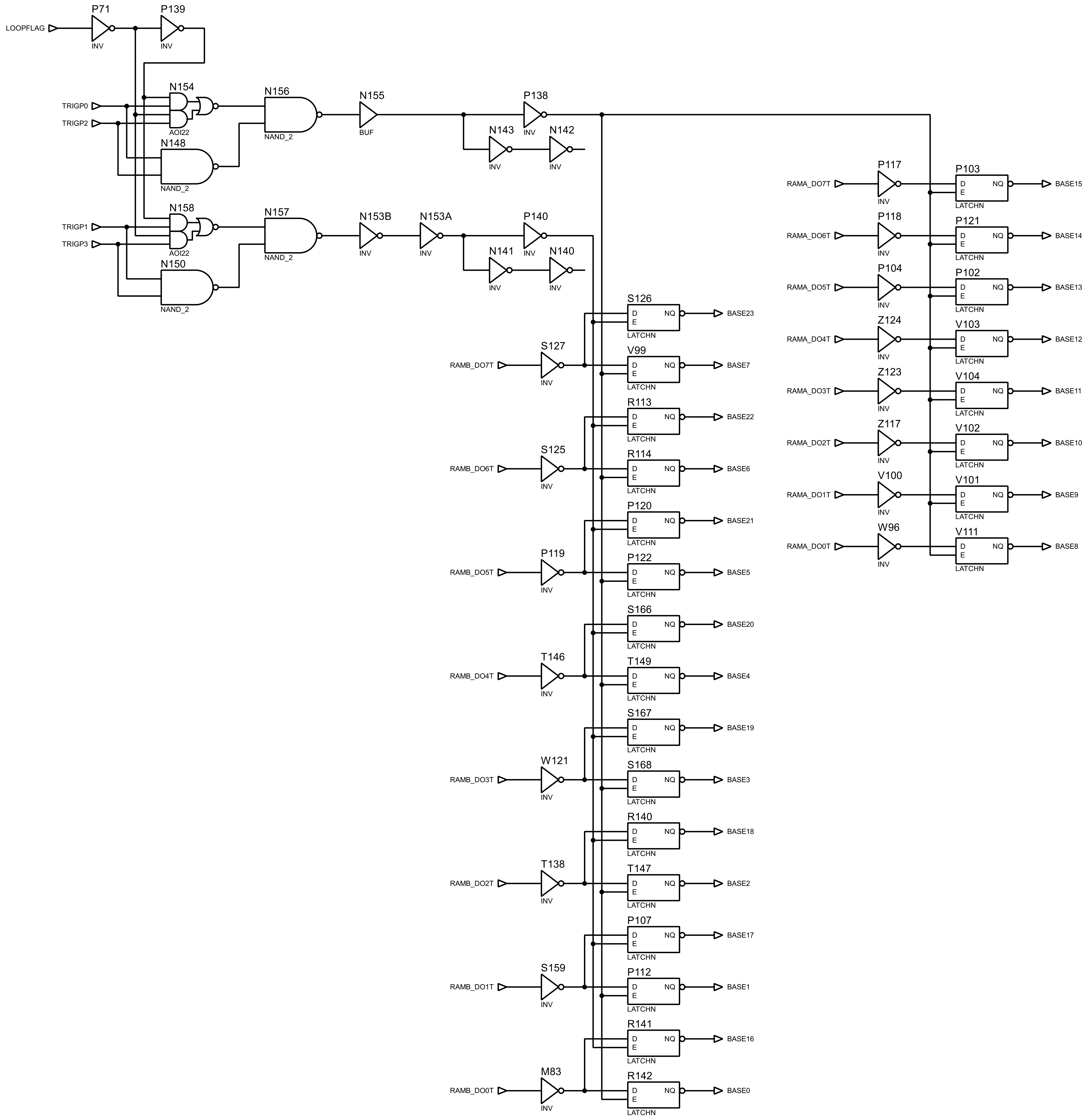
REV: @REV

DATE:

10/29/2025

PAGE:

7/53

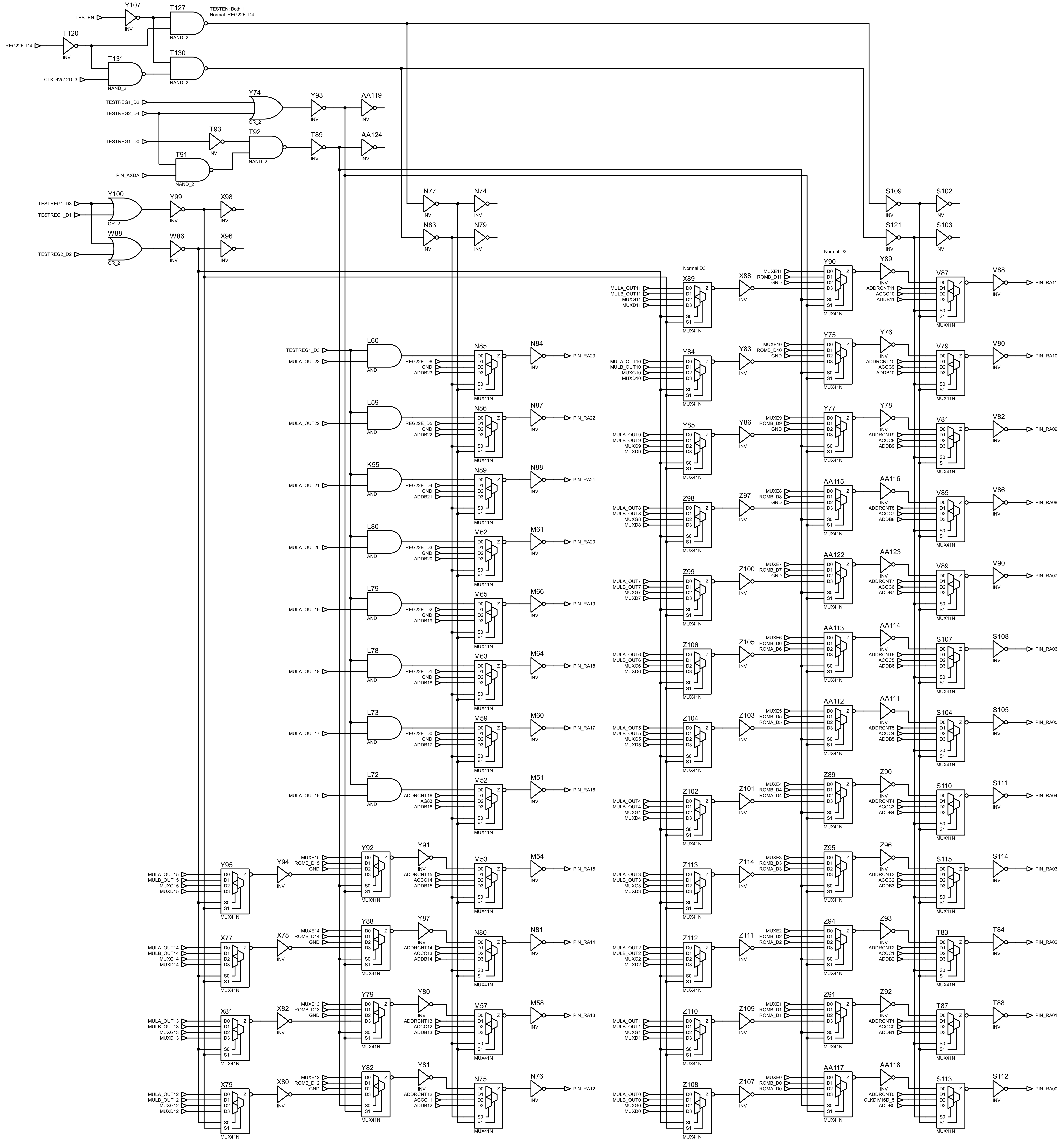


TITLE:
Konami 054539
CH START/LOOP ADDR

DATE:
10/29/2025

PAGE:
8/53

BY: Sean Gonsalves
REV: @REV



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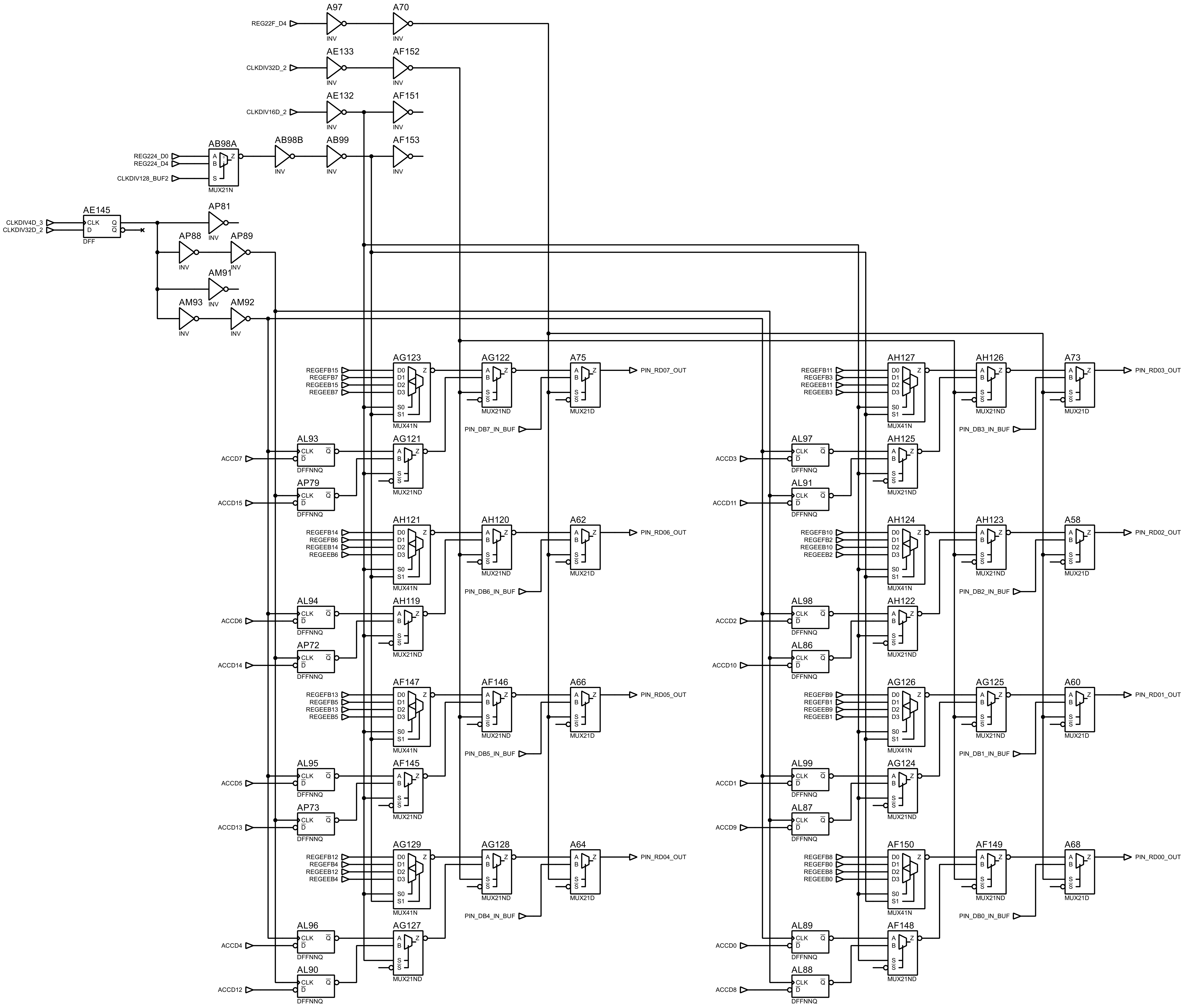
Konami 054539 EXT MEM ADDRESS	10/29/2025
	PAGE: 9/53
BY: Sean Gonsalves REV: @REV	



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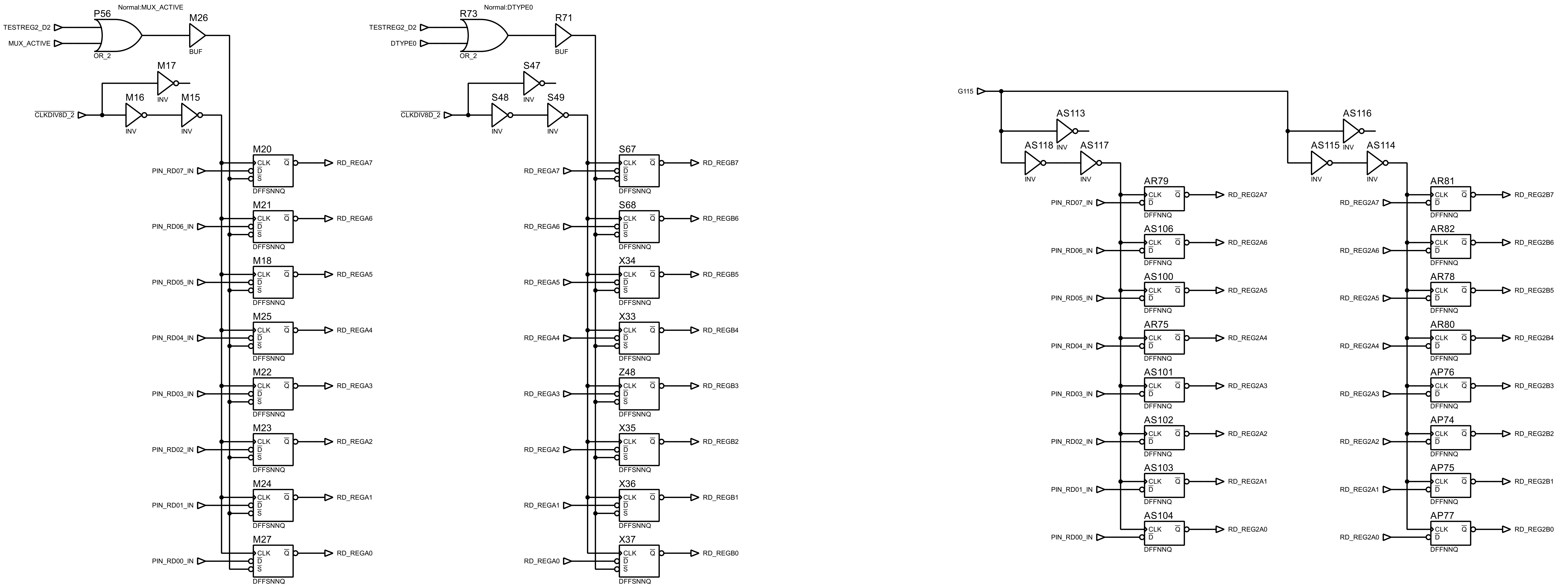
Konami 054539
EXT MEM DATA

BY: Sean Gonsalves

REV: @REV

DATE:
10/29/2025

PAGE:
10/53



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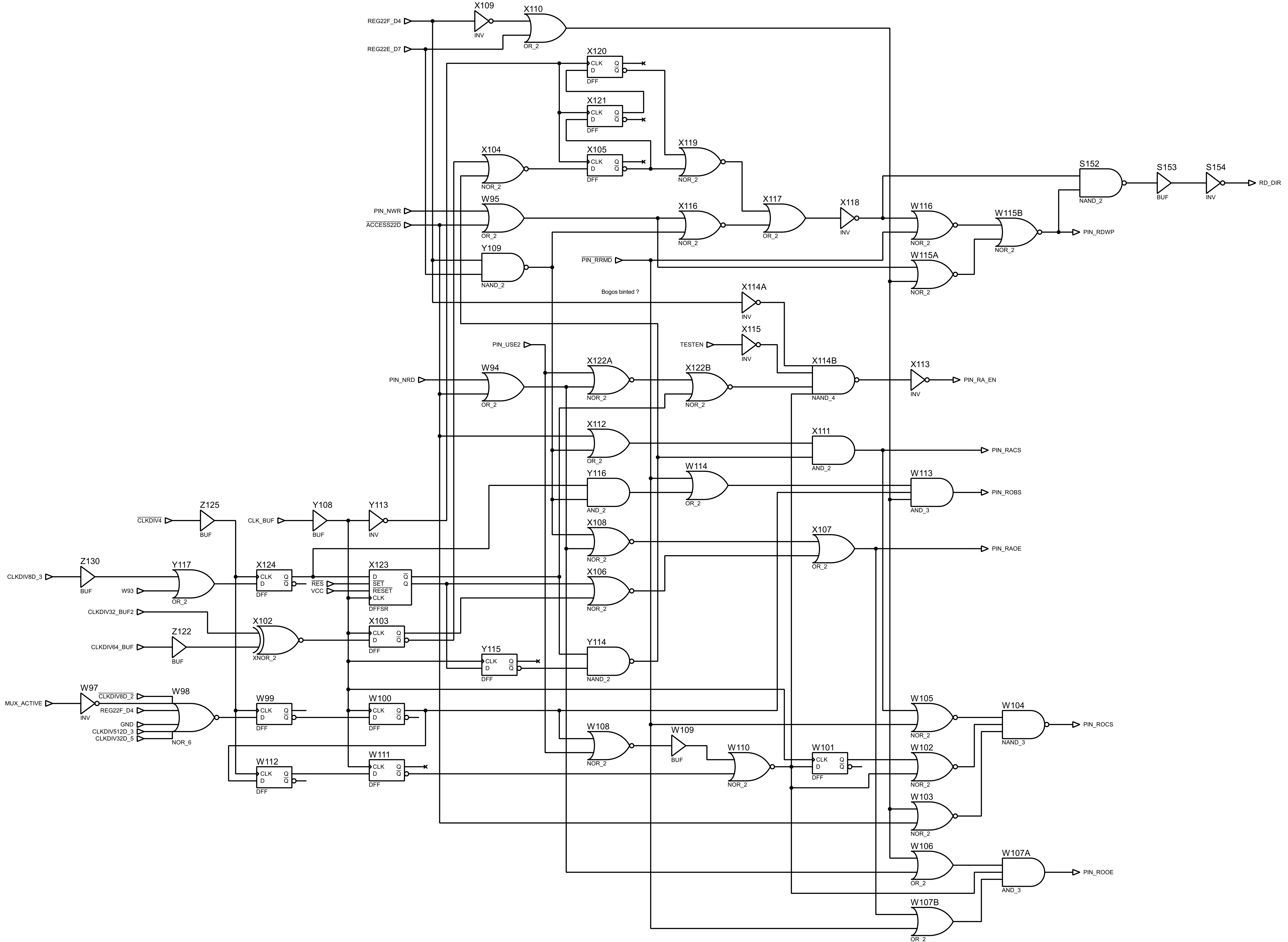
EXT DATA REGISTERS
DATE:
10/29/2025
PAGE:
11/53
BY: Sean Gonsalves
REV: @REV

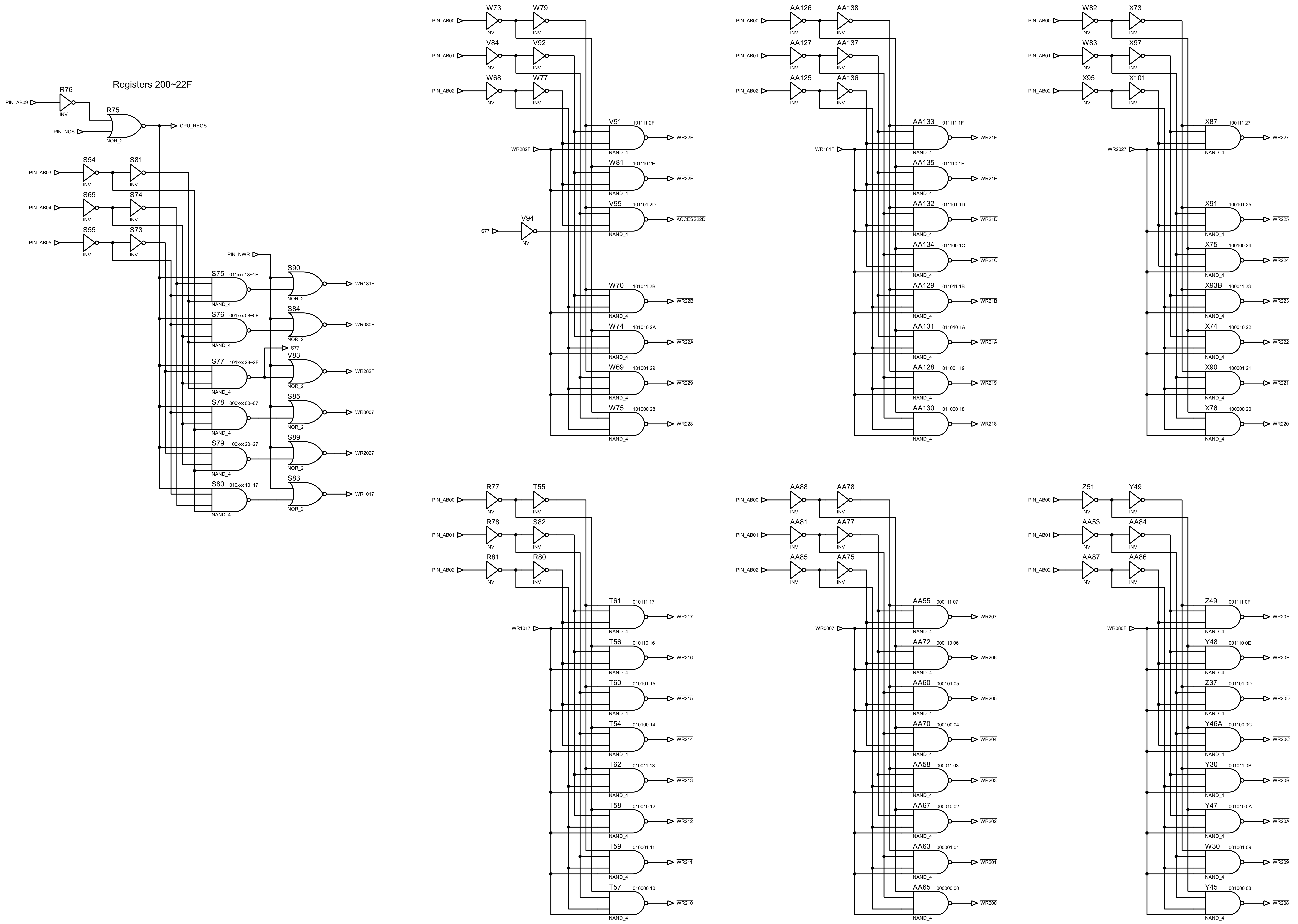


TITLE:
Konami 054539
EXT MEM CONTROL

DATE:
10/29/2025

PAGE:
12/53

BY: Sean Gonsalves
REV: @REV



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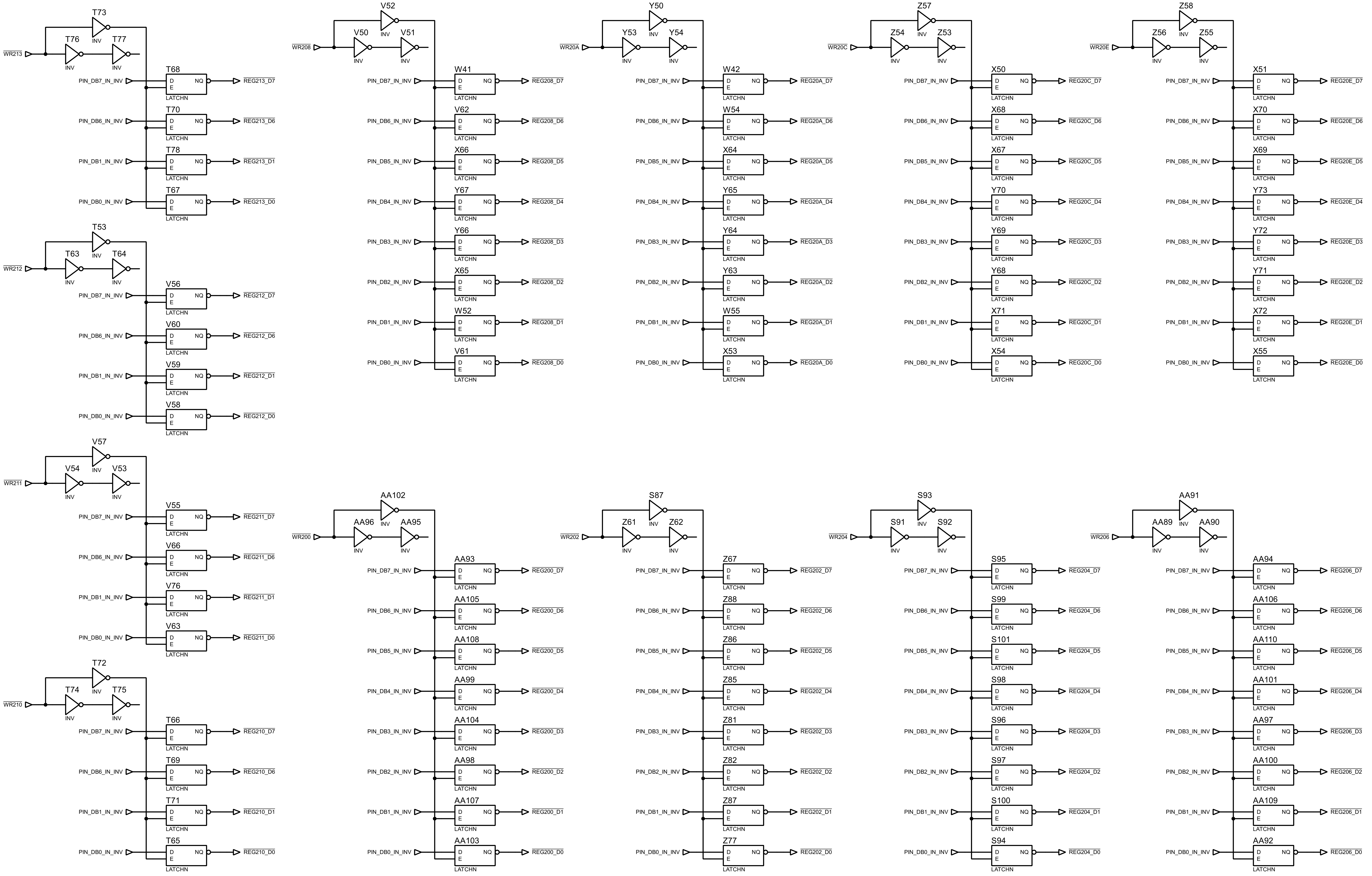
Konami 054539
REG DECODE
BY: Sean Gonsalves
REV: @REV
DATE:
10/29/2025
PAGE:
13/53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T73 INV
T76 INV
T77 INV
PIN_DB7_IN_INV T68 D E NQ LATCHN REG213_D7
PIN_DB6_IN_INV T70 D E NQ LATCHN REG213_D6
PIN_DB1_IN_INV T78 D E NQ LATCHN REG213_D1
PIN_DB0_IN_INV T67 D E NQ LATCHN REG213_D0
WR212
T53 INV
T63 INV
T64 INV
PIN_DB7_IN_INV V56 D E NQ LATCHN REG212_D7
PIN_DB6_IN_INV V60 D E NQ LATCHN REG212_D6
PIN_DB1_IN_INV V59 D E NQ LATCHN REG212_D1
PIN_DB0_IN_INV V58 D E NQ LATCHN REG212_D0
WR211
V57 INV
V54 INV
V53 INV
PIN_DB7_IN_INV V55 D E NQ LATCHN REG211_D7
PIN_DB6_IN_INV V66 D E NQ LATCHN REG211_D6
PIN_DB1_IN_INV V76 D E NQ LATCHN REG211_D1
PIN_DB0_IN_INV V63 D E NQ LATCHN REG211_D0
WR210
T72 INV
T74 INV
T75 INV
PIN_DB7_IN_INV T66 D E NQ LATCHN REG210_D7
PIN_DB6_IN_INV T69 D E NQ LATCHN REG210_D6
PIN_DB1_IN_INV T71 D E NQ LATCHN REG210_D1
PIN_DB0_IN_INV T65 D E NQ LATCHN REG210_D0
WR208
V52 INV
V50 INV
V51 INV
PIN_DB7_IN_INV W41 D E NQ LATCHN REG208_D7
PIN_DB6_IN_INV V62 D E NQ LATCHN REG208_D6
PIN_DB5_IN_INV X66 D E NQ LATCHN REG208_D5
PIN_DB4_IN_INV Y67 D E NQ LATCHN REG208_D4
PIN_DB3_IN_INV Y66 D E NQ LATCHN REG208_D3
PIN_DB2_IN_INV X65 D E NQ LATCHN REG208_D2
PIN_DB1_IN_INV W52 D E NQ LATCHN REG208_D1
PIN_DB0_IN_INV V61 D E NQ LATCHN REG208_D0
WR20A
Y50 INV
Y53 INV
Y54 INV
PIN_DB7_IN_INV W42 D E NQ LATCHN REG20A_D7
PIN_DB6_IN_INV W54 D E NQ LATCHN REG20A_D6
PIN_DB5_IN_INV X64 D E NQ LATCHN REG20A_D5
PIN_DB4_IN_INV Y65 D E NQ LATCHN REG20A_D4
PIN_DB3_IN_INV Y64 D E NQ LATCHN REG20A_D3
PIN_DB2_IN_INV Y63 D E NQ LATCHN REG20A_D2
PIN_DB1_IN_INV W55 D E NQ LATCHN REG20A_D1
PIN_DB0_IN_INV X53 D E NQ LATCHN REG20A_D0
WR20C
Z57 INV
Z54 INV
Z53 INV
PIN_DB7_IN_INV X50 D E NQ LATCHN REG20C_D7
PIN_DB6_IN_INV X68 D E NQ LATCHN REG20C_D6
PIN_DB5_IN_INV X67 D E NQ LATCHN REG20C_D5
PIN_DB4_IN_INV Y70 D E NQ LATCHN REG20C_D4
PIN_DB3_IN_INV Y69 D E NQ LATCHN REG20C_D3
PIN_DB2_IN_INV Y68 D E NQ LATCHN REG20C_D2
PIN_DB1_IN_INV X71 D E NQ LATCHN REG20C_D1
PIN_DB0_IN_INV X54 D E NQ LATCHN REG20C_D0
WR20E
Z58 INV
Z56 INV
Z55 INV
PIN_DB7_IN_INV X51 D E NQ LATCHN REG20E_D7
PIN_DB6_IN_INV X70 D E NQ LATCHN REG20E_D6
PIN_DB5_IN_INV X69 D E NQ LATCHN REG20E_D5
PIN_DB4_IN_INV Y73 D E NQ LATCHN REG20E_D4
PIN_DB3_IN_INV Y72 D E NQ LATCHN REG20E_D3
PIN_DB2_IN_INV Y71 D E NQ LATCHN REG20E_D2
PIN_DB1_IN_INV X72 D E NQ LATCHN REG20E_D1
PIN_DB0_IN_INV X55 D E NQ LATCHN REG20E_D0
WR200
AA102 INV
AA96 INV
AA95 INV
PIN_DB7_IN_INV AA93 D E NQ LATCHN REG200_D7
PIN_DB6_IN_INV AA105 D E NQ LATCHN REG200_D6
PIN_DB5_IN_INV AA108 D E NQ LATCHN REG200_D5
PIN_DB4_IN_INV AA99 D E NQ LATCHN REG200_D4
PIN_DB3_IN_INV AA104 D E NQ LATCHN REG200_D3
PIN_DB2_IN_INV AA98 D E NQ LATCHN REG200_D2
PIN_DB1_IN_INV AA107 D E NQ LATCHN REG200_D1
PIN_DB0_IN_INV AA103 D E NQ LATCHN REG200_D0
WR202
S87 INV
Z61 INV
Z62 INV
PIN_DB7_IN_INV Z67 D E NQ LATCHN REG202_D7
PIN_DB6_IN_INV Z88 D E NQ LATCHN REG202_D6
PIN_DB5_IN_INV Z86 D E NQ LATCHN REG202_D5
PIN_DB4_IN_INV Z85 D E NQ LATCHN REG202_D4
PIN_DB3_IN_INV Z81 D E NQ LATCHN REG202_D3
PIN_DB2_IN_INV Z82 D E NQ LATCHN REG202_D2
PIN_DB1_IN_INV Z87 D E NQ LATCHN REG202_D1
PIN_DB0_IN_INV Z77 D E NQ LATCHN REG202_D0
WR204
S93 INV
S91 INV
S92 INV
PIN_DB7_IN_INV S95 D E NQ LATCHN REG204_D7
PIN_DB6_IN_INV S99 D E NQ LATCHN REG204_D6
PIN_DB5_IN_INV S101 D E NQ LATCHN REG204_D5
PIN_DB4_IN_INV S98 D E NQ LATCHN REG204_D4
PIN_DB3_IN_INV S96 D E NQ LATCHN REG204_D3
PIN_DB2_IN_INV S97 D E NQ LATCHN REG204_D2
PIN_DB1_IN_INV S100 D E NQ LATCHN REG204_D1
PIN_DB0_IN_INV S94 D E NQ LATCHN REG204_D0
WR206
AA91 INV
AA89 INV
AA90 INV
PIN_DB7_IN_INV AA94 D E NQ LATCHN REG206_D7
PIN_DB6_IN_INV AA106 D E NQ LATCHN REG206_D6
PIN_DB5_IN_INV AA110 D E NQ LATCHN REG206_D5
PIN_DB4_IN_INV AA101 D E NQ LATCHN REG206_D4
PIN_DB3_IN_INV AA97 D E NQ LATCHN REG206_D3
PIN_DB2_IN_INV AA100 D E NQ LATCHN REG206_D2
PIN_DB1_IN_INV AA109 D E NQ LATCHN REG206_D1
PIN_DB0_IN_INV AA92 D E NQ LATCHN REG206_D0

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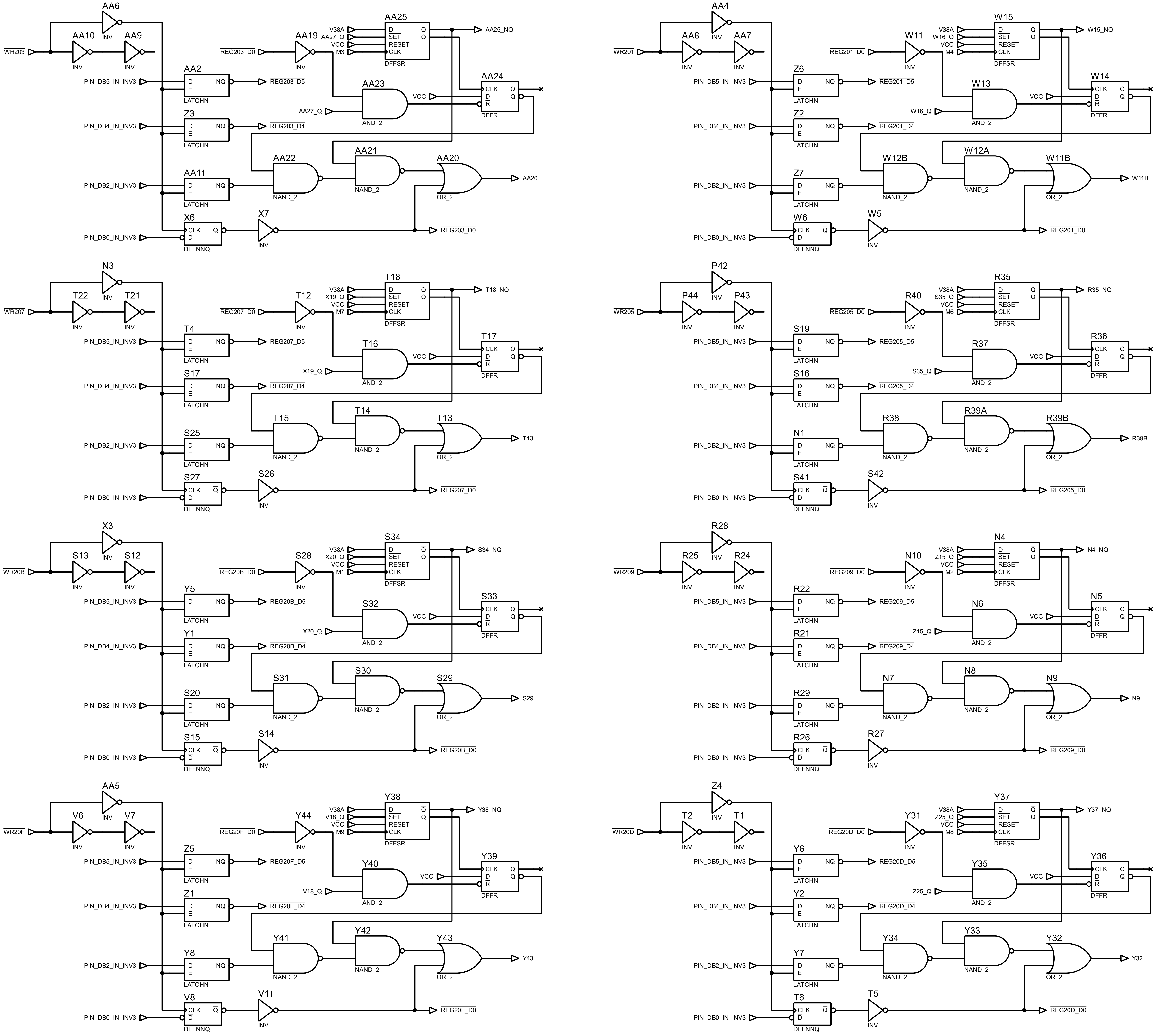
Konami 054539 REGS 200~20F EVEN	10/29/2025
BY: Sean Gonsalves REV: @REV	PAGE: 14/53



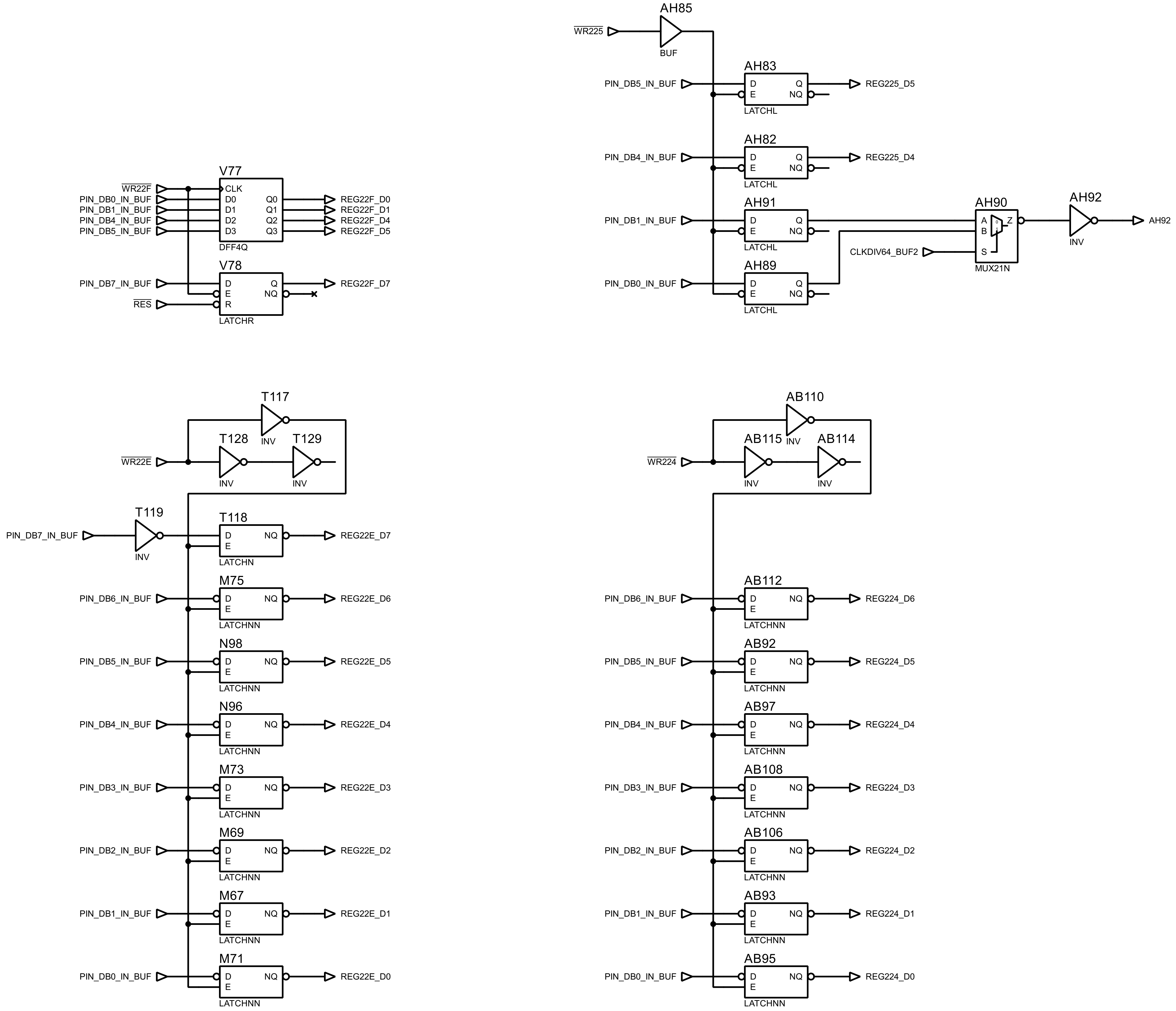
TITLE:
Konami 054539
REGS 200~20F ODD

BY: Sean Gonsalves

REV: @REV

DATE:
10/29/2025

PAGE:
15/53



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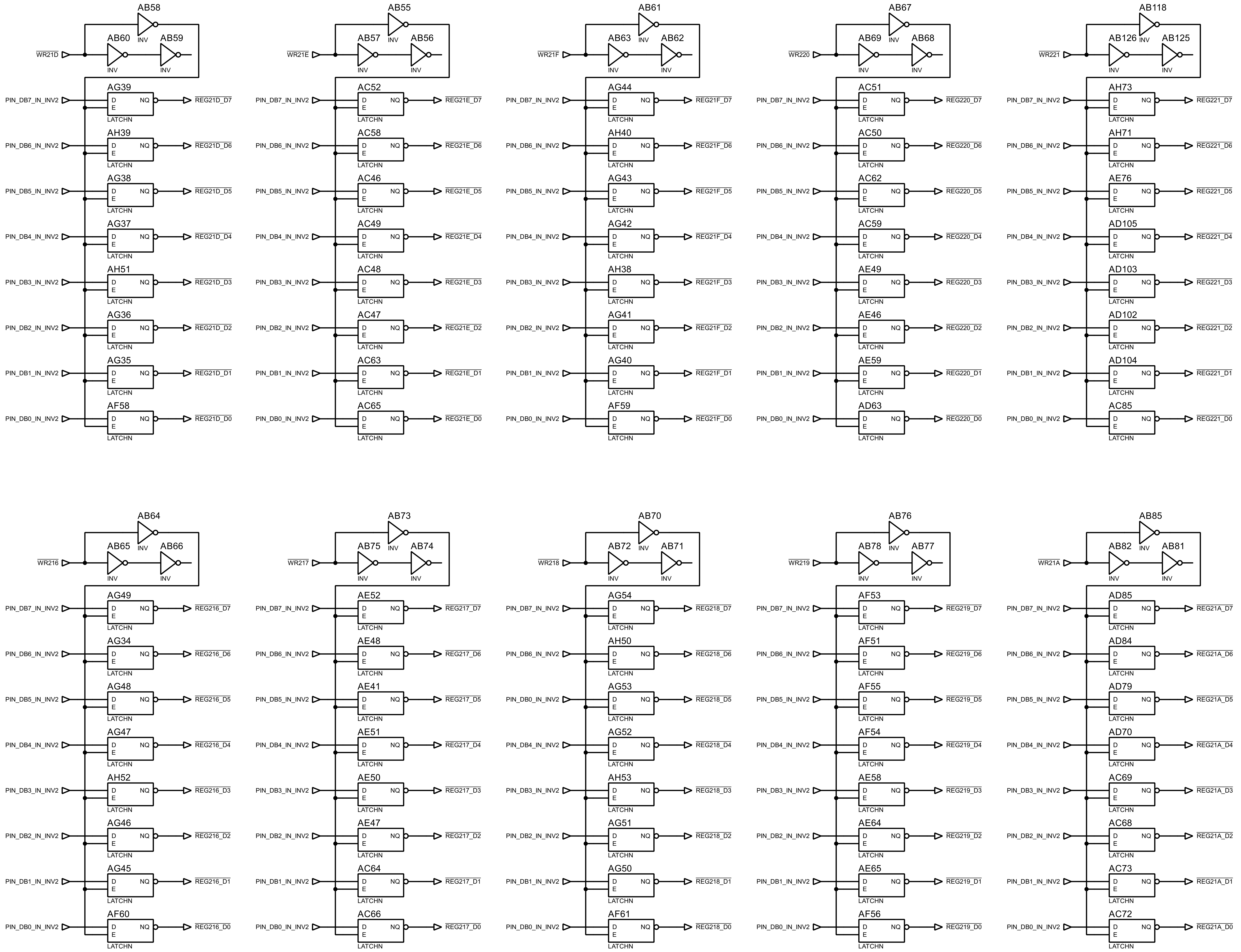
Konami 054539 REGISTERS 1	10/29/2025
BY: Sean Gonsalves REV: @REV	PAGE: 16/53



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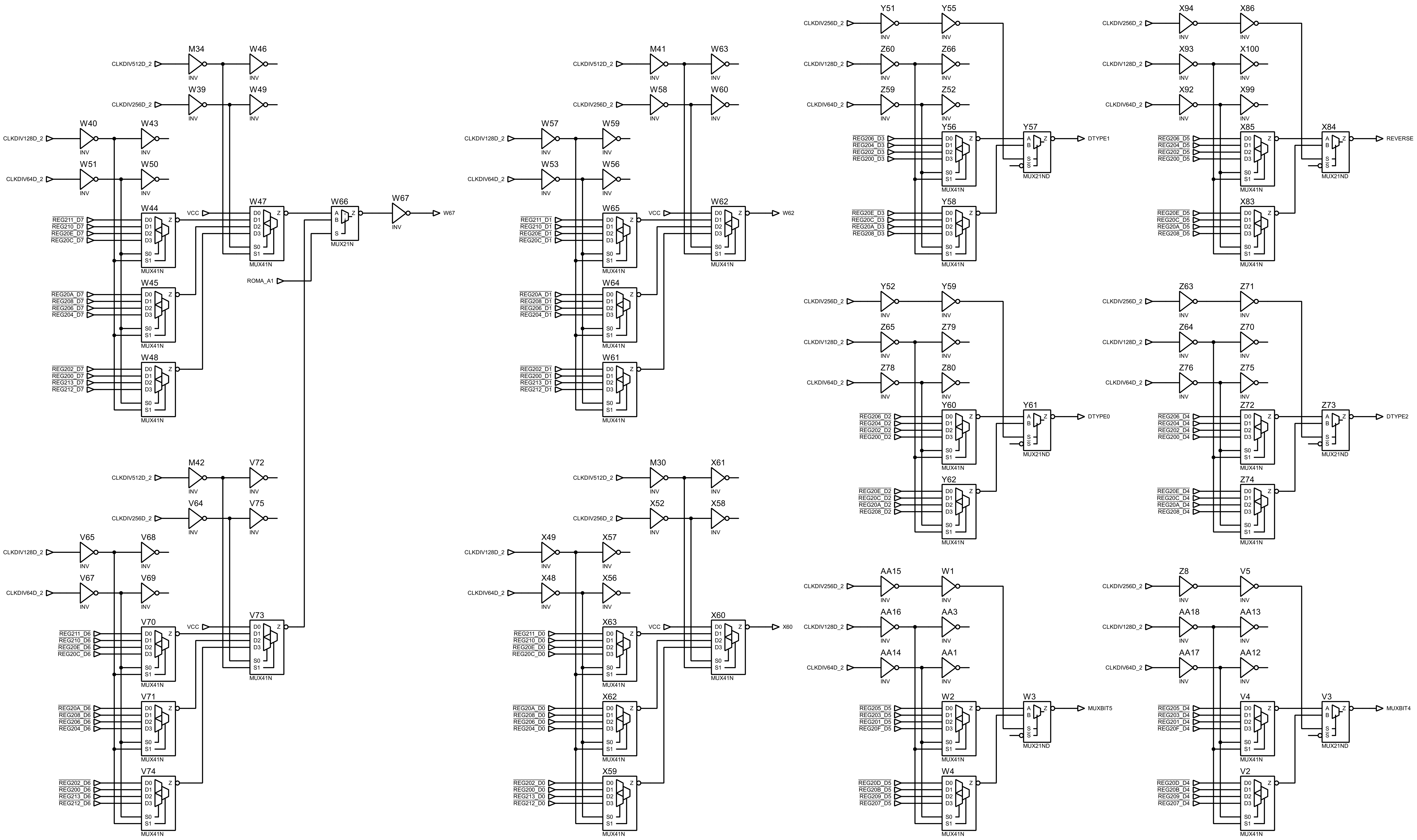
Konami 054539
REGISTERS 2
BY: Sean Gonsalves
REV: @REV

DATE:
10/29/2025
PAGE:
17/53



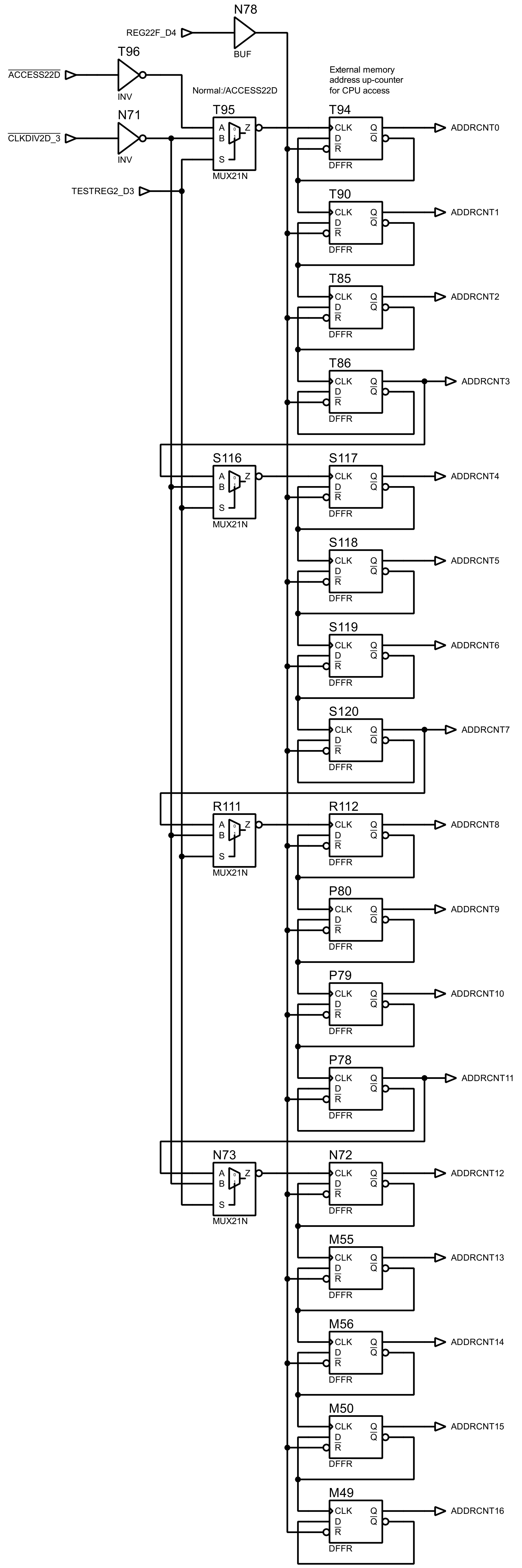
TITLE:
Konami 054539
REGISTER MUX

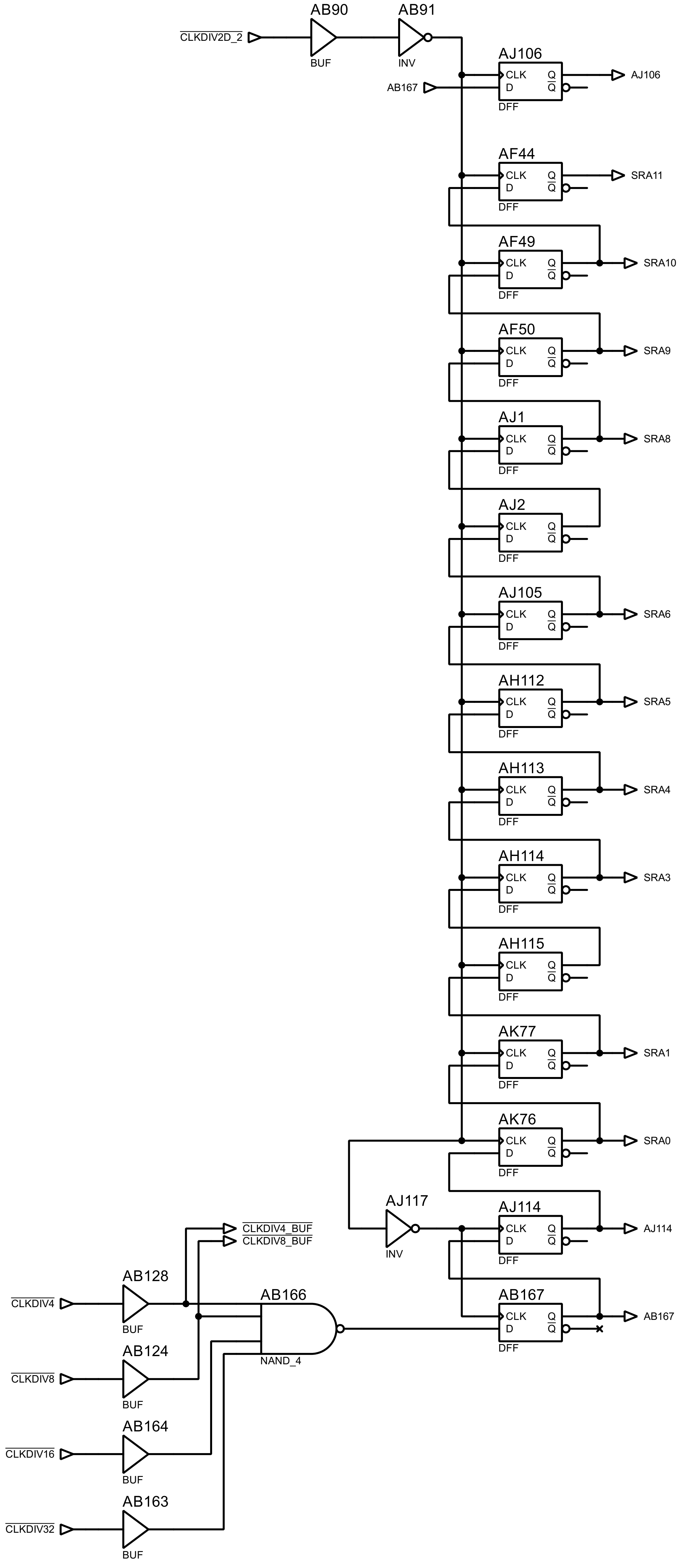
DATE:
10/29/2025

PAGE:
18/53

BY: Sean Gonsalves

REV: @REV





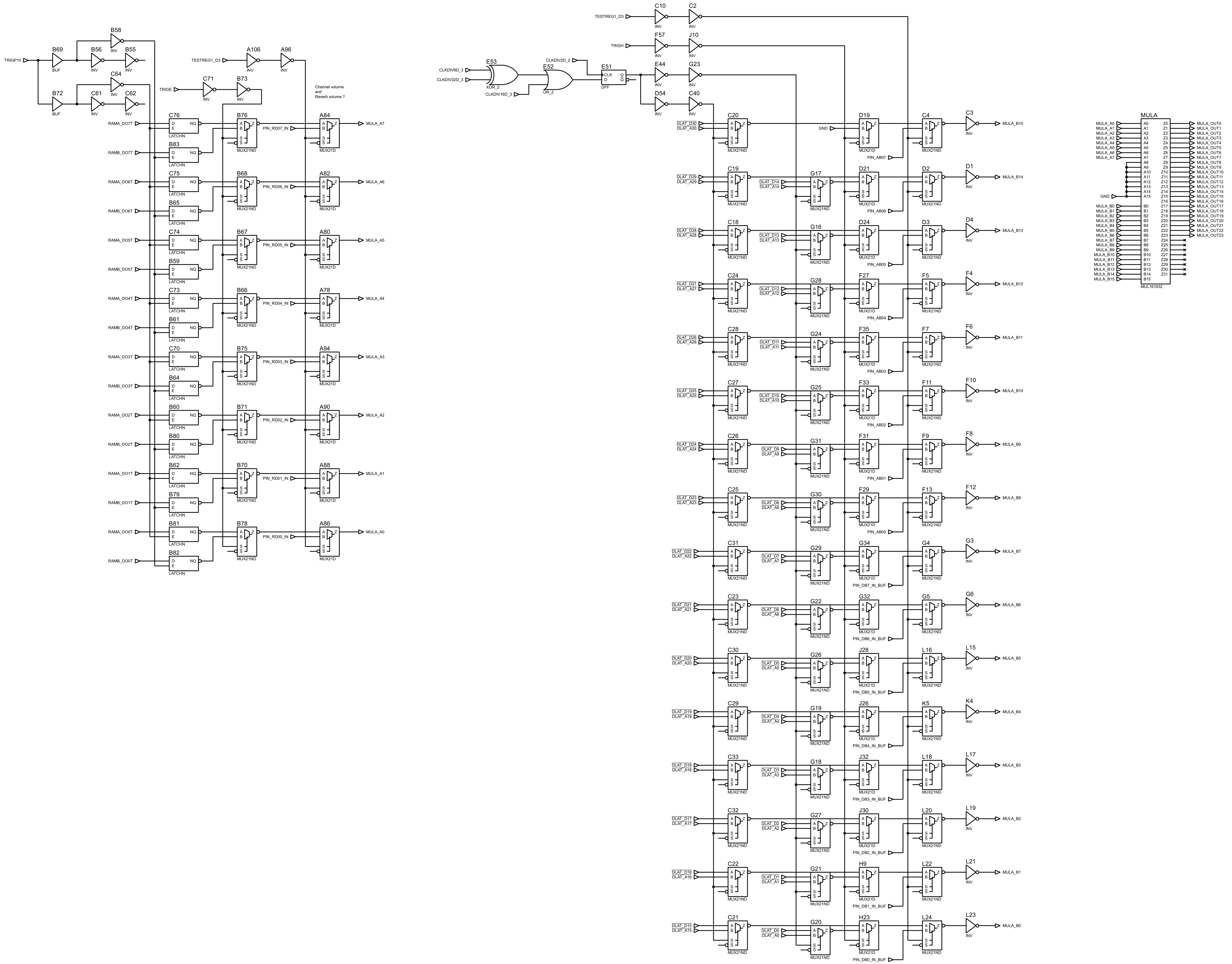
TITLE:

Konami 054539
TEST COUNTER and SEQ

DATE:
10/29/2025

PAGE:
19/53

BY: Sean Gonsalves
REV: @REV

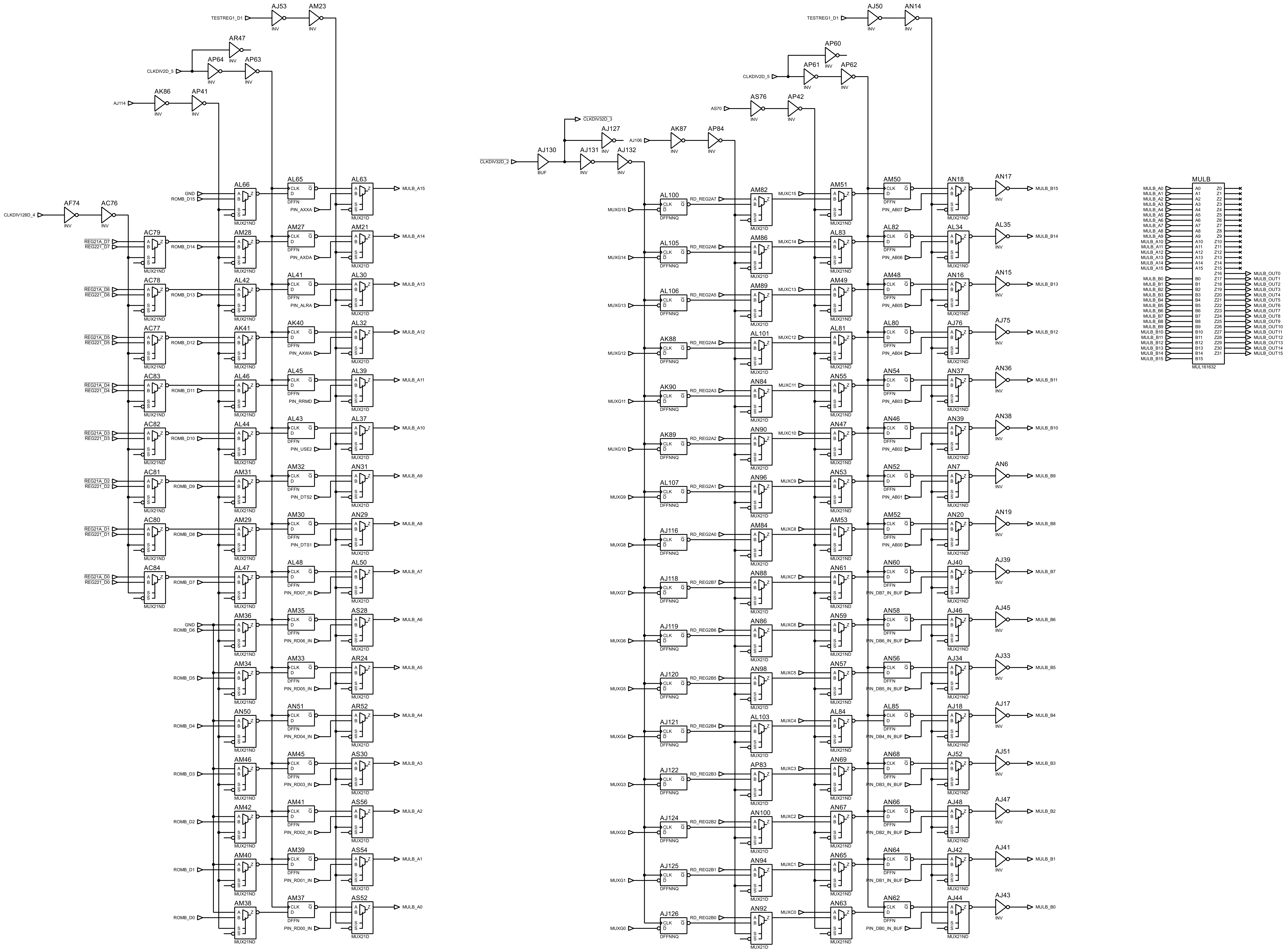


TITLE:
Konami 054539
MUL A

DATE:
10/29/2025

PAGE:
20/53

BY: Sean Gonsalves REV: @REV



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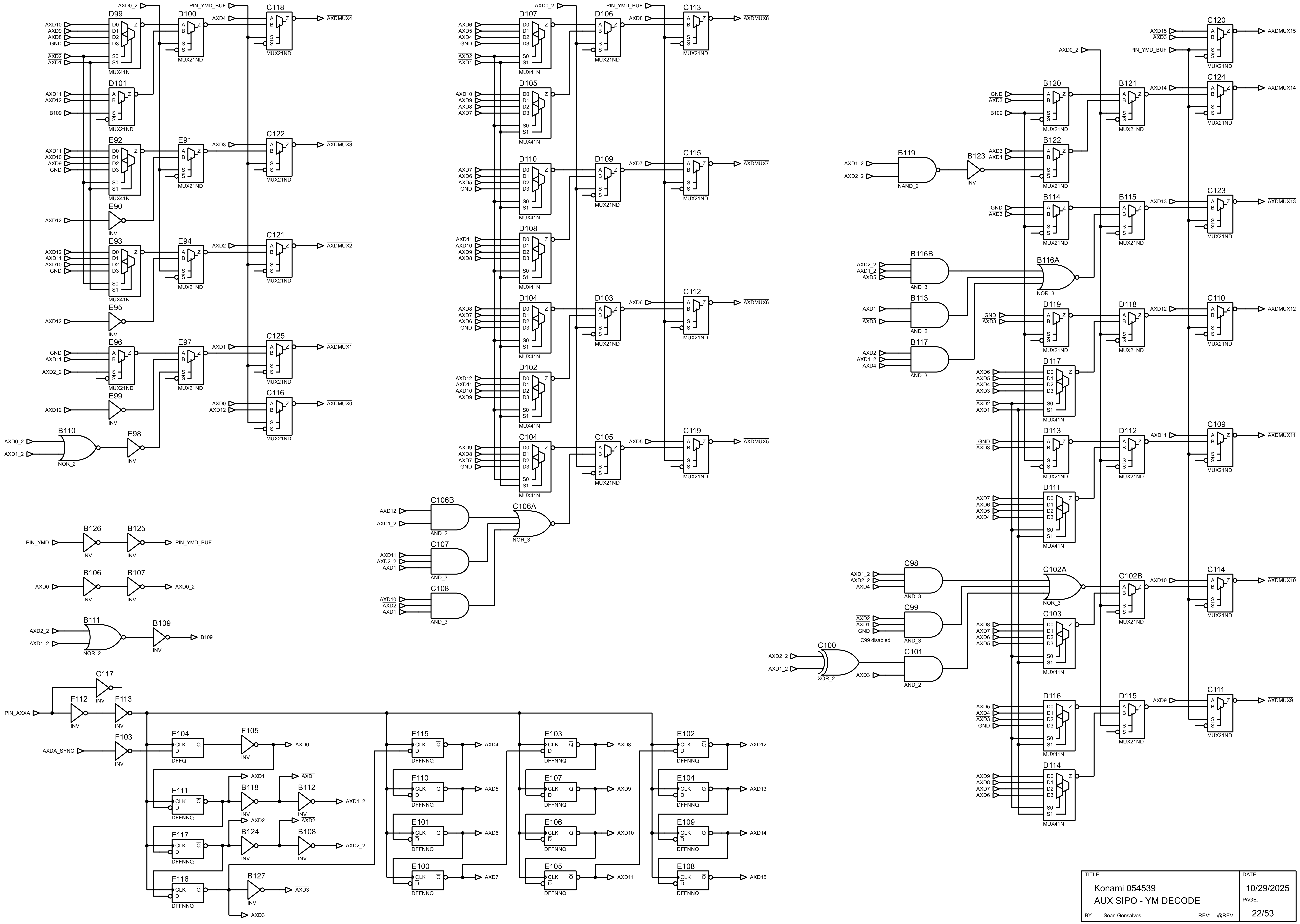
MUL B

DATE:
10/29/2025

PAGE:
21/53

BY: Sean Gonsalves
REV: @REV



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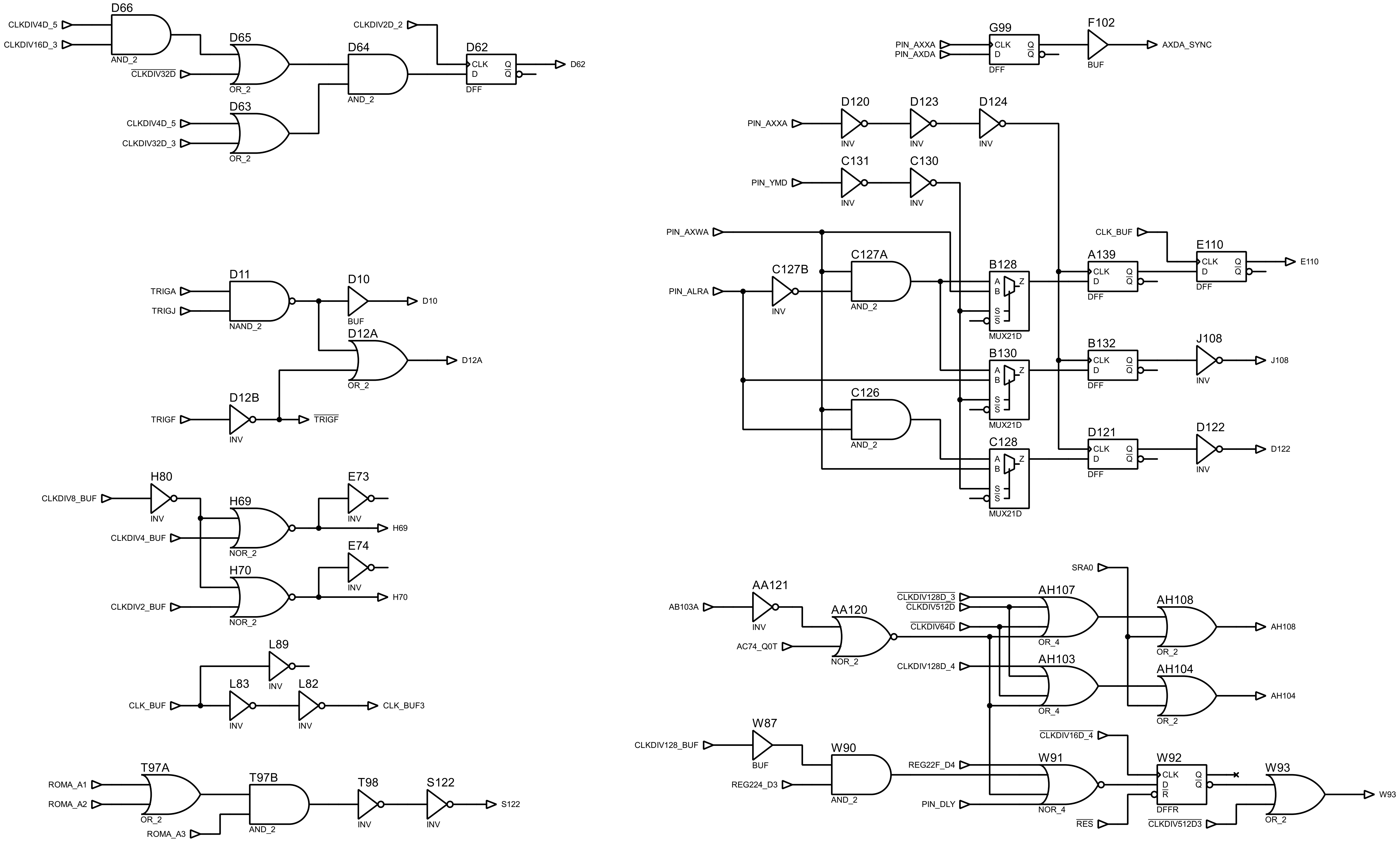
DATE:
10/29/2025

PAGE:
22/53

BY: Sean Gonsalves

REV: @REV



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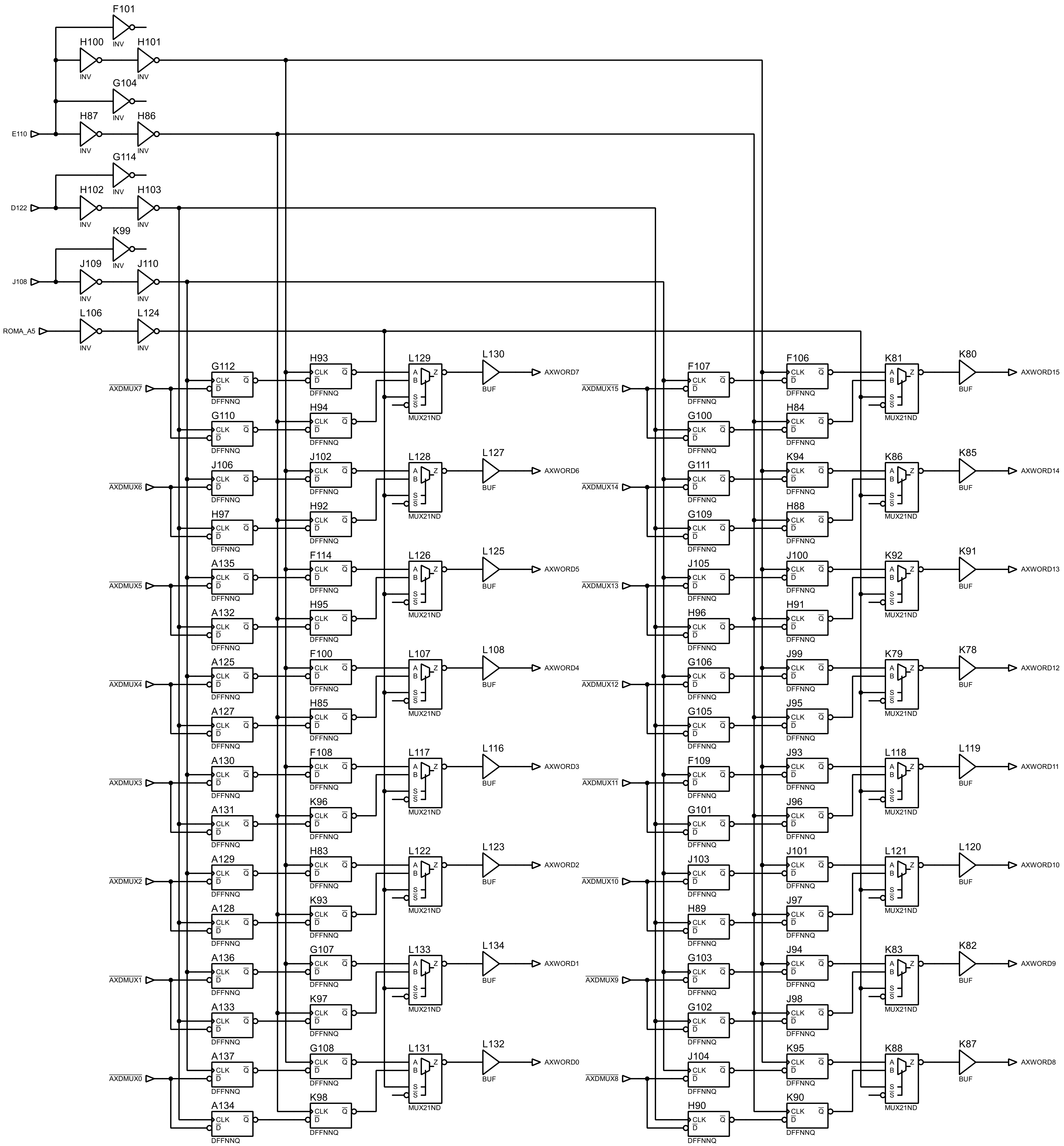
AUX DATA CONTROL

DATE:
10/29/2025

PAGE:
23/53

BY: Sean Gonsalves

REV: @REV



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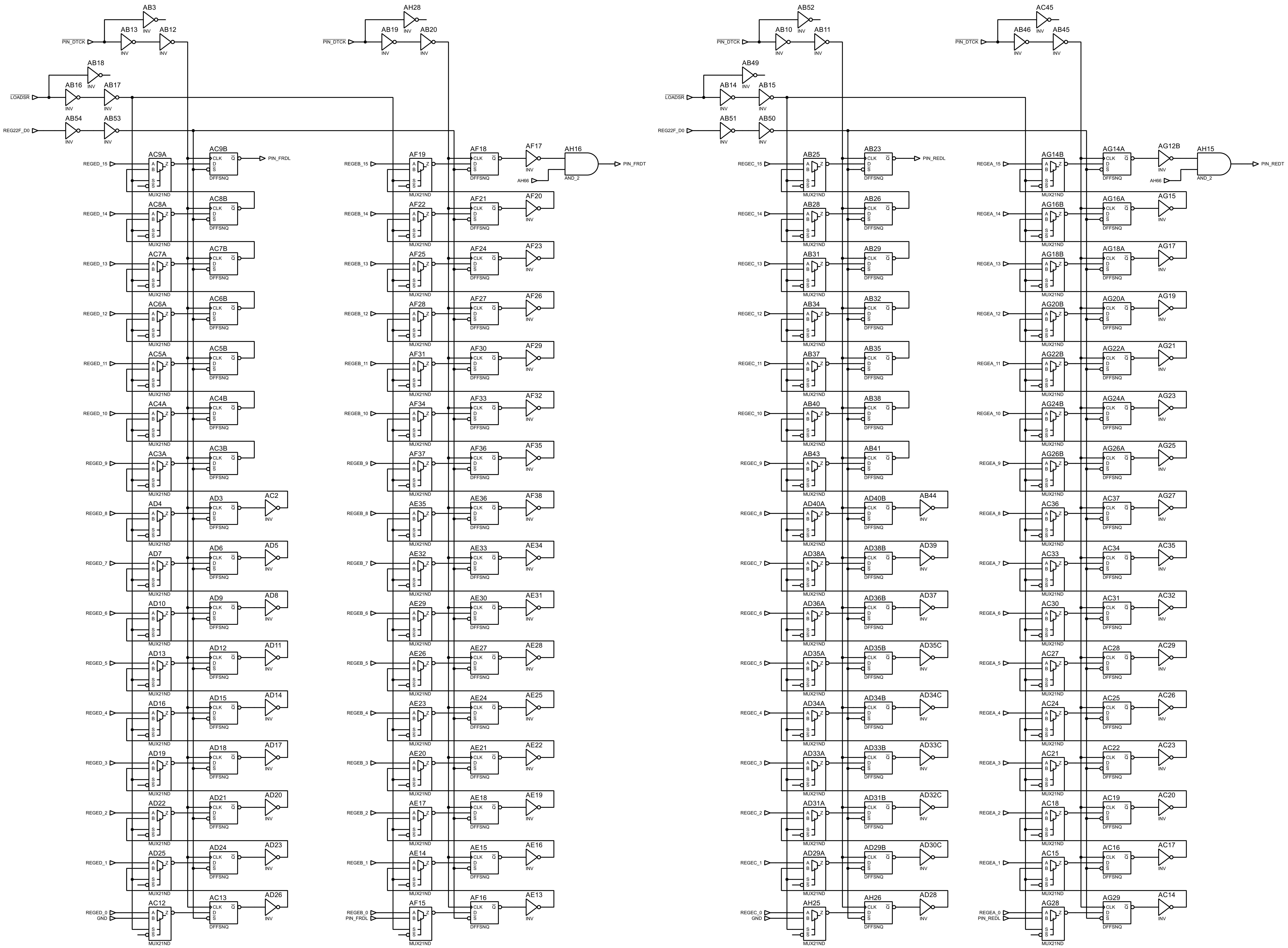
DATE:
10/29/2025
PAGE:
24/53
BY: Sean Gonsalves
REV: @REV



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Konami 054539
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BY: Sean Gonsalves
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DATE:
10/29/2025

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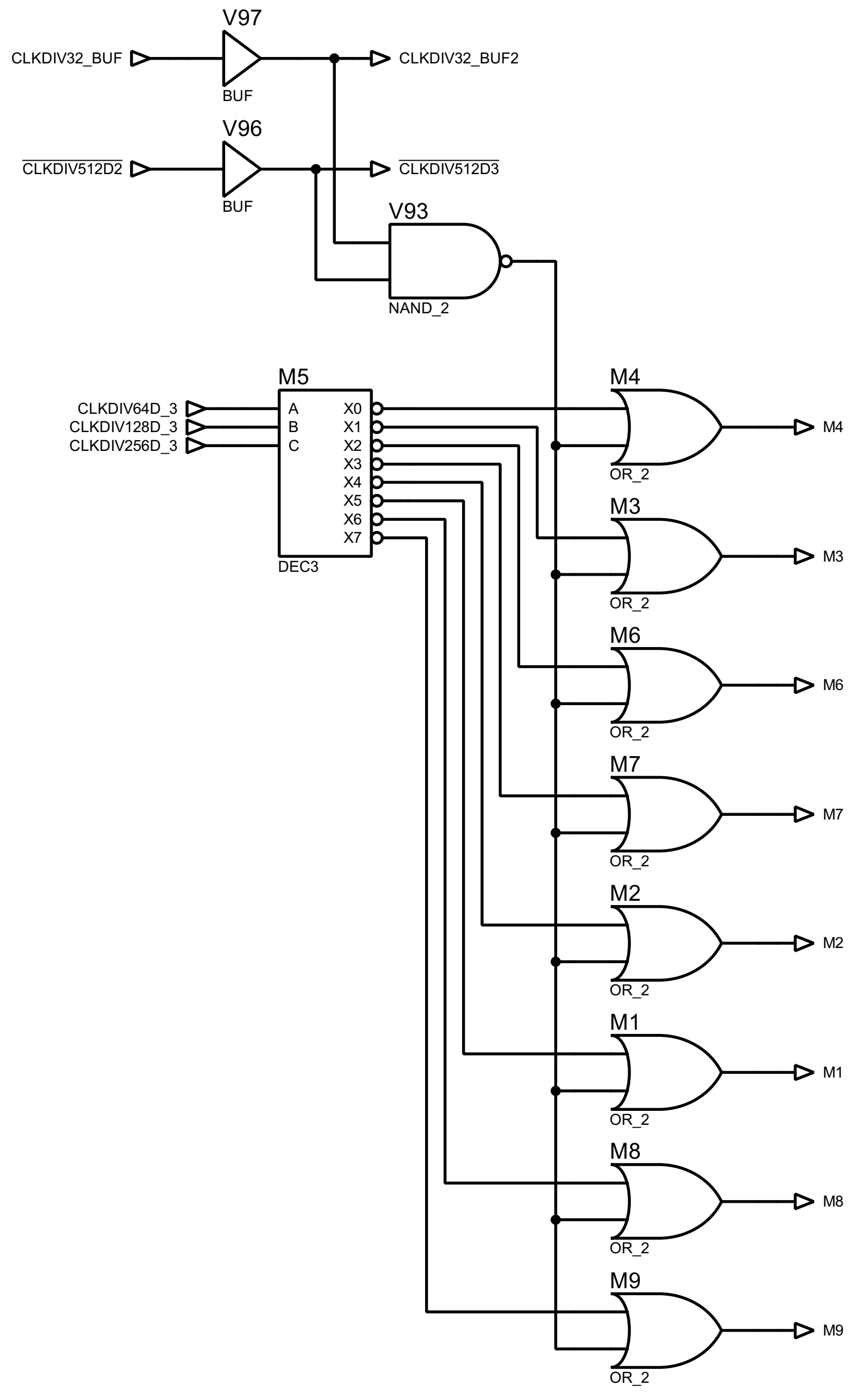

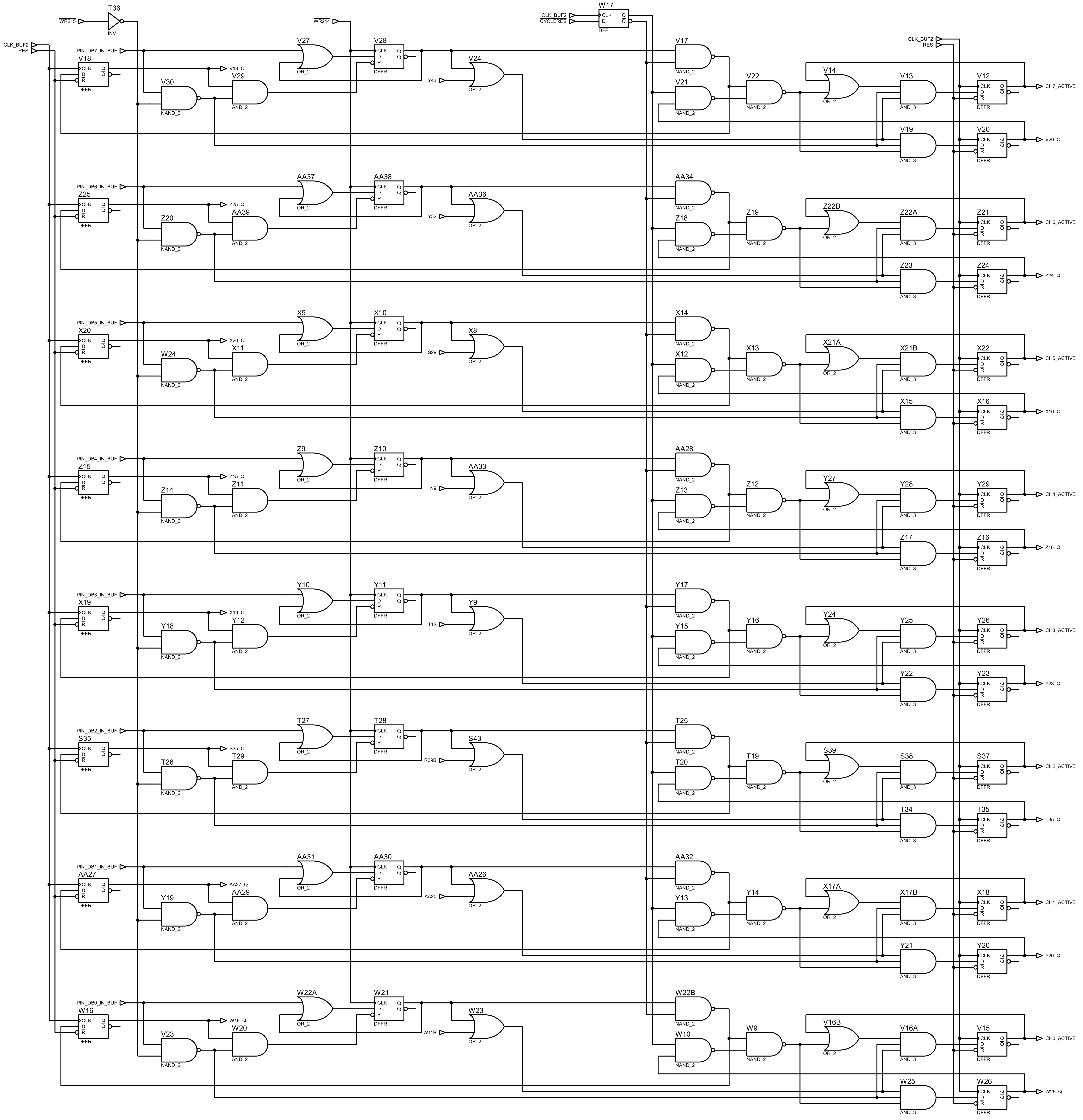

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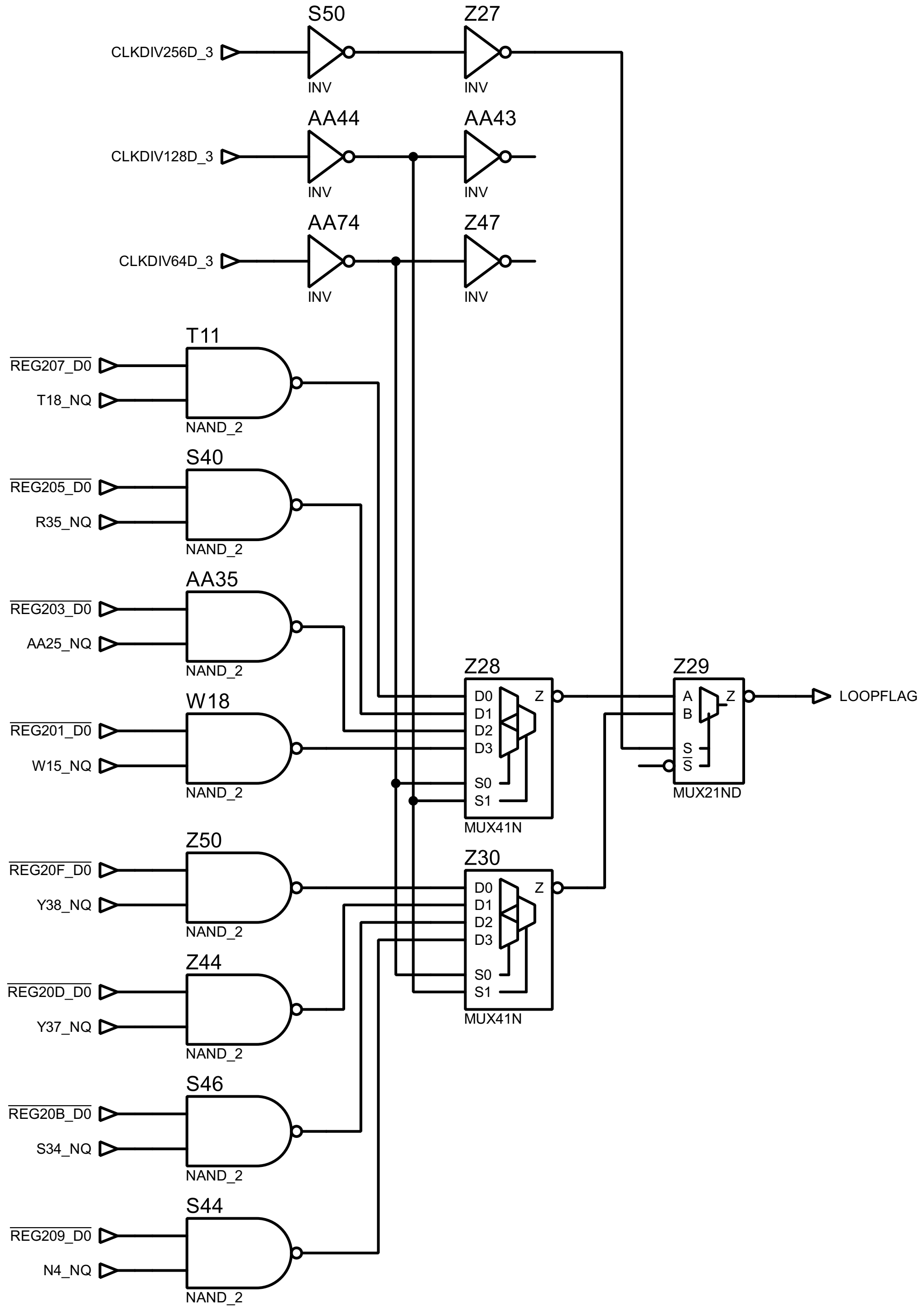
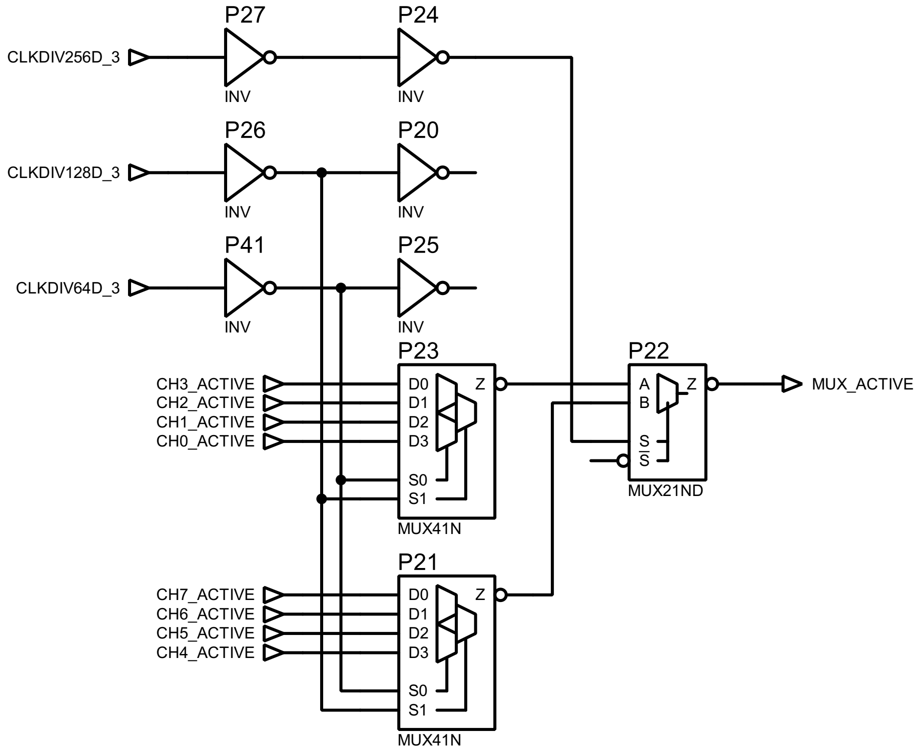
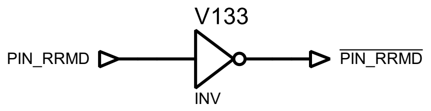
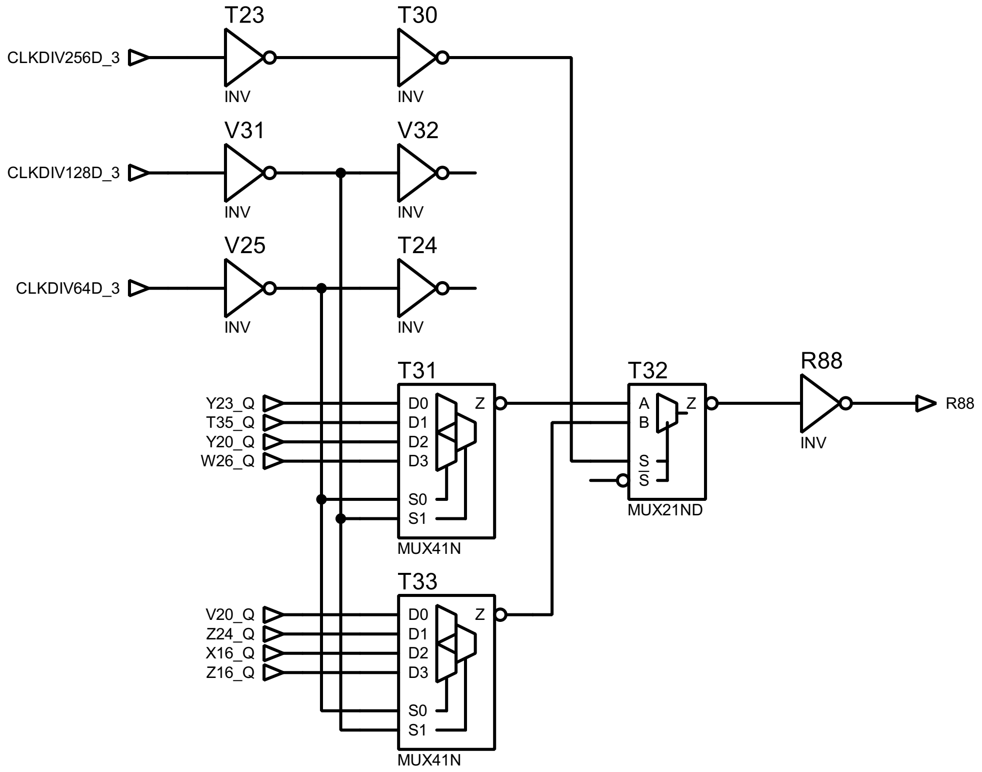
Konami 054539
KEY ON/OFF
BY: Sean Gonsalves REV: @REV
DATE:
10/29/2025
PAGE:
26/53







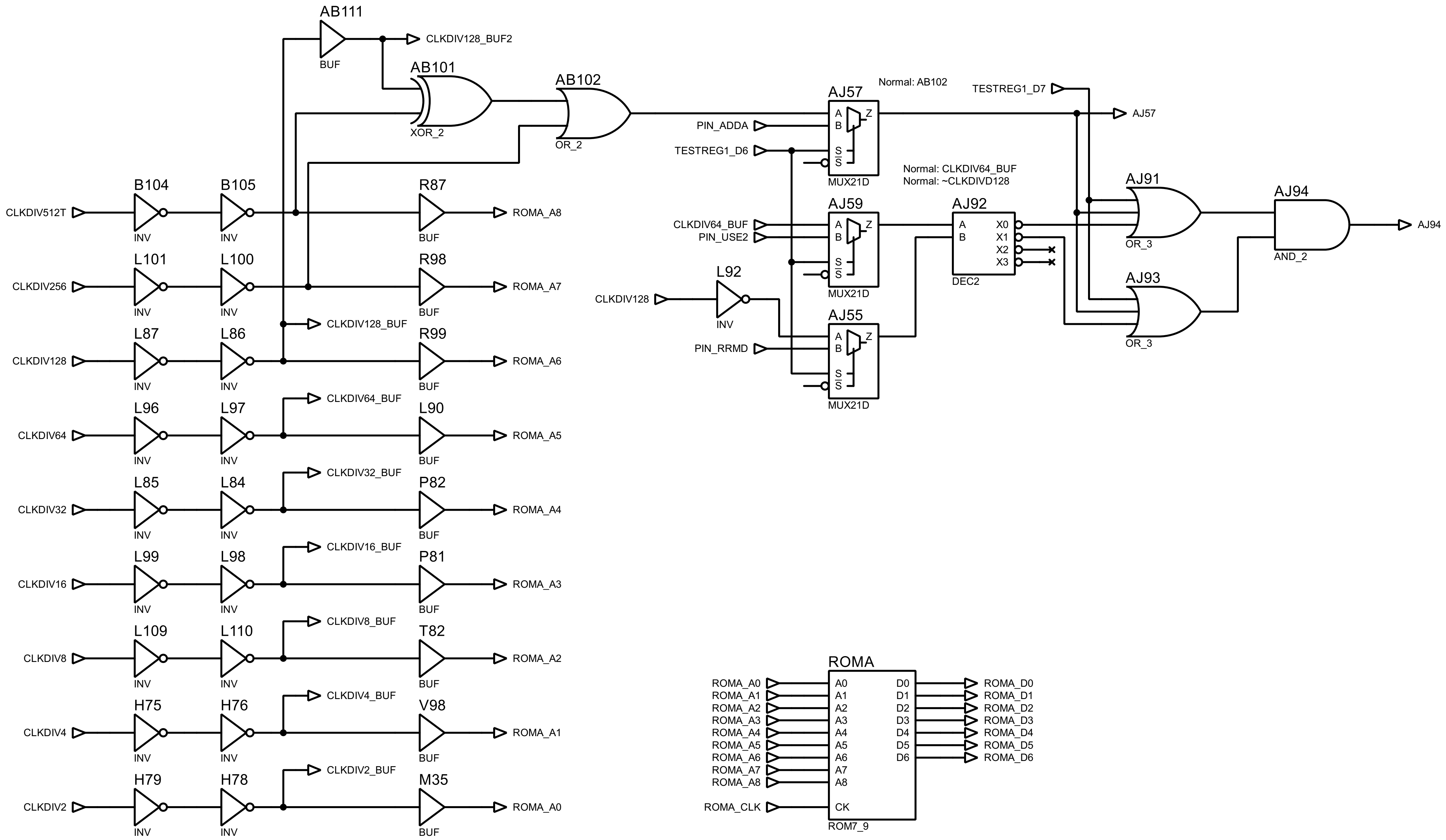


TITLE:
Konami 054539
START/STOP

DATE:
10/29/2025

PAGE:
27/53

BY: Sean Gonsalves
REV: @REV



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Konami 054539

ROM A

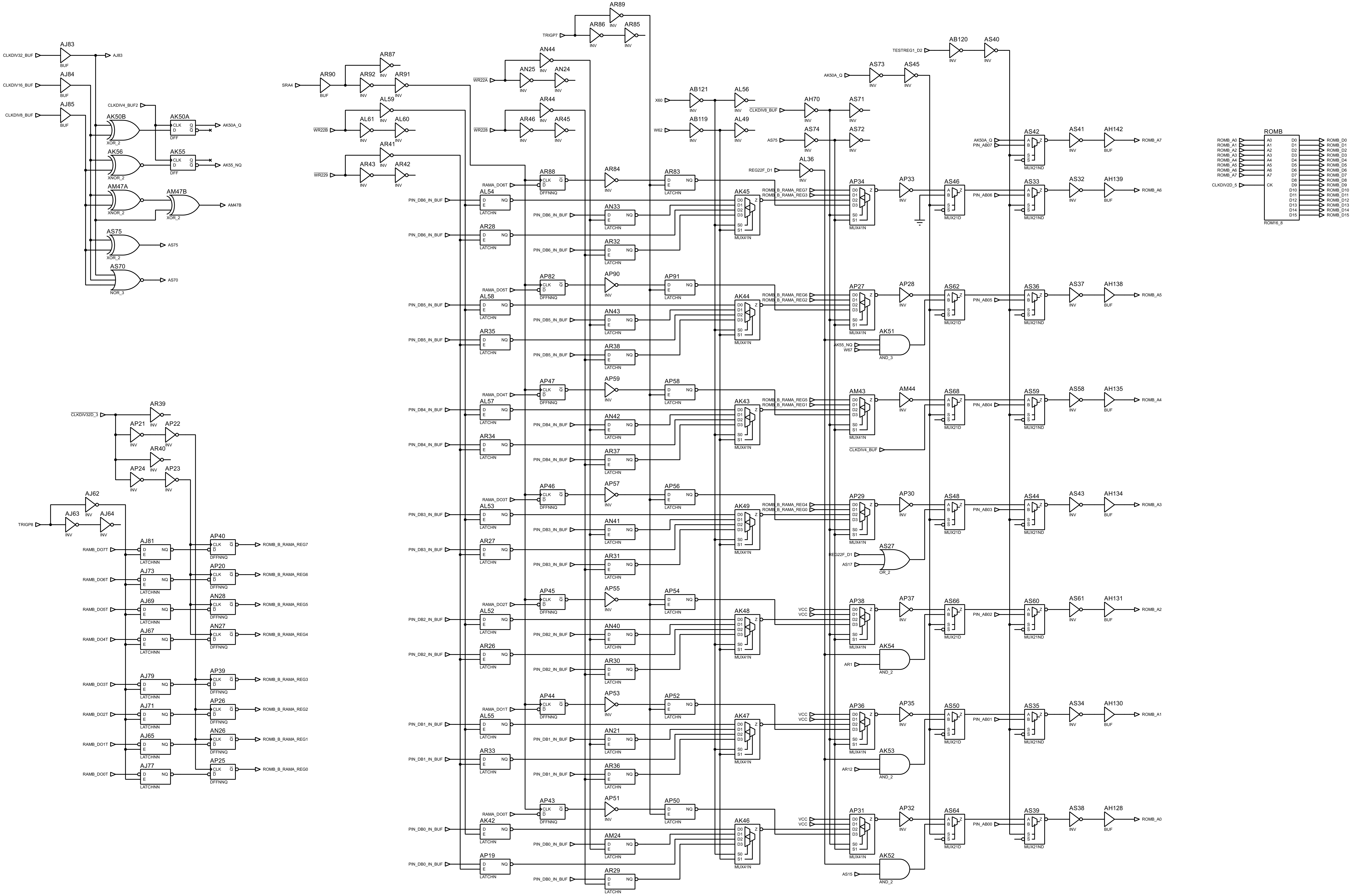
DATE:

10/29/2025

PAGE:

BY: Sean Gonsalves

REV: @REV



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Konami 054539

ROM B

DATE:

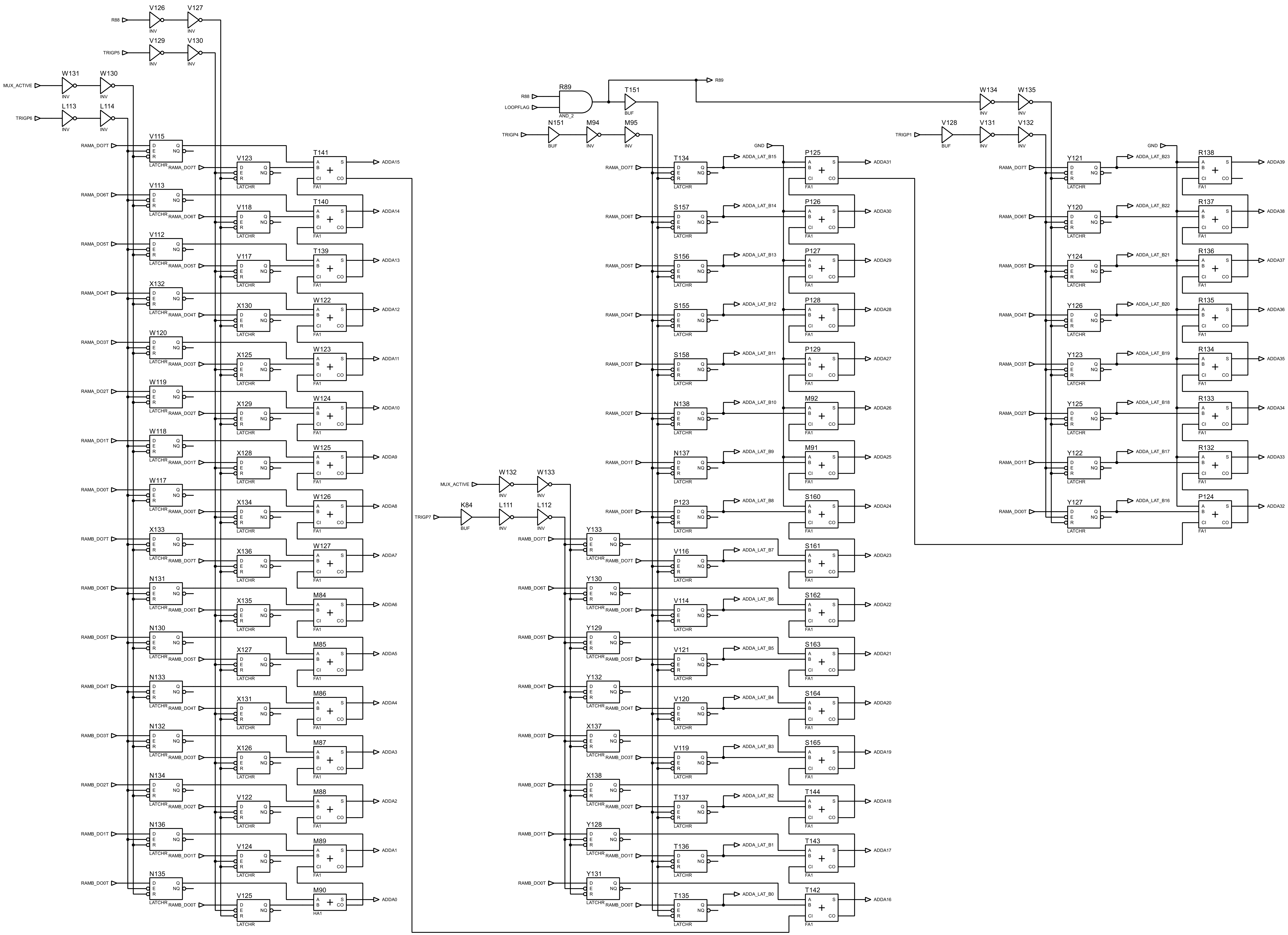
10/29/2025

PAGE:

29/53

BY: Sean Gonsalves

REV: @REV



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ADDER A - FRAC

BY: Sean Gonsalves

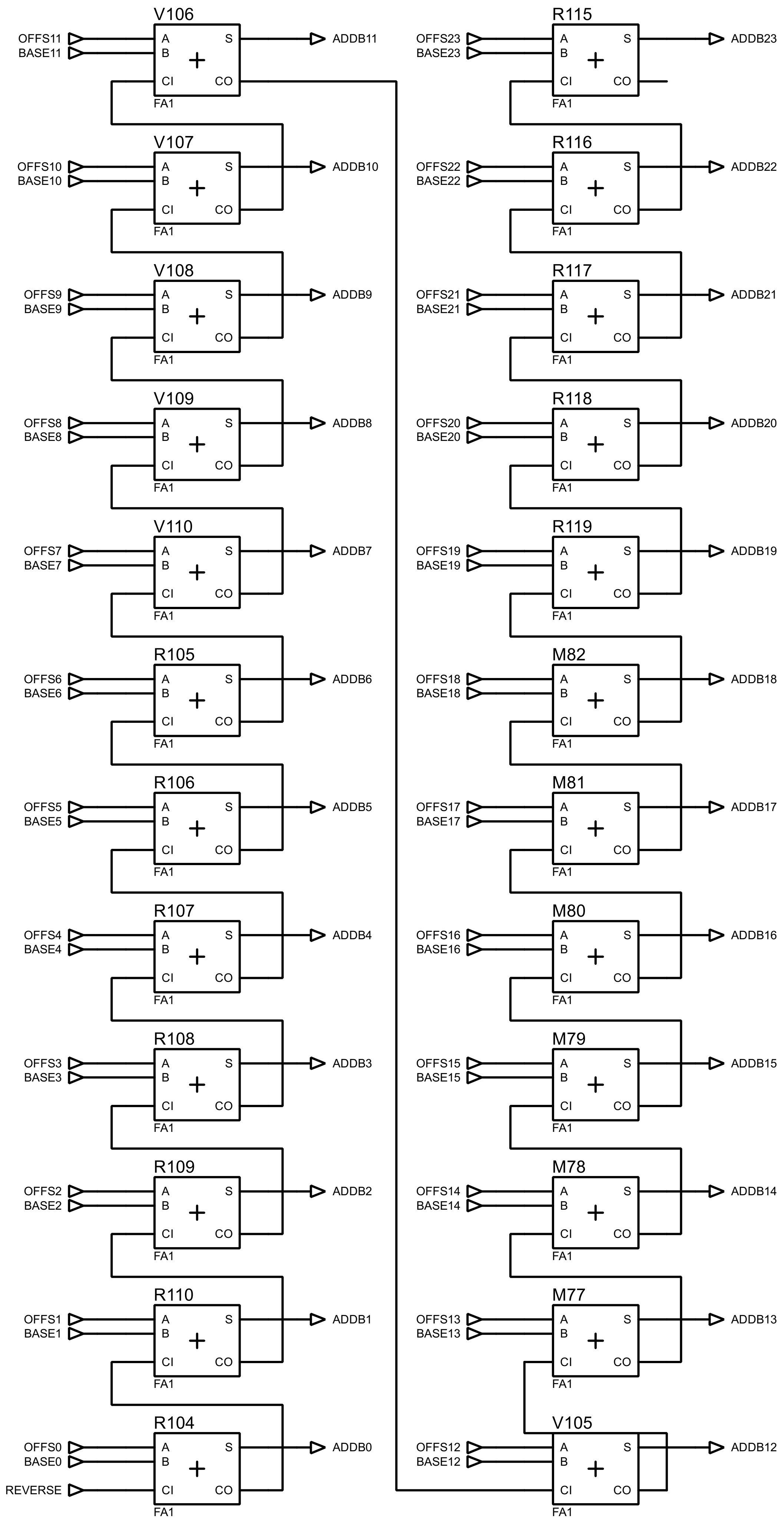
REV: @REV

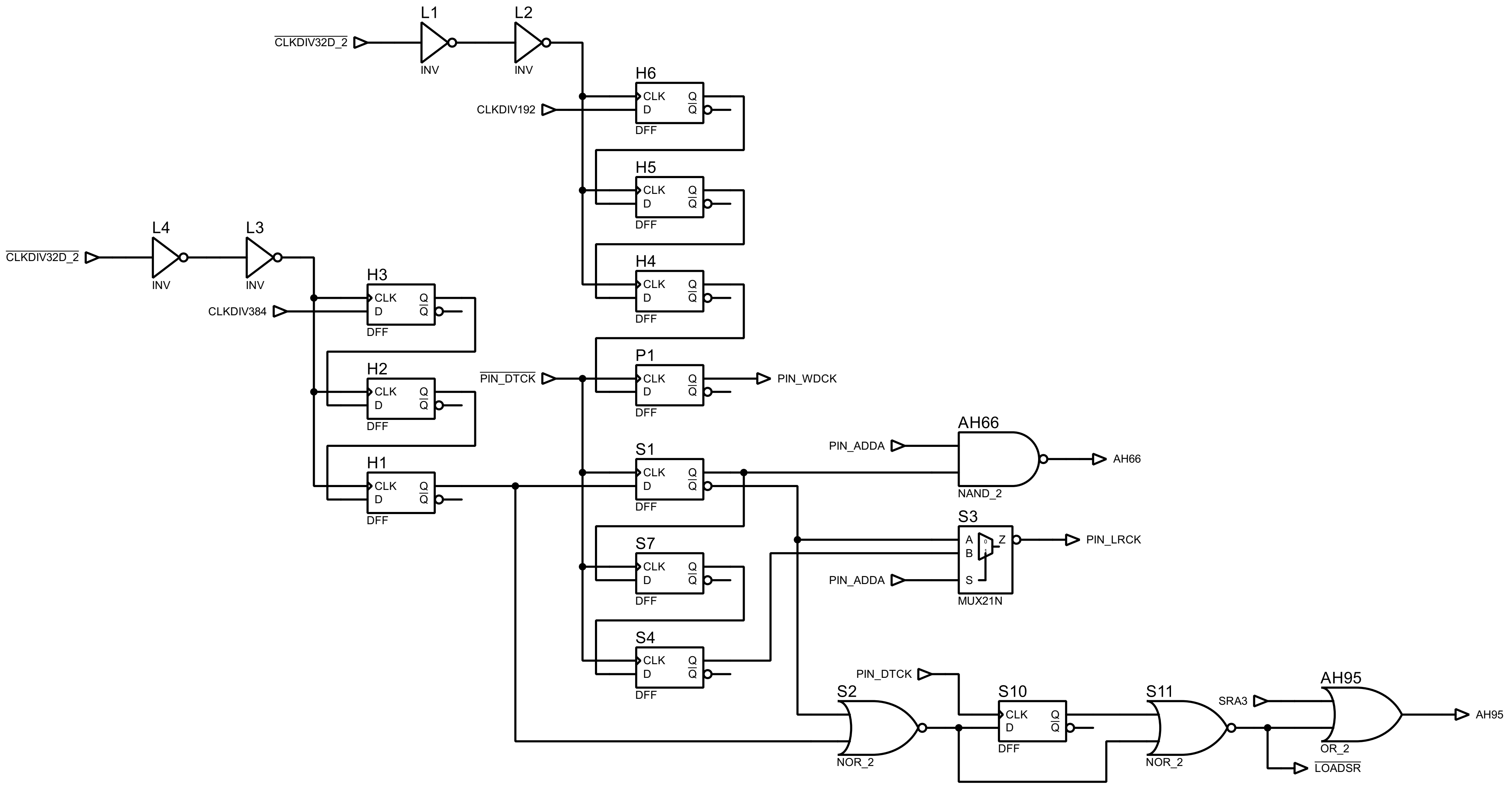
DATE:

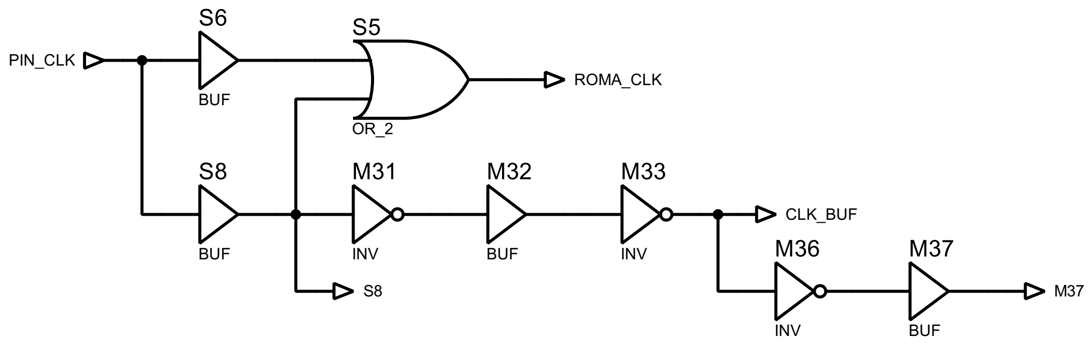
10/29/2025

PAGE:

30/53







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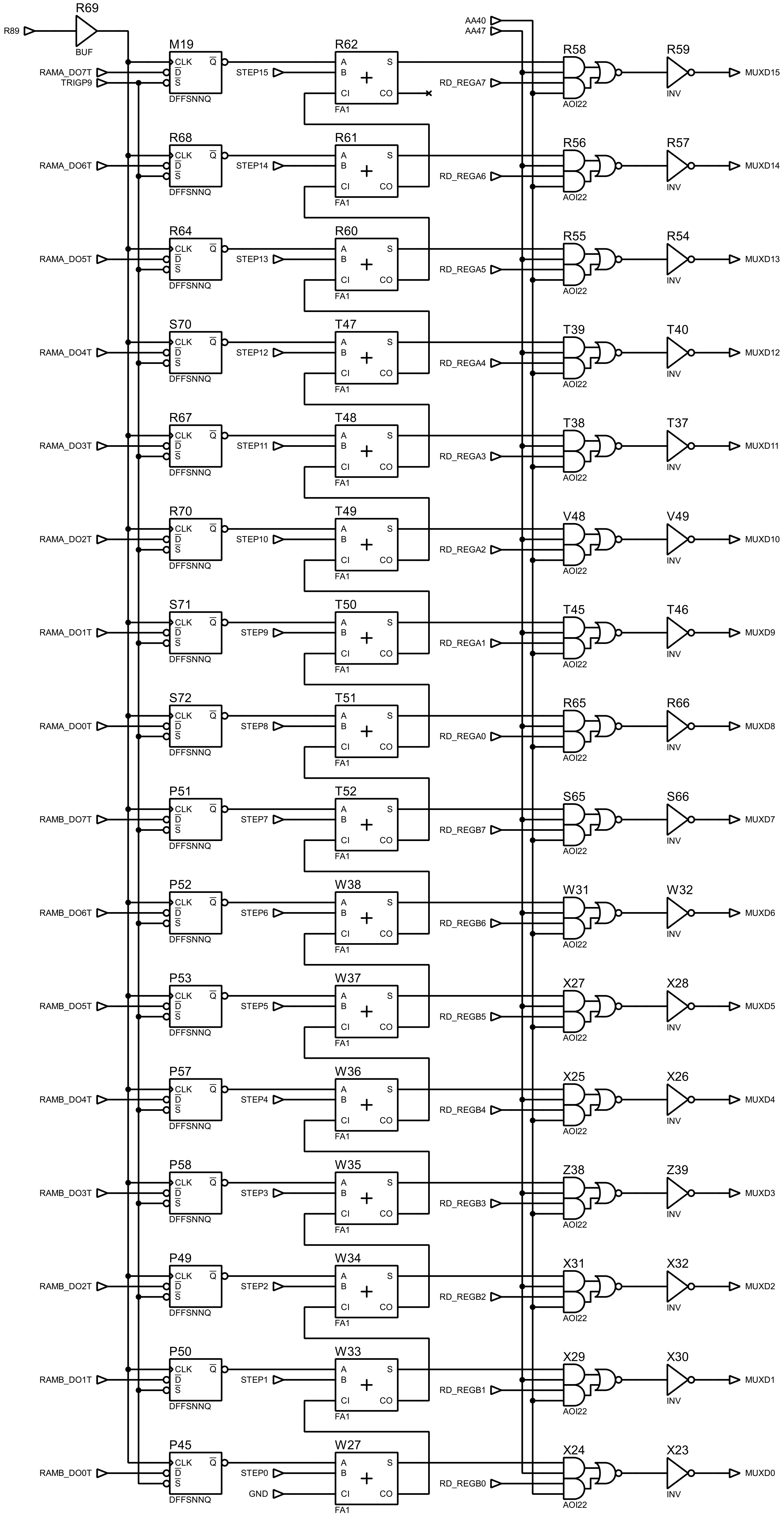
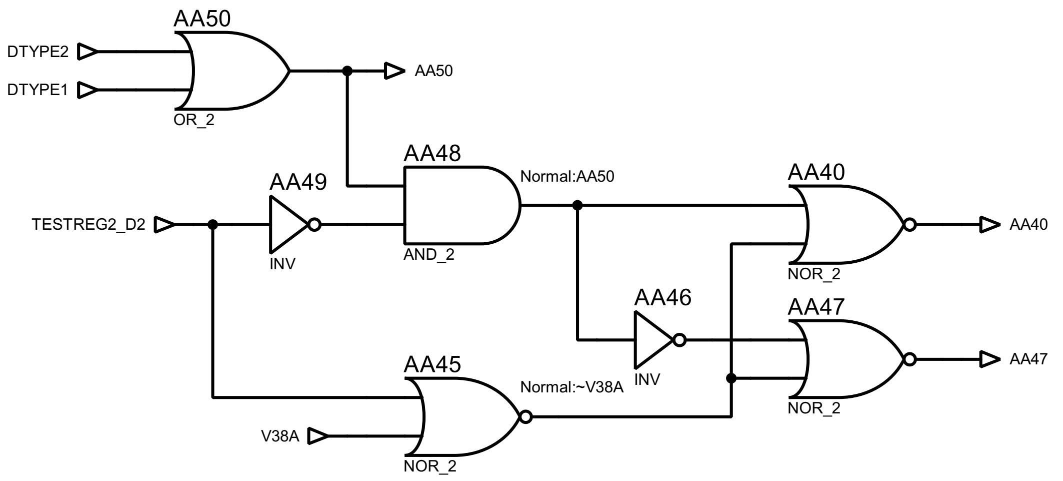
ADDER B - SAMPLE ADDRESS
BY: Sean Gonsalves
REV: @REV
DATE:
10/29/2025
PAGE:
31/53





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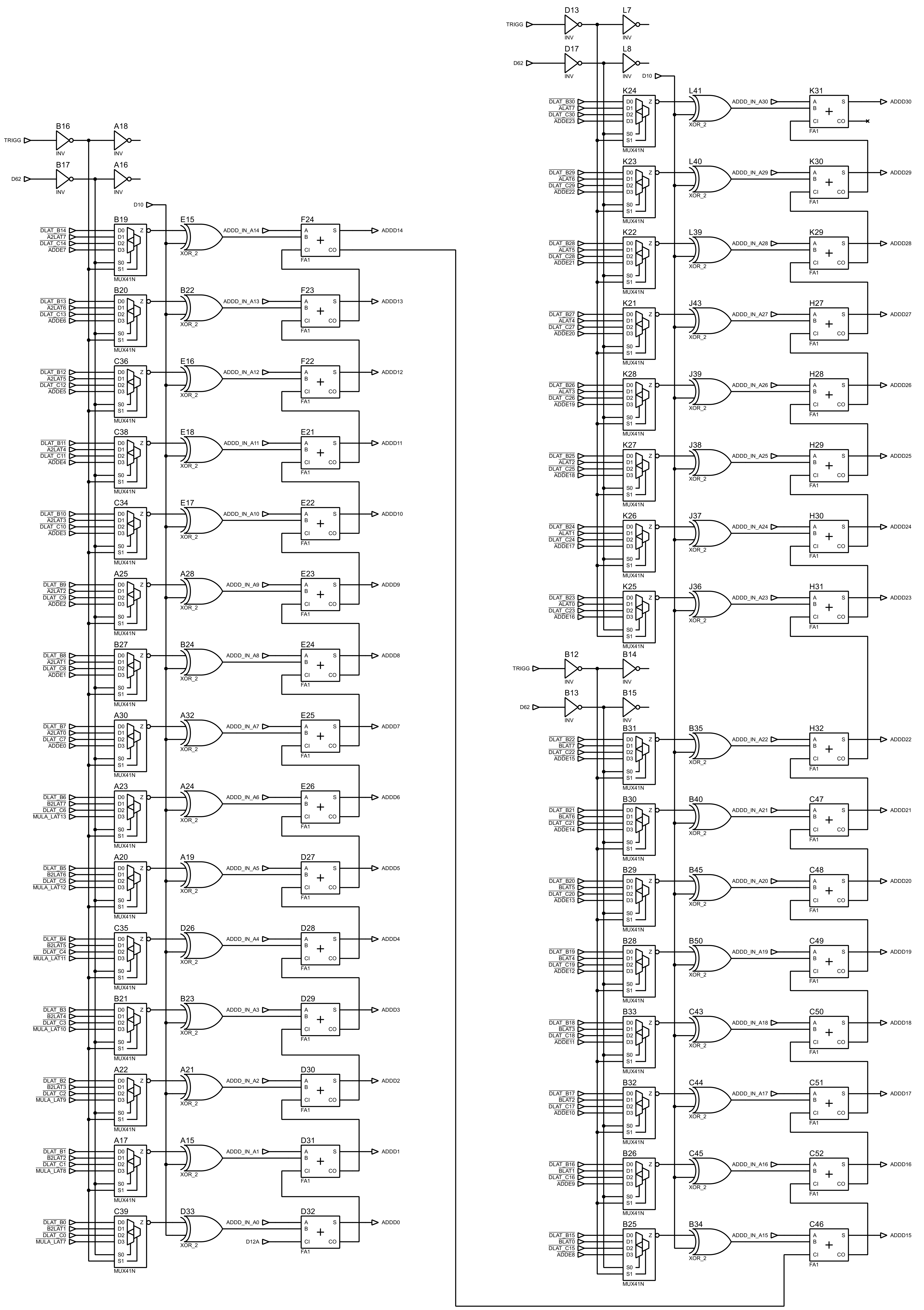
DATE:
10/29/2025

PAGE:
32/53

BY: Sean Gonsalves
REV: @REV



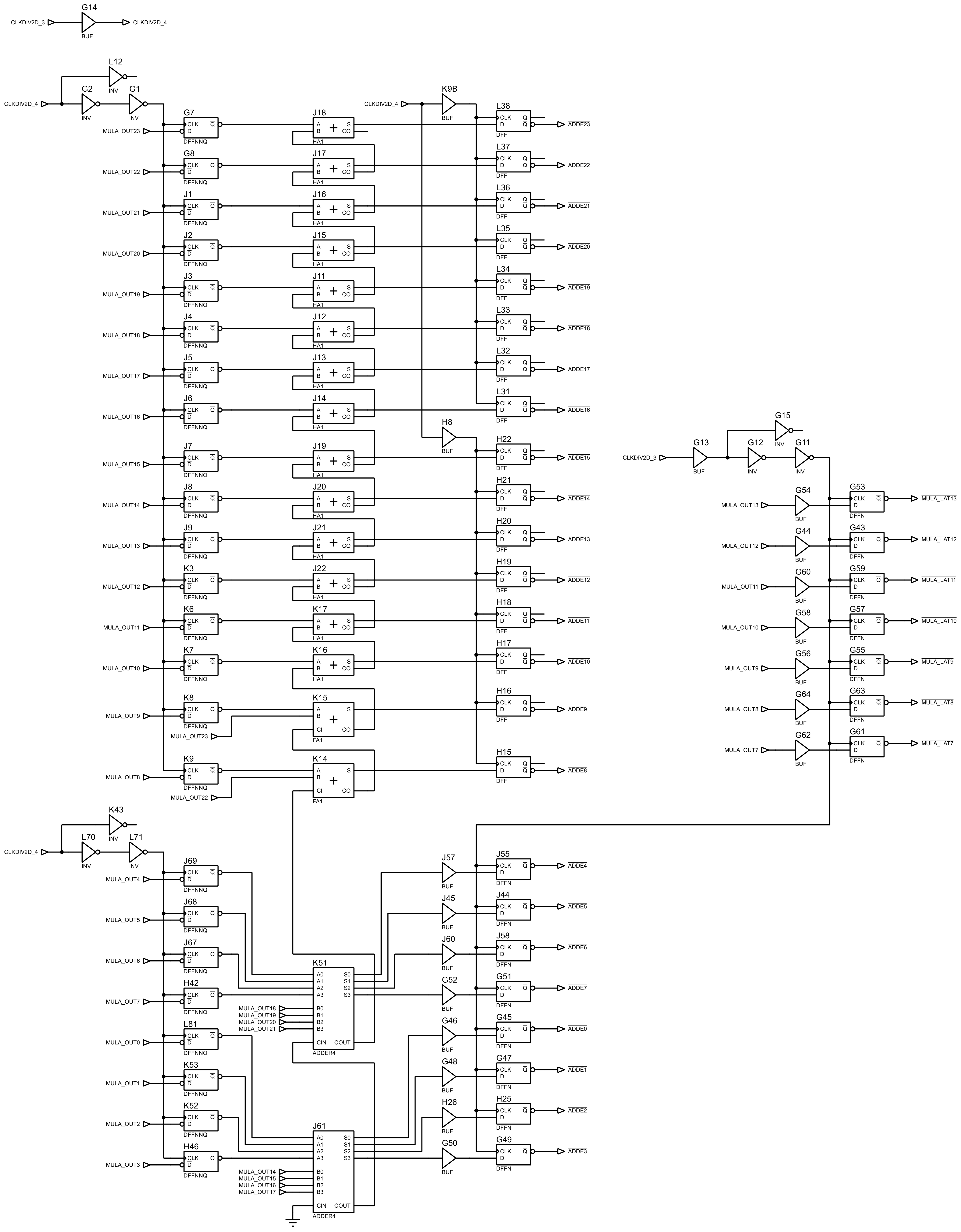
TITLE:
Konami 054539
ADDER D

DATE:
10/29/2025

PAGE:
33/53

BY: Sean Gonsalves

REV: @REV



TITLE:

Konami 054539
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DATE:
10/29/2025

PAGE:
34/53

BY: Sean Gonsalves

REV: @REV

D LATCHES and MUX F

TITLE:

Konami 054539

D LATCHES and MUX F

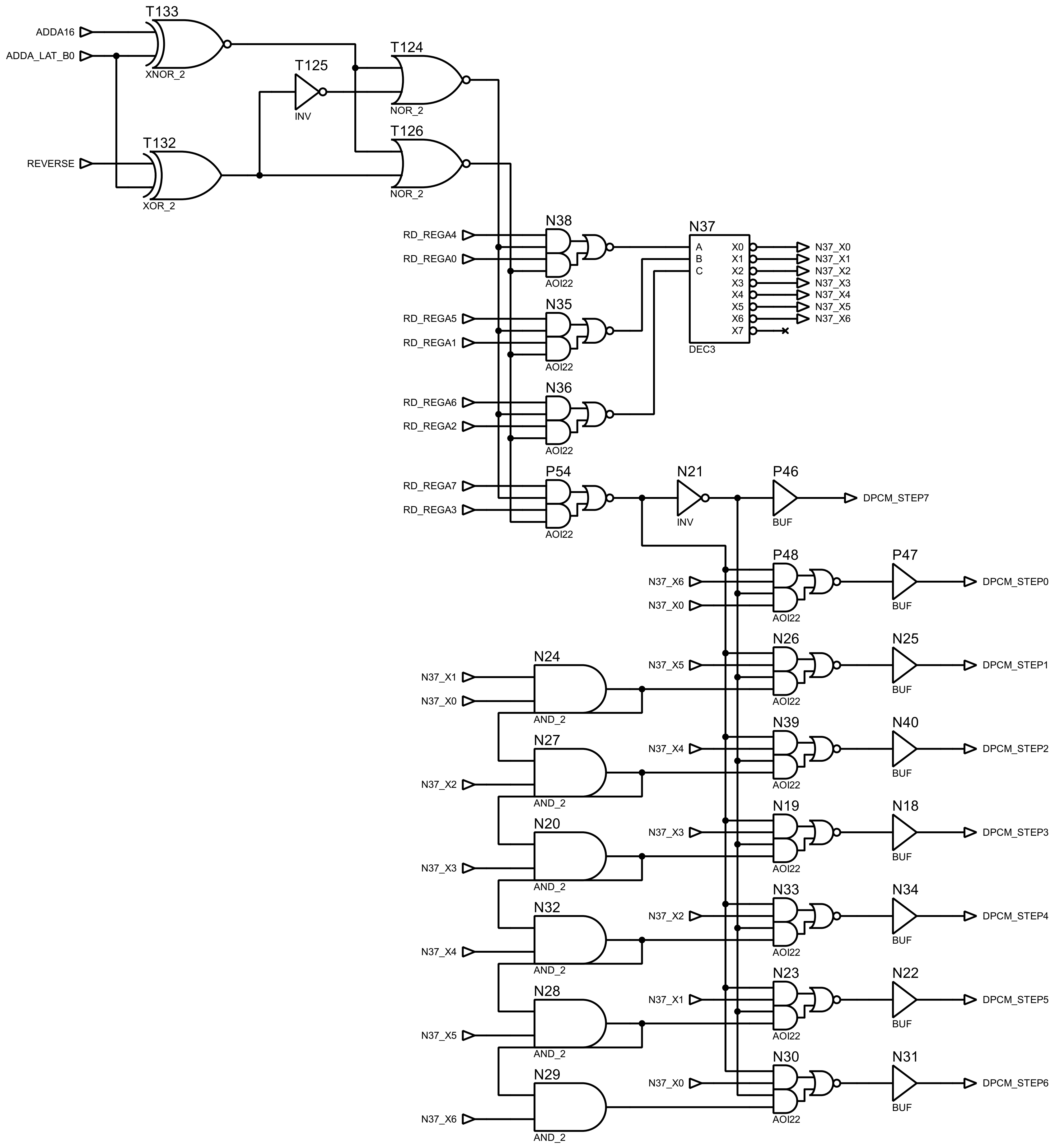
BY: Sean Gonsalves REV: @REV

DATE:

10/29/2025

PAGE:

35/53



TITLE:

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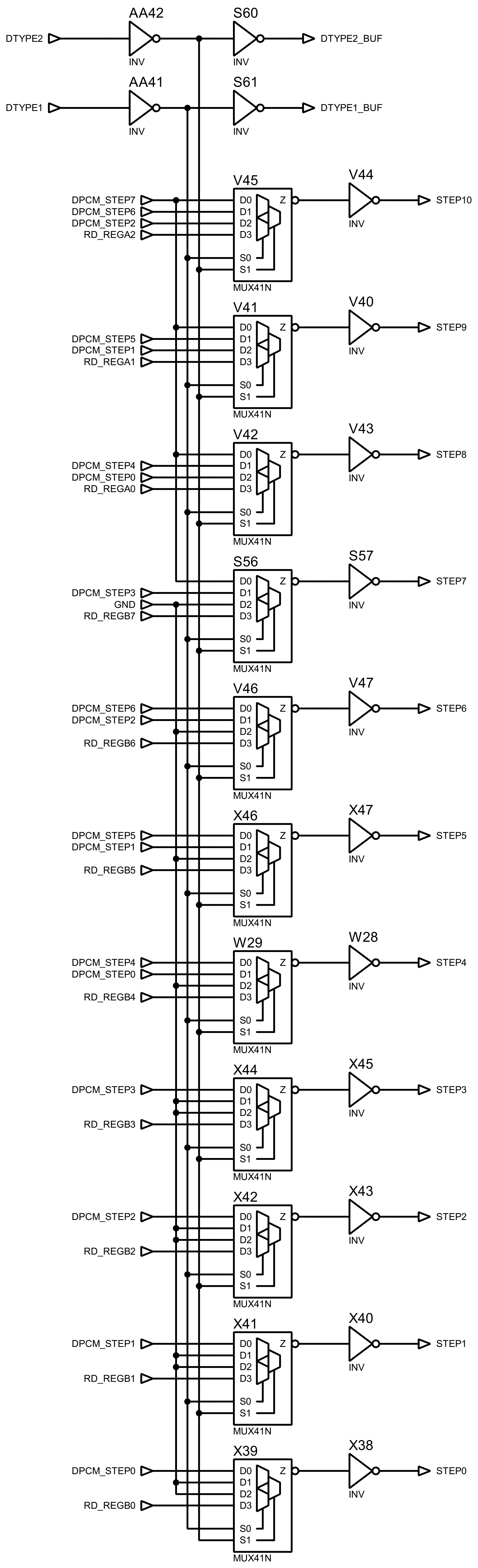
DPCM STEP

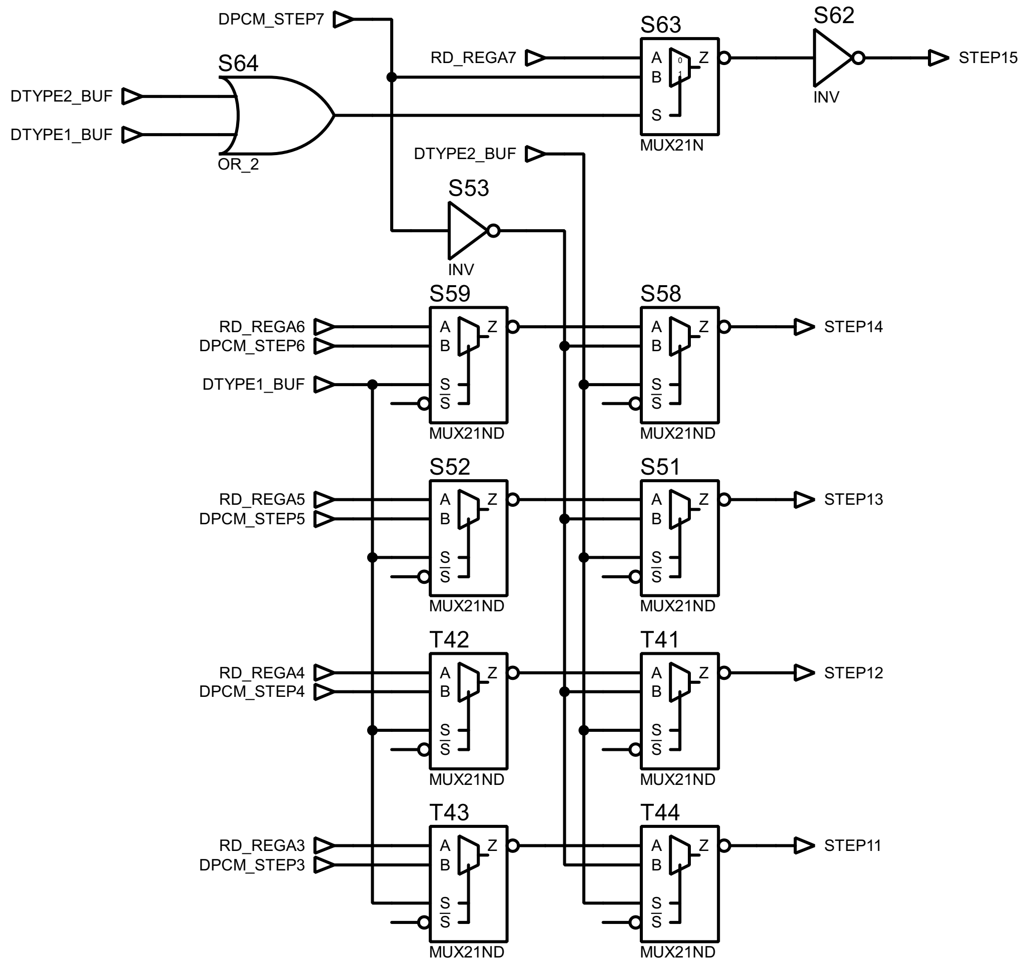
DATE:
10/29/2025

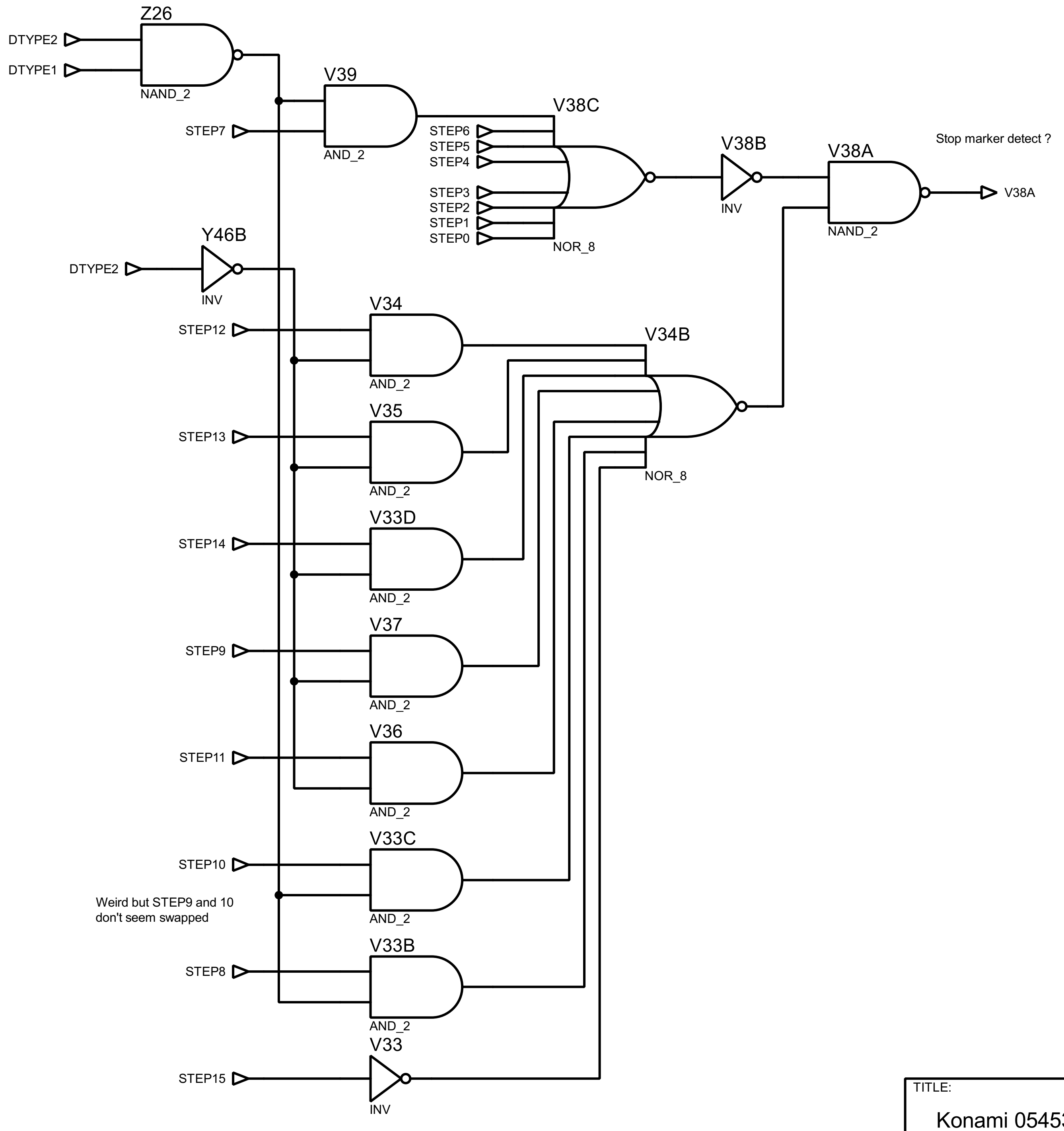
PAGE:
36/53

BY: Sean Gonsalves

REV: @REV







Stop marker detect ?

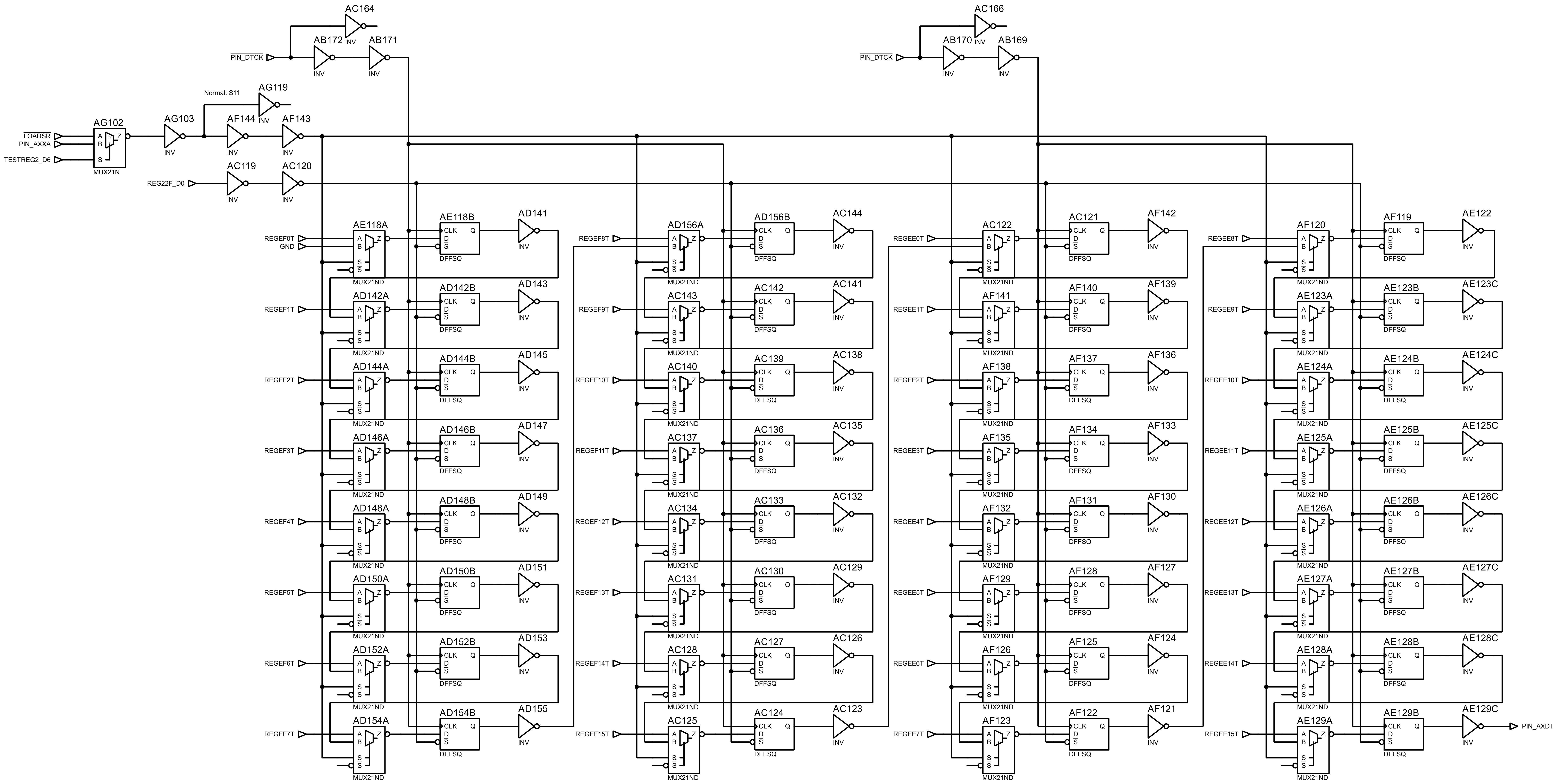
Weird but STEP9 and 10 don't seem swapped

TITLE:
Konami 054539
STEP TYPE MUX

DATE:
10/29/2025

PAGE:
37/53

BY: Sean Gonsalves
REV: @REV



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FINAL OUTPUT PISO

DATE:
10/29/2025

PAGE:
38/53

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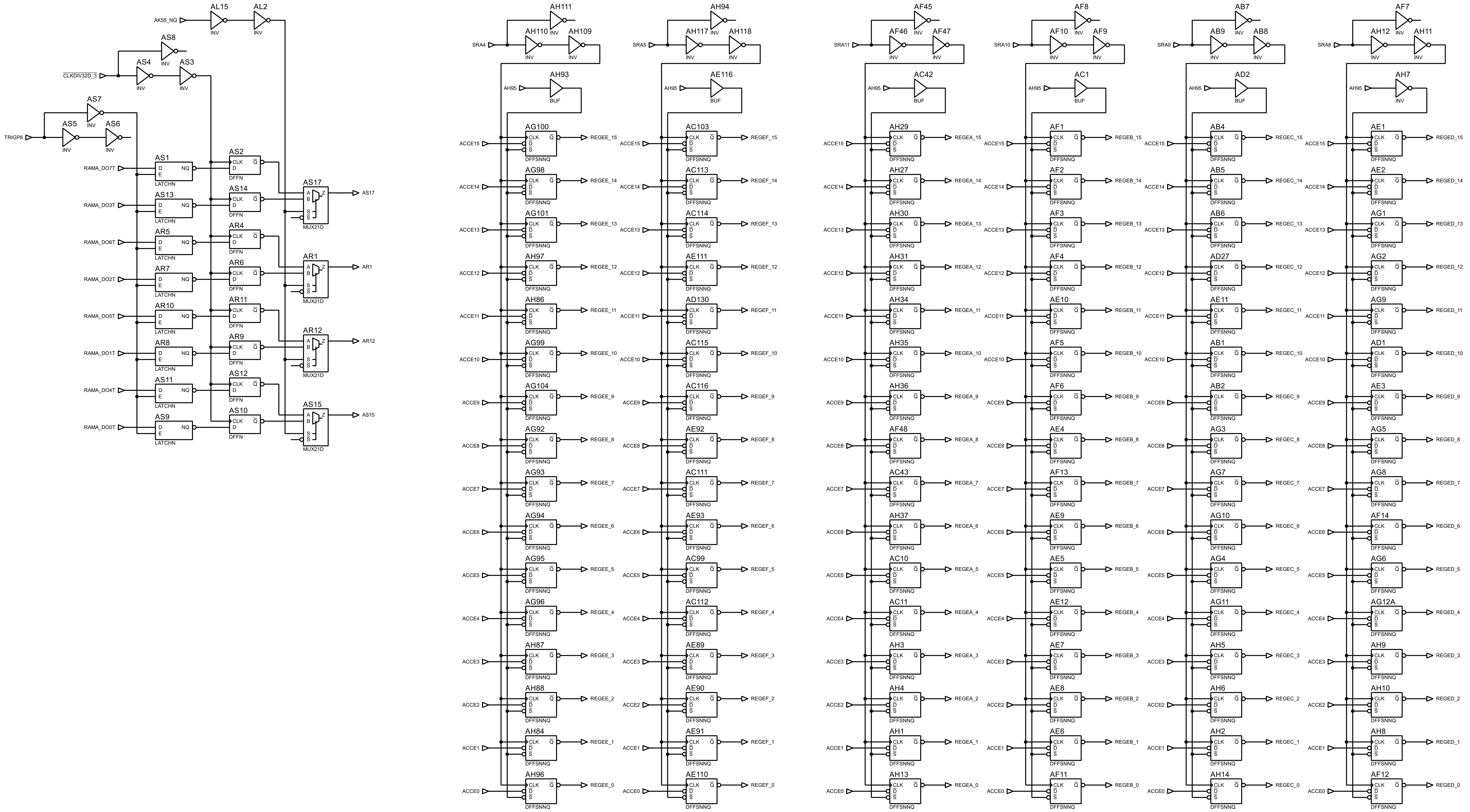

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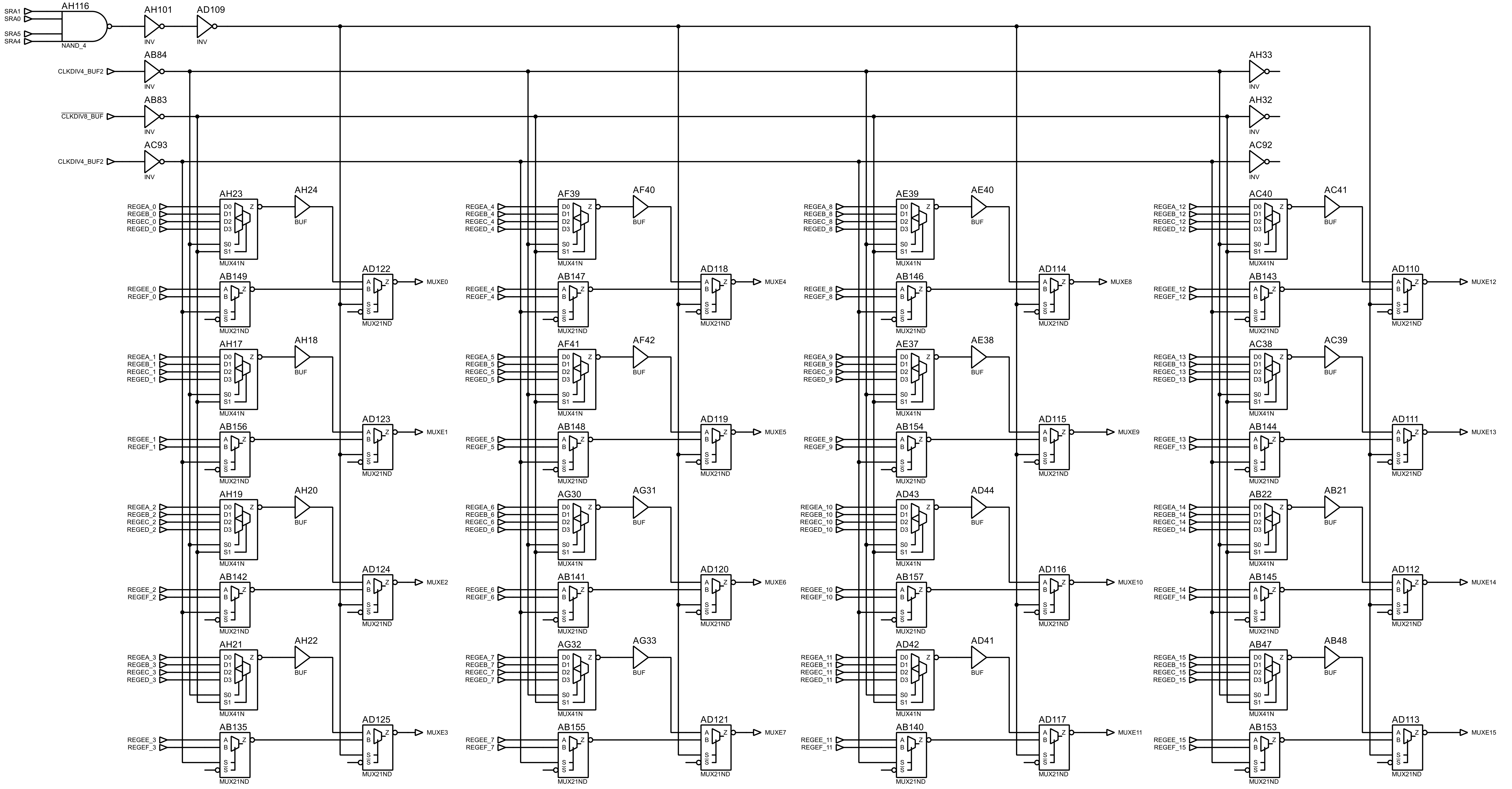
REGISTERS E

BY: Sean Gonsalves REV: @REV

DATE:
10/29/2025

PAGE:
39/53



TITLE:
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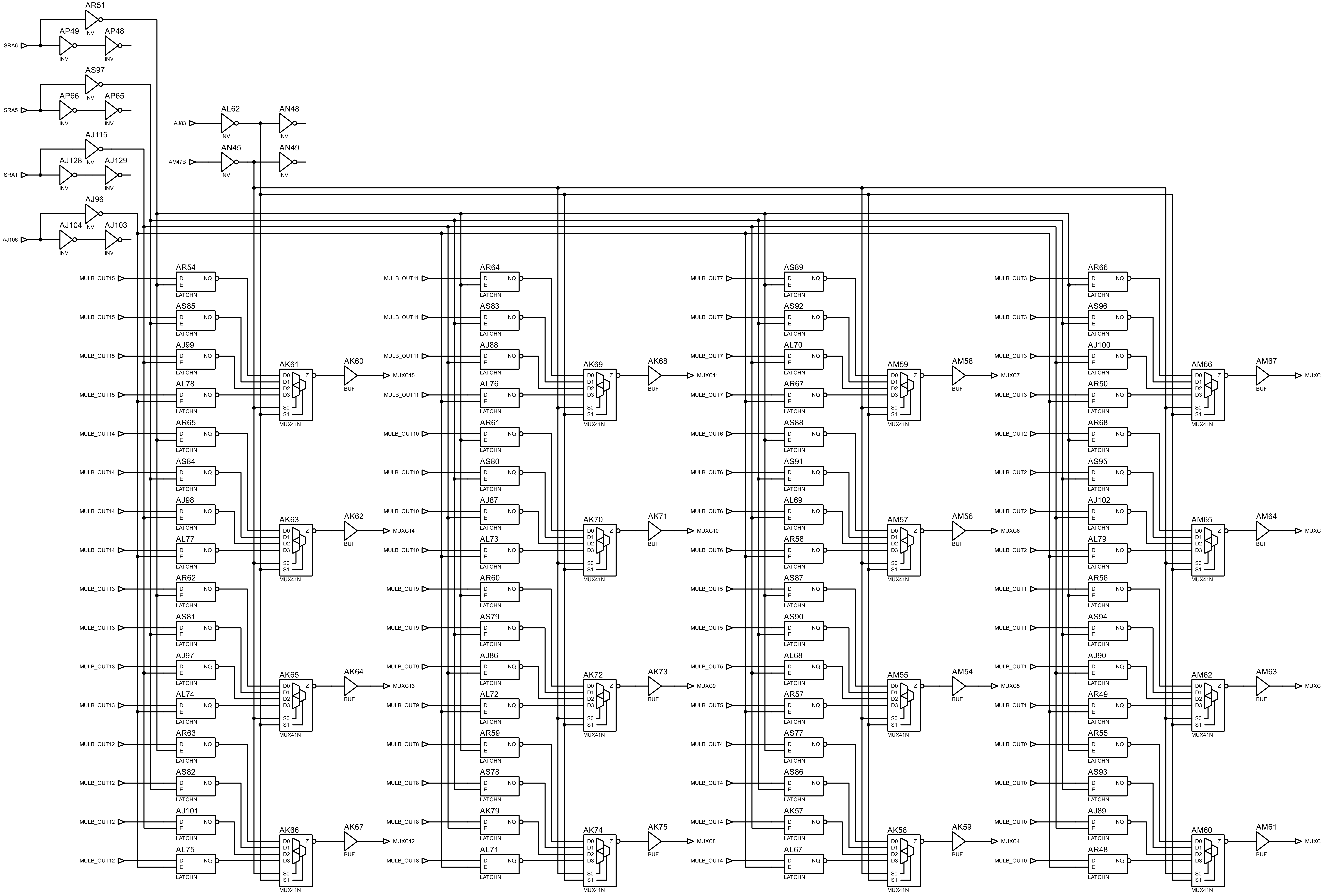
REG E MUX

DATE:
10/29/2025

PAGE:
40/53

BY: Sean Gonsalves
REV: @REV



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	PAGE:
	41/53
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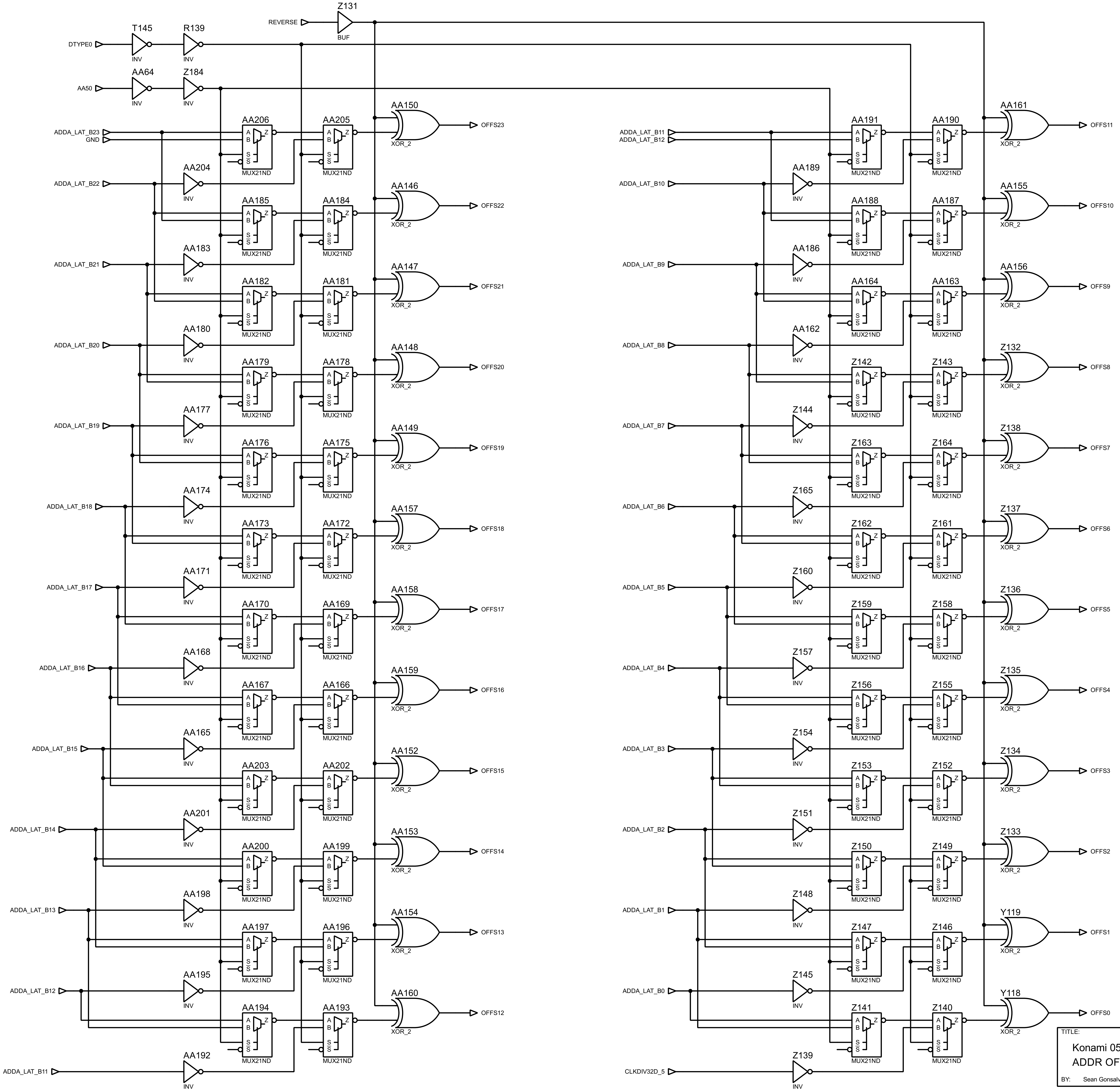

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ADDR OFFSET SELECT
BY: Sean Gonsalves
REV: @REV
DATE:
10/29/2025
PAGE:
42/53

AE119
AE120
AD157
AD158
AD159
AD160
AD121
AG115
AG116
AG113
AE73
AE121
AE130
AE131
AB178
AD68
AD69
AE53
AE44
AE45
AE42
AE43
AF62
AF57
AB103B
AB103A
AB88
AB87
AC70
AC71
AB89
AE72
AF70
AB80
AF73
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AH58
AJ49
AJ16
AF43
AH41
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AH105
AH106
AE84A
AE84B
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AE87
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AG97
AE94
AB116
AB117B
AB117A
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AD98
AD94
AJ15
AF52
CLKDIV32D_2
CLKDIV4D_3
CLKDIV16D_2
CLKDIV128D_4
REG224_D2
REG224_D6
CLKDIV128_BUF2
REG224_D1
REG224_D5
REG225_D4
REG225_D5
REG224_D3
AF117
ACCB7
ACCA7
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ACCC13
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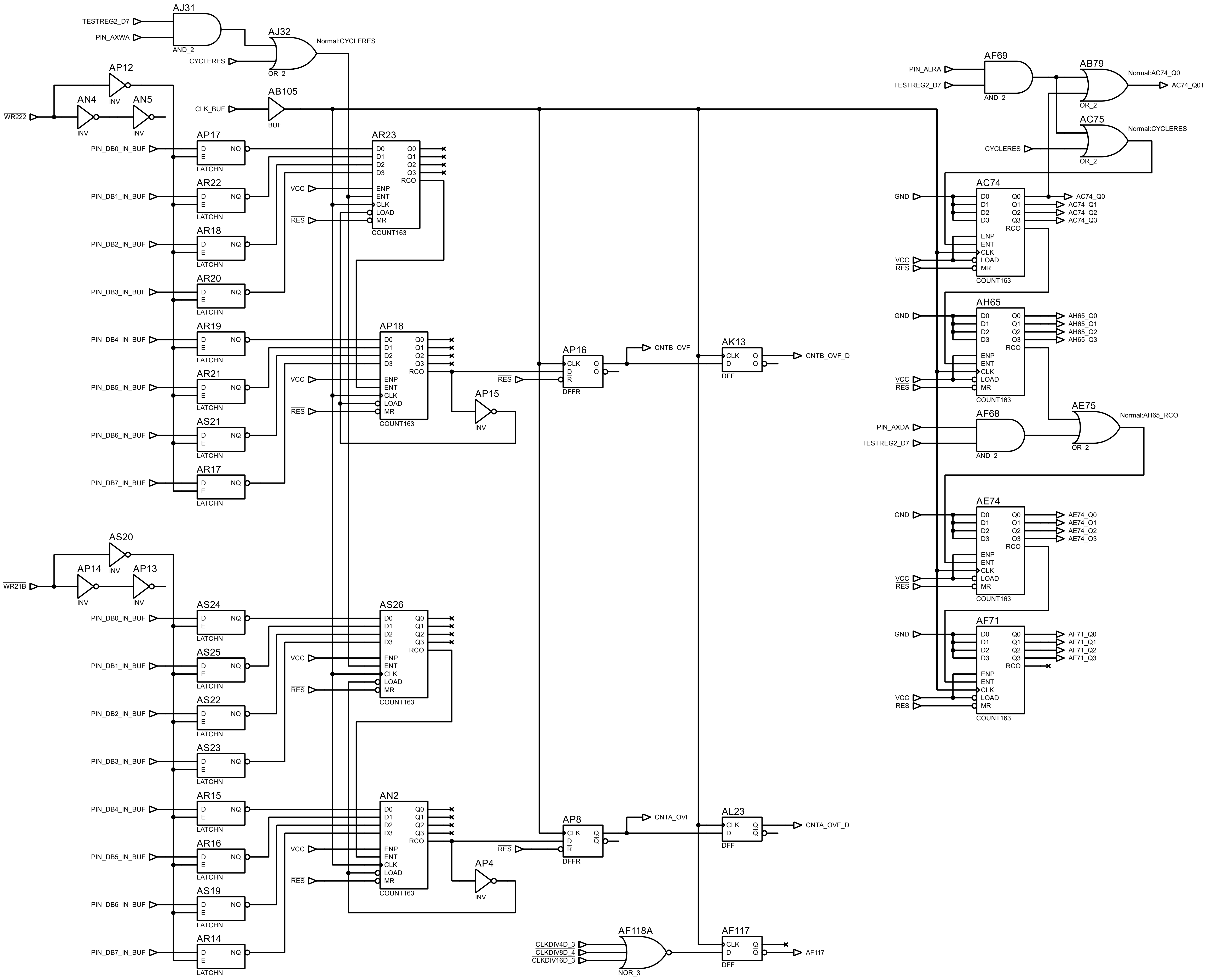
BY: Sean Gonsalves

REV: @REV

DATE:
10/29/2025

PAGE:
43/53



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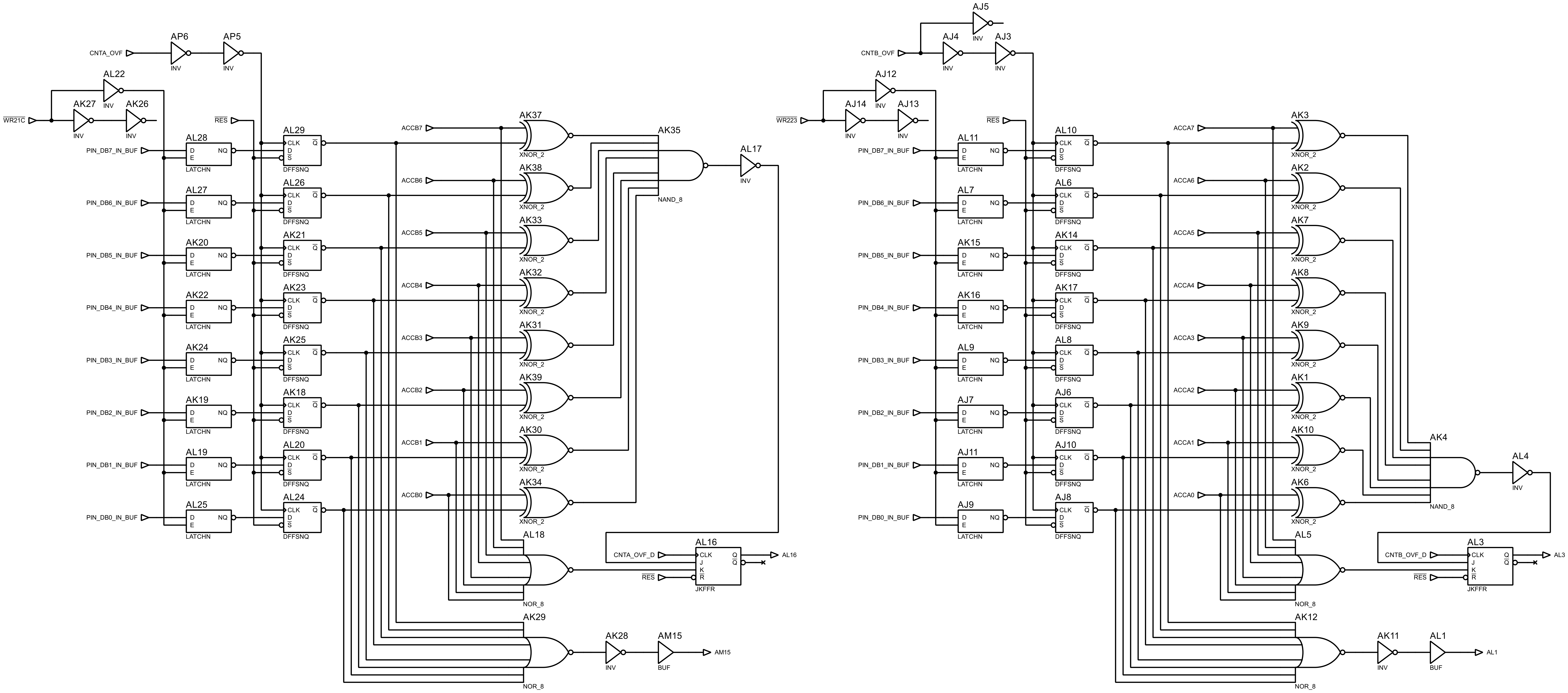
Konami 054539 COUNTERS	10/29/2025
	PAGE:
BY: Sean Gonsalves REV: @REV	44/53



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DATE:

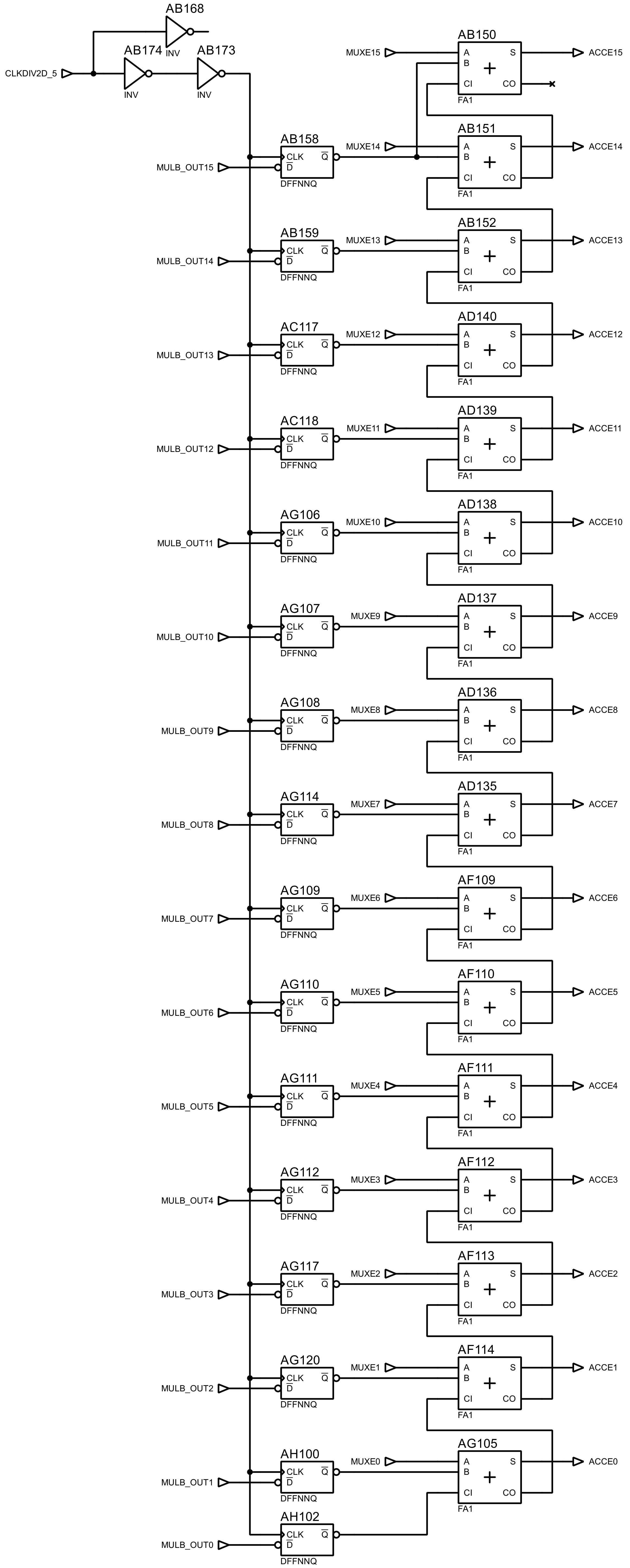
10/29/2025

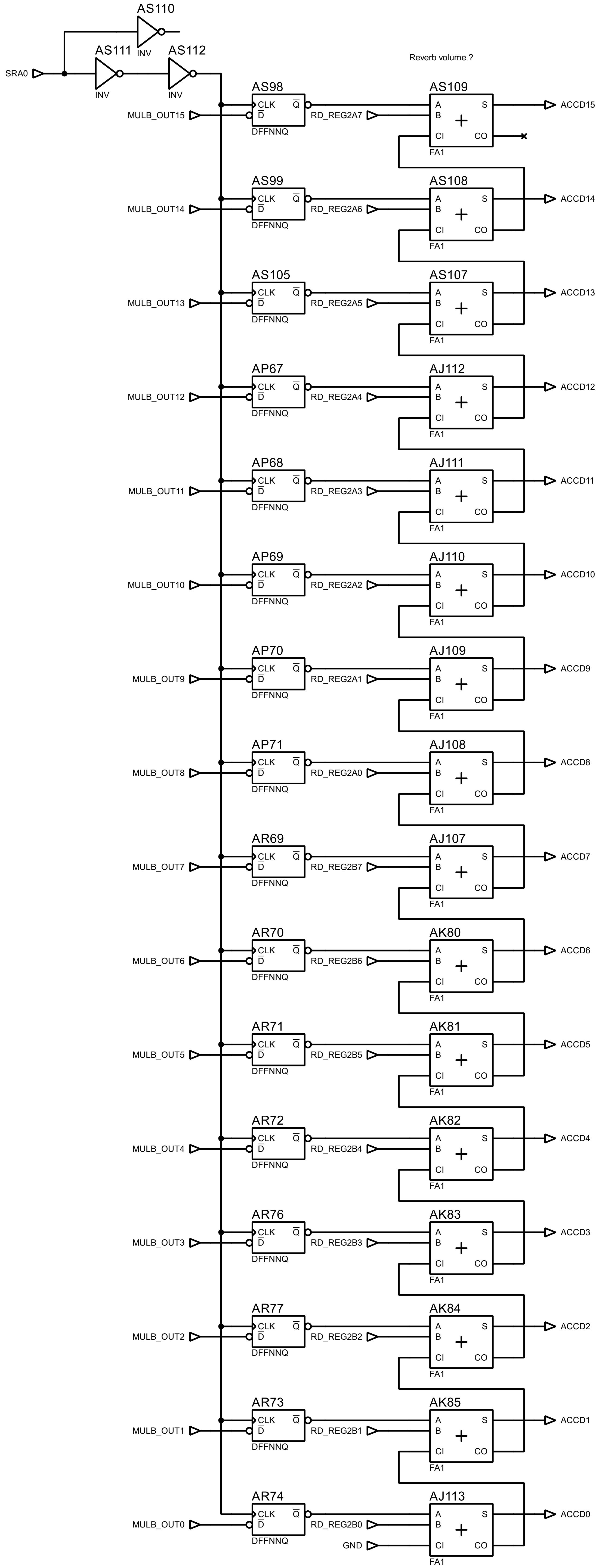
PAGE:

45/53

BY: Sean Gonsalves

REV: @REV





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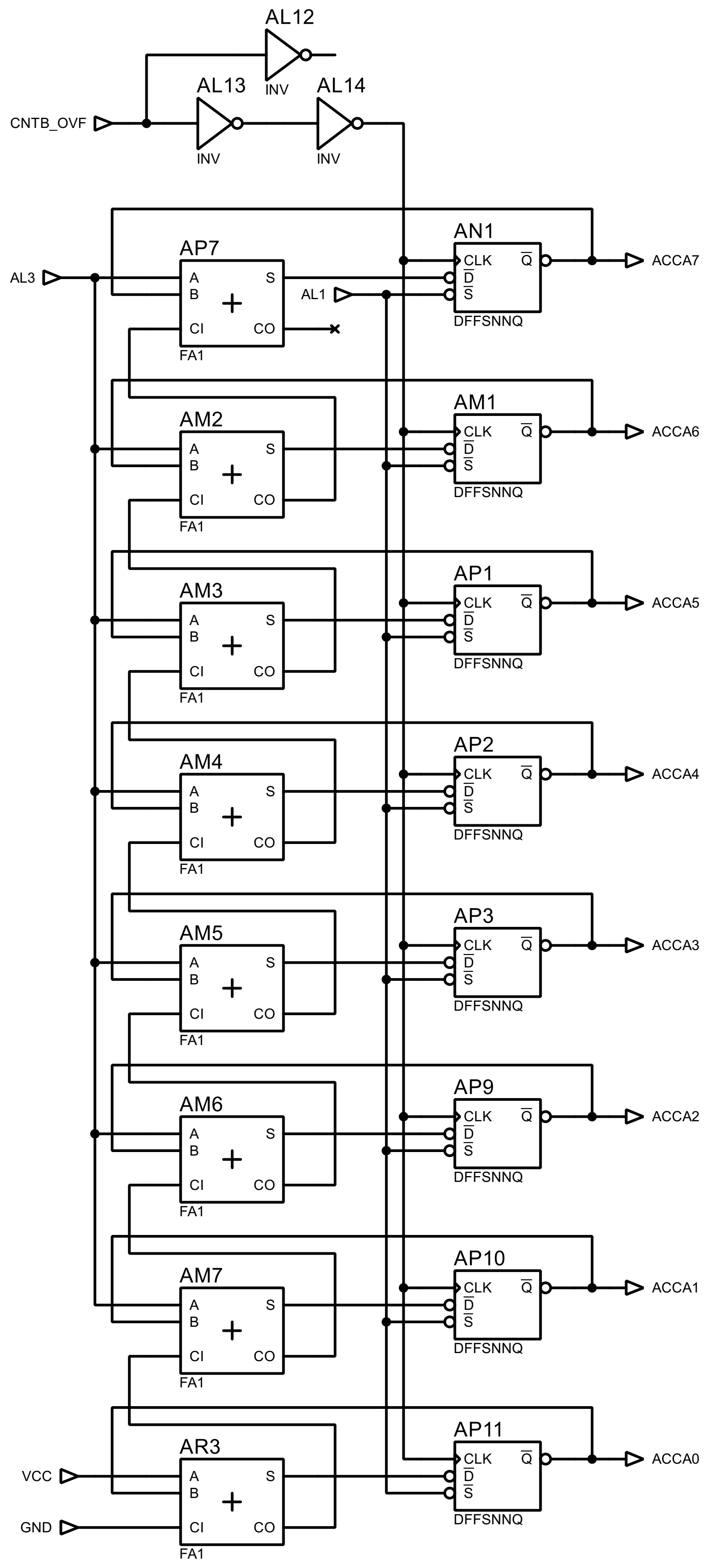
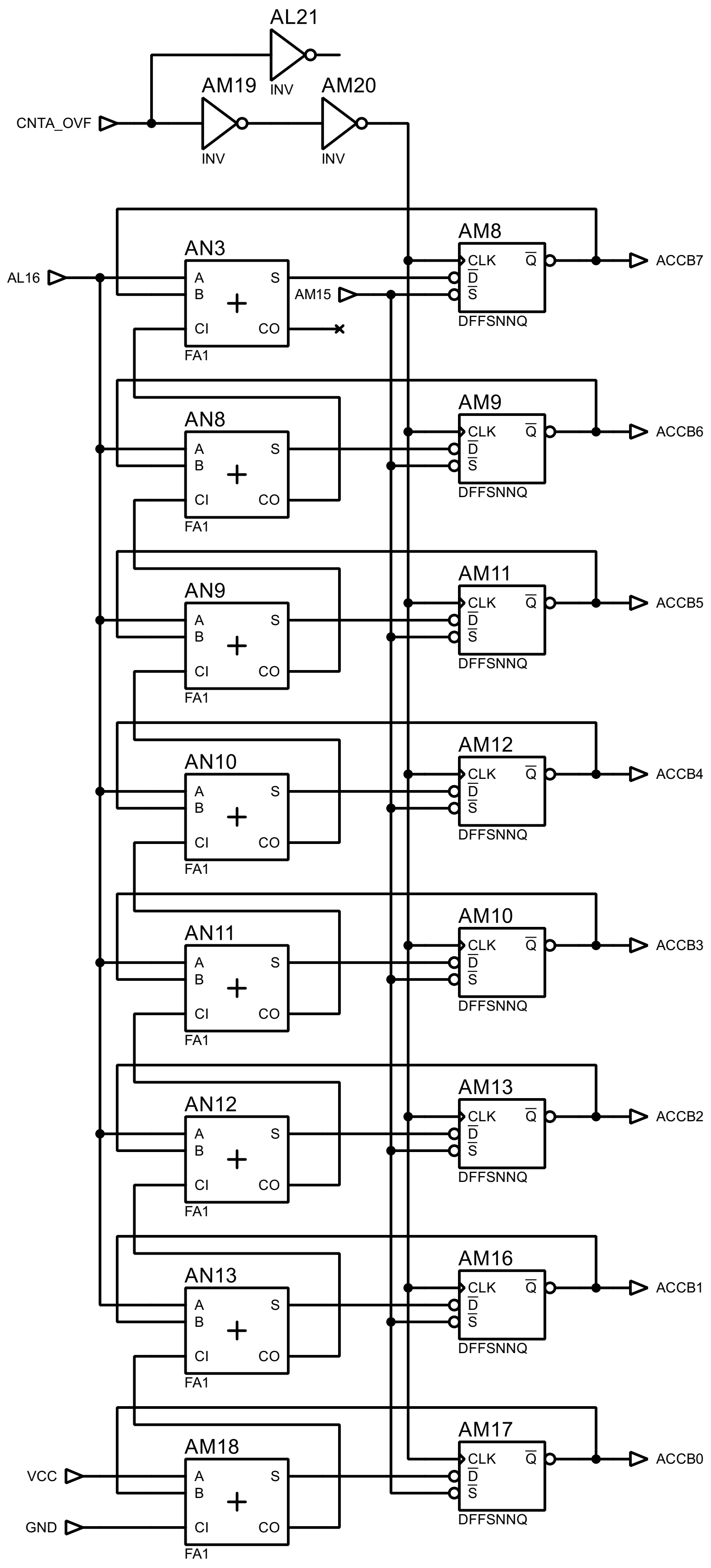
BY: Sean Gonsalves REV: @REV

DATE:
10/29/2025

PAGE:
46/53





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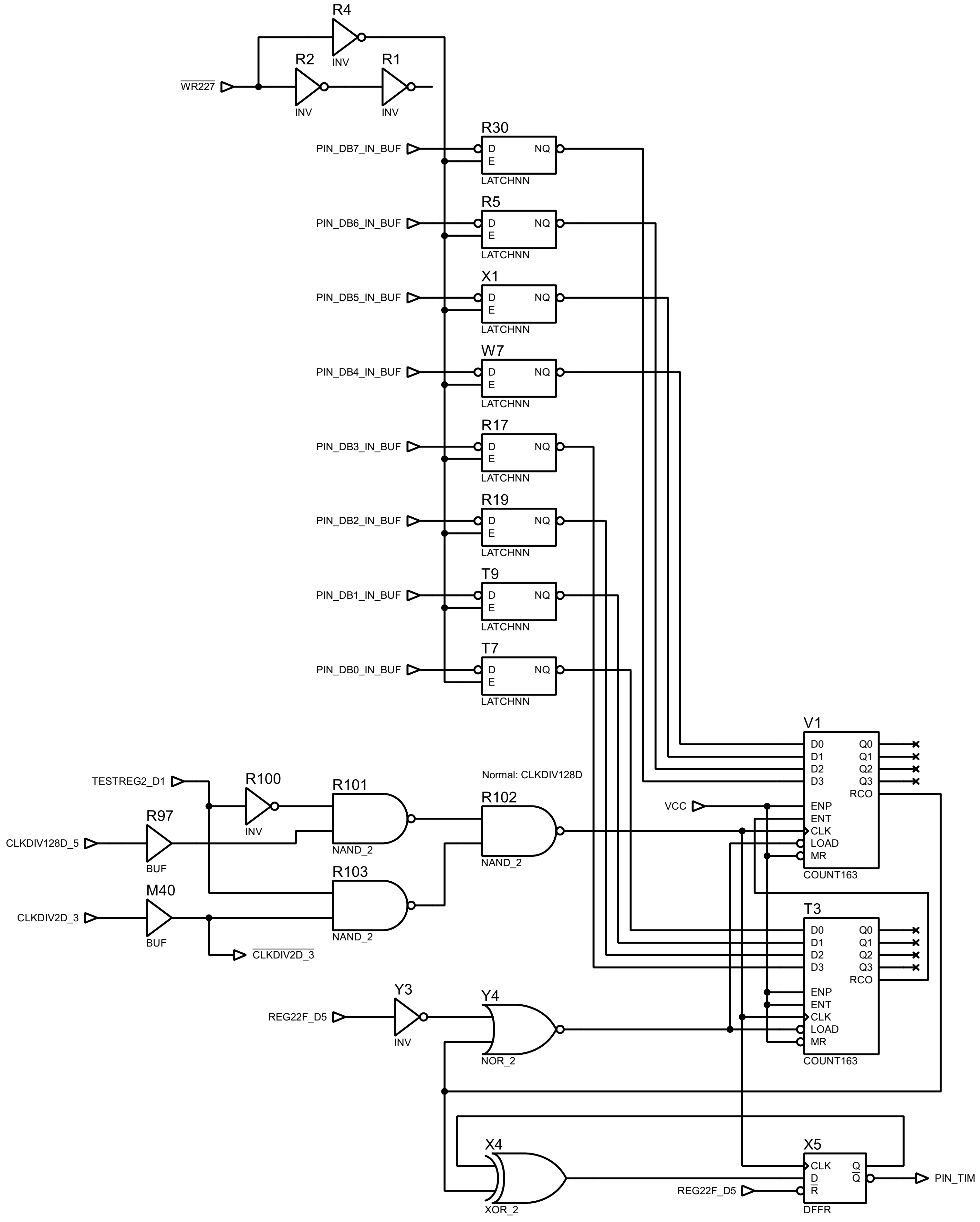
DATE:
10/29/2025

PAGE:
47/53

BY: Sean Gonsalves

REV: @REV



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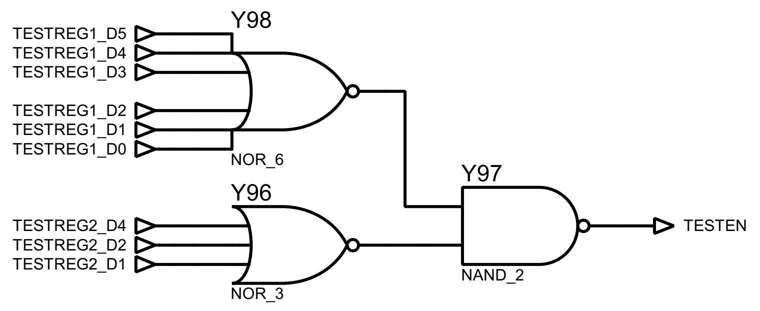
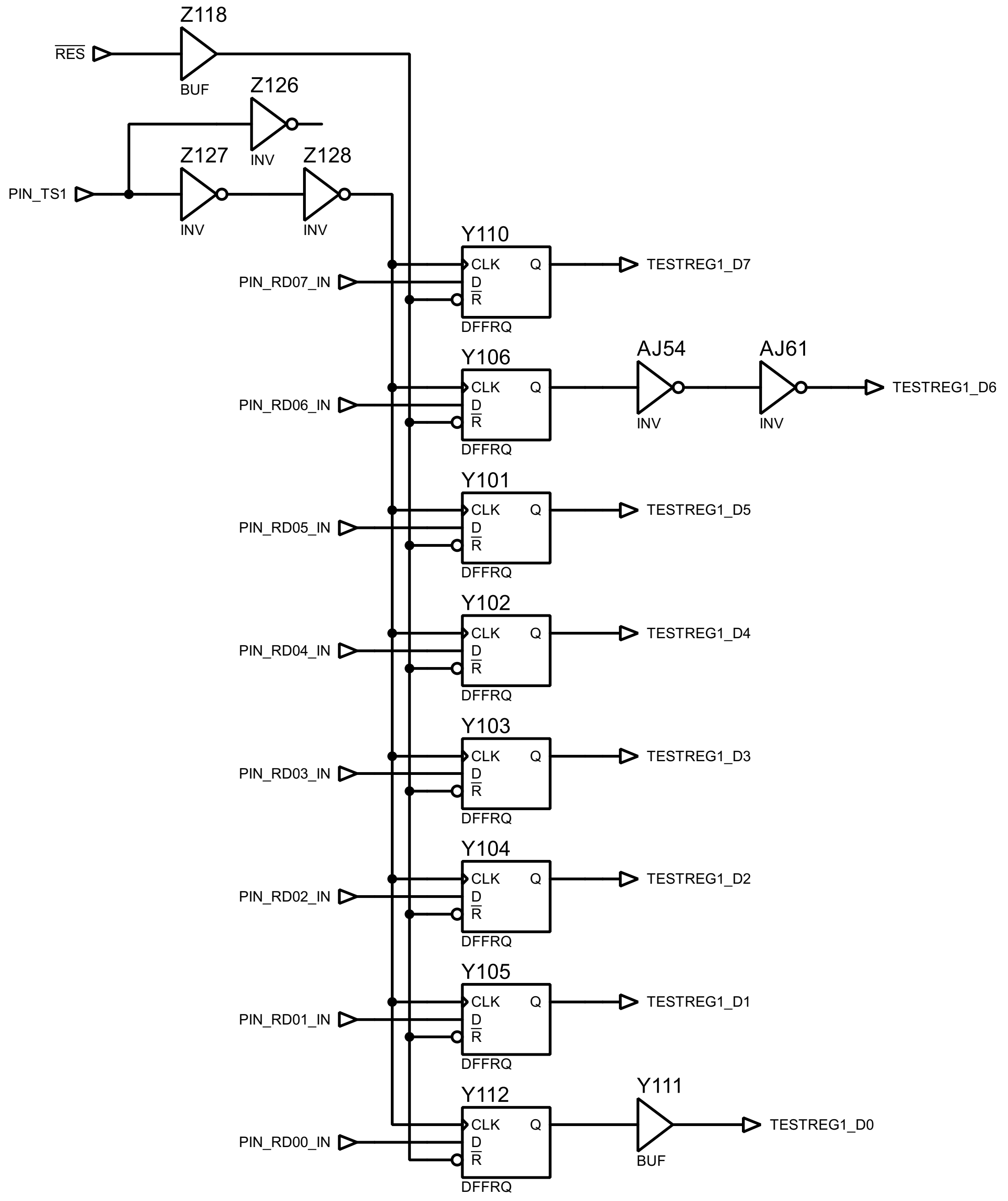
TIMER

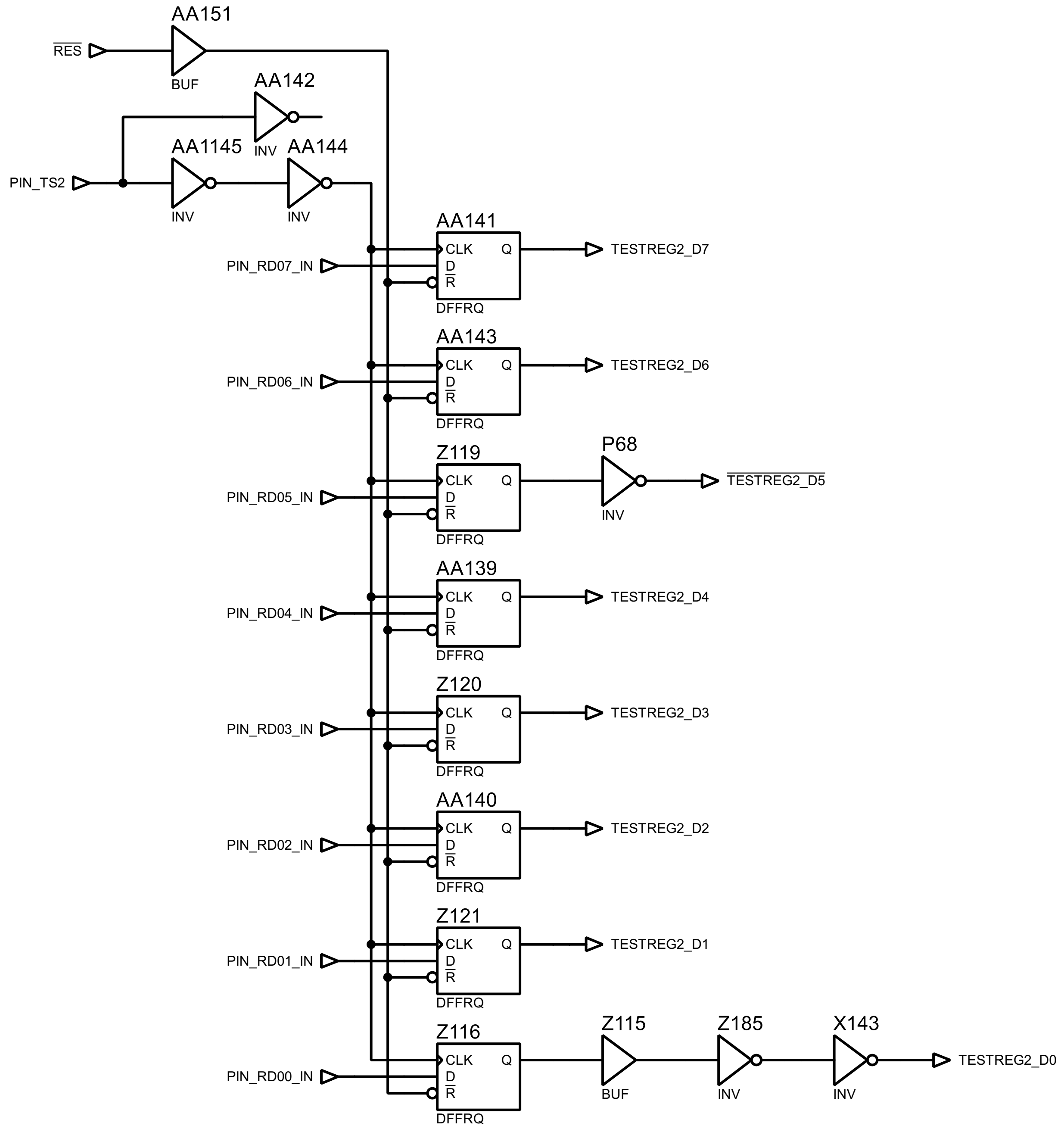
DATE:
10/29/2025

PAGE:
48/53

BY: Sean Gonsalves REV: @REV







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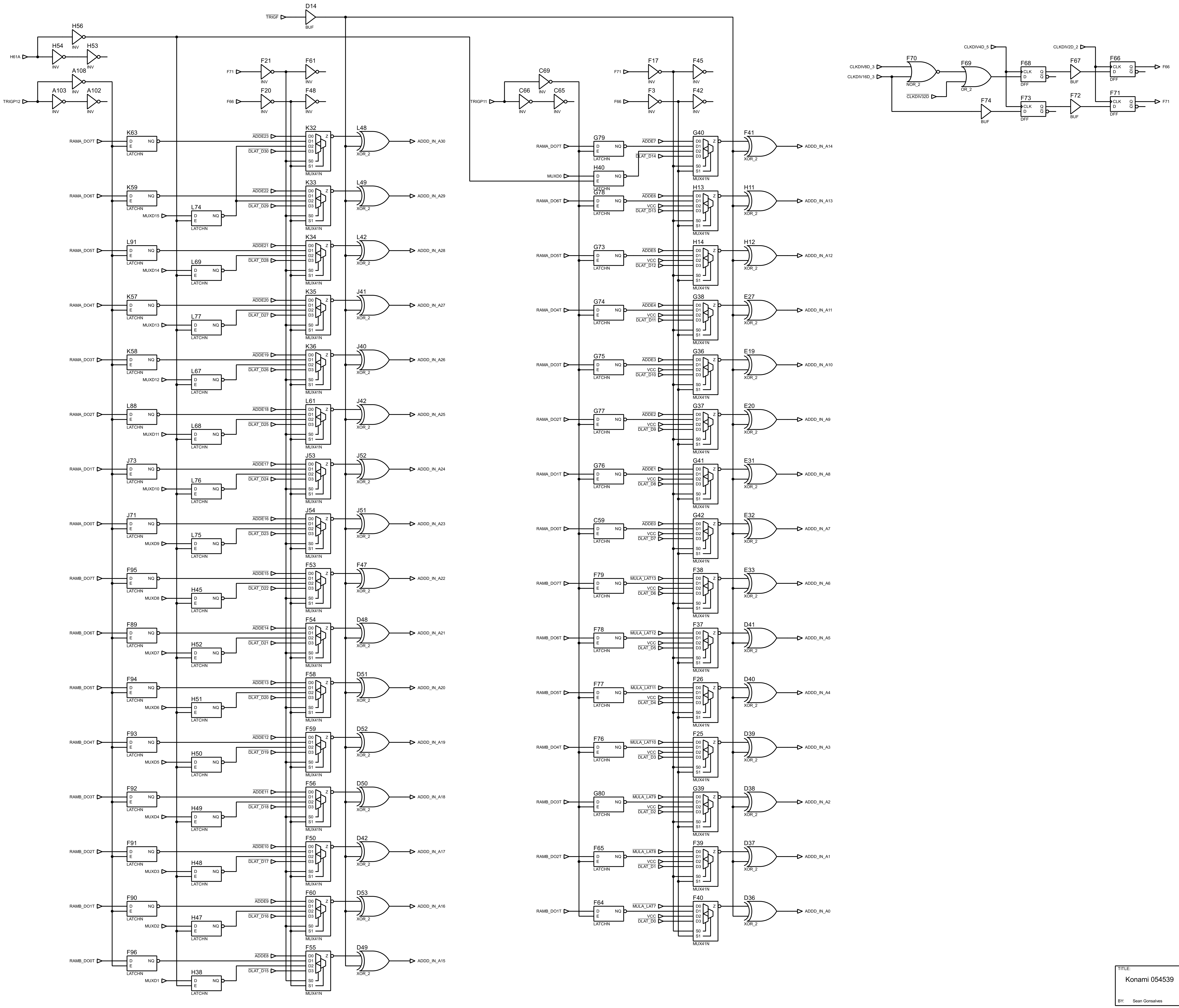
	PAGE:
BY: Sean Gonsalves REV: @REV	49/53



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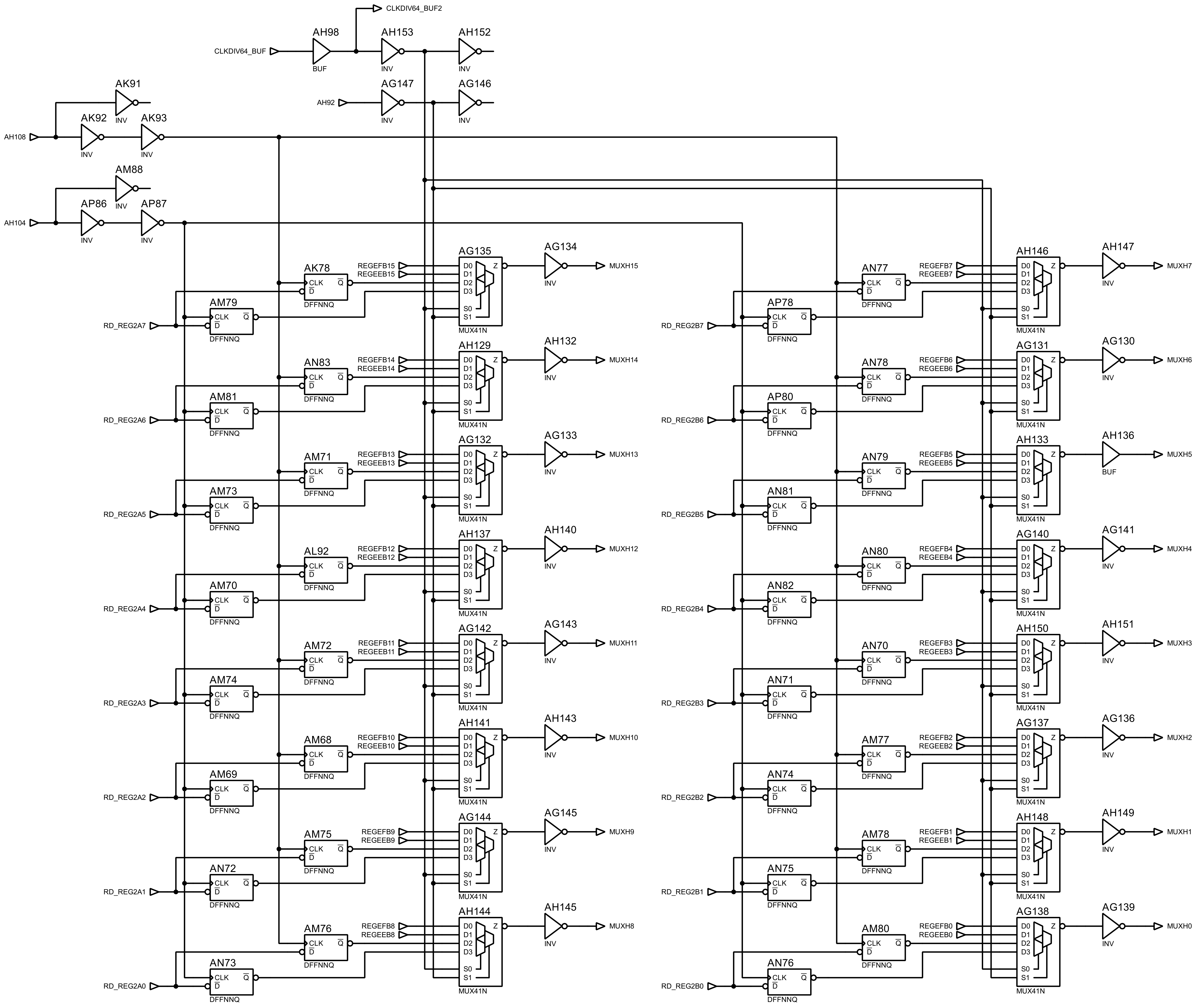
DATE:
10/29/2025

PAGE:
50/53

BY: Sean Gonsalves

REV: @REV



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DATE:
10/29/2025

PAGE:
51/53

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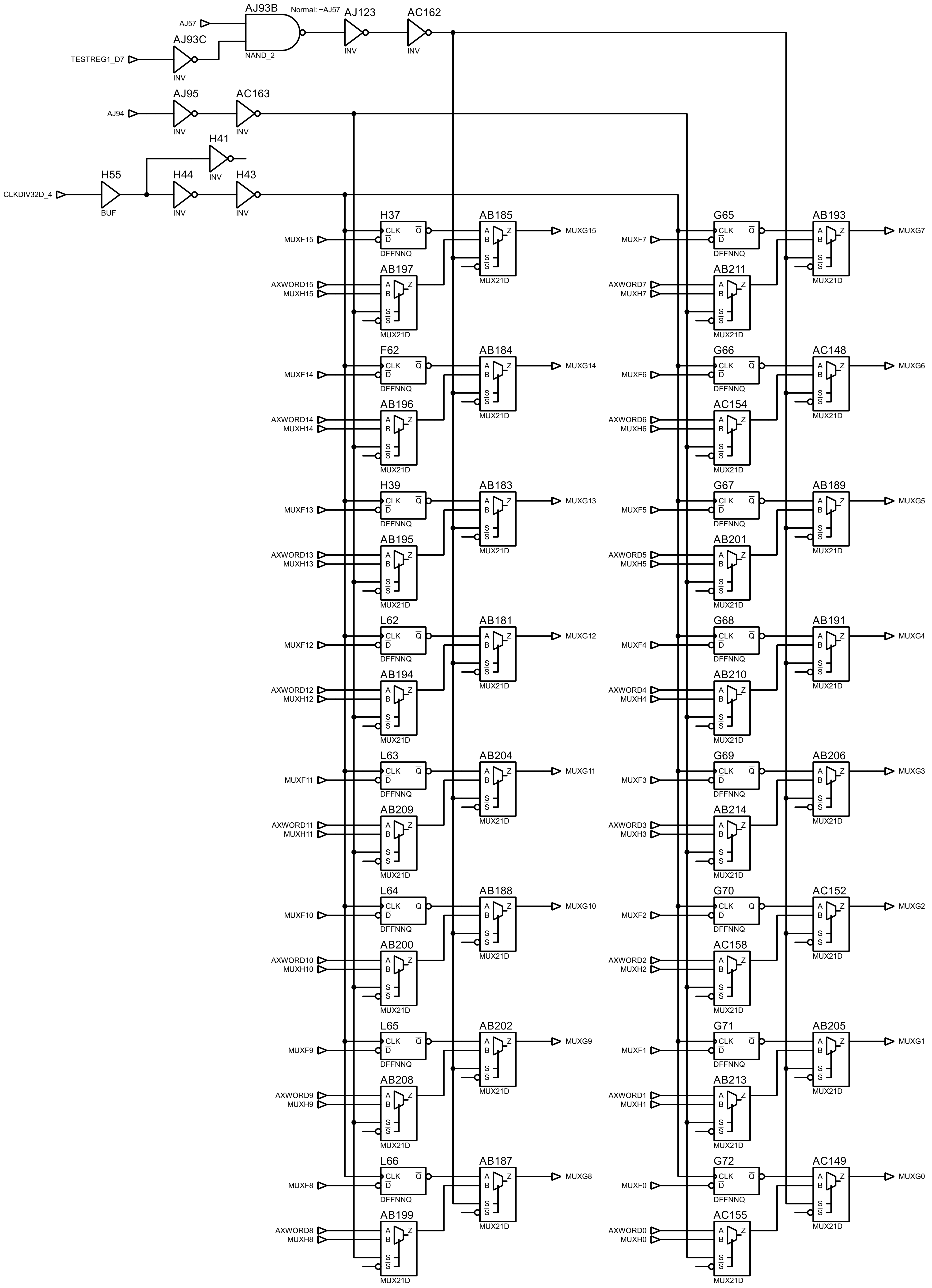

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10/29/2025

PAGE:
52/53

BY: Sean Gonsalves
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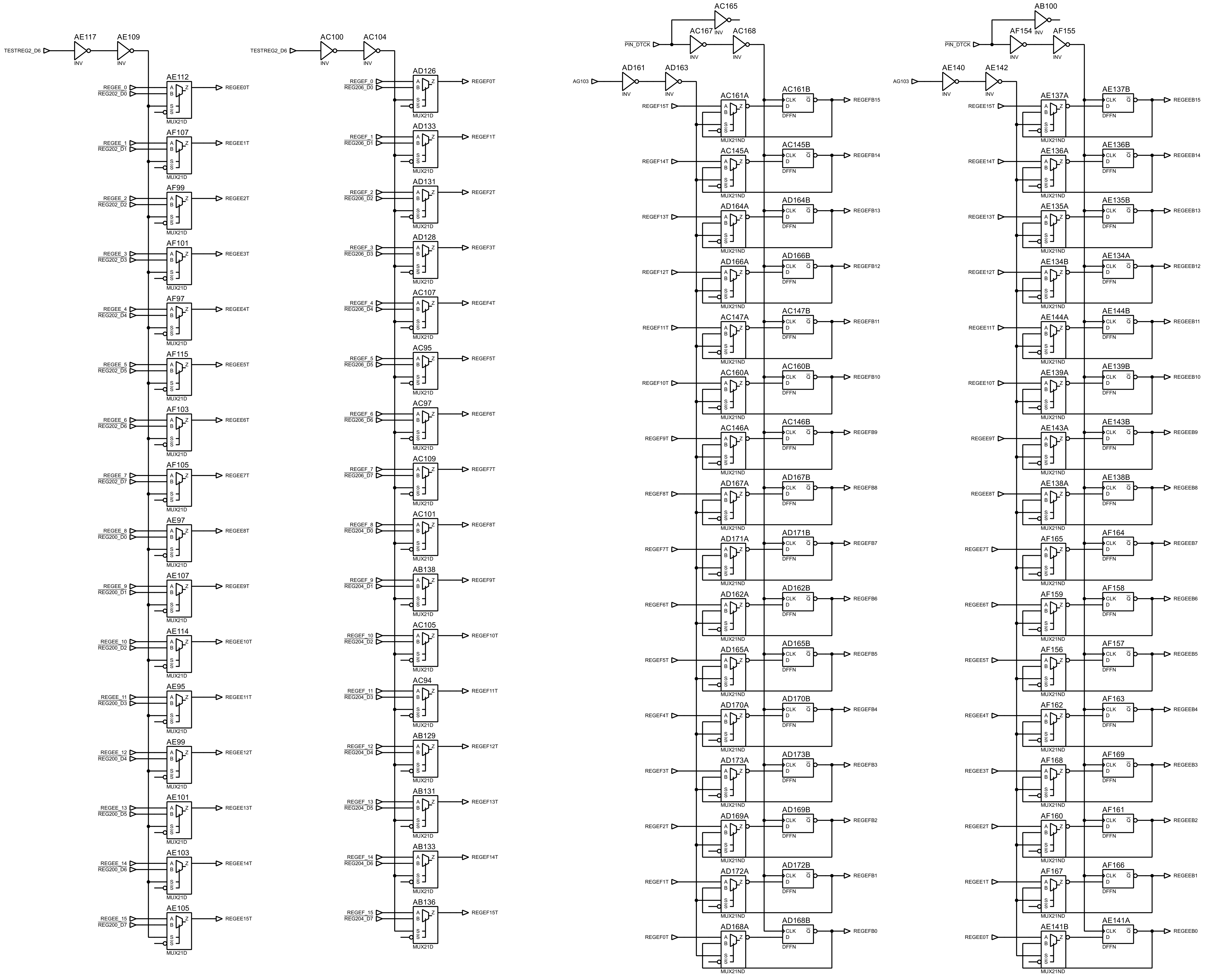

TITLE:
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DATE:
10/29/2025

PAGE:
53/53

BY: Sean Gonsalves

REV: @REV