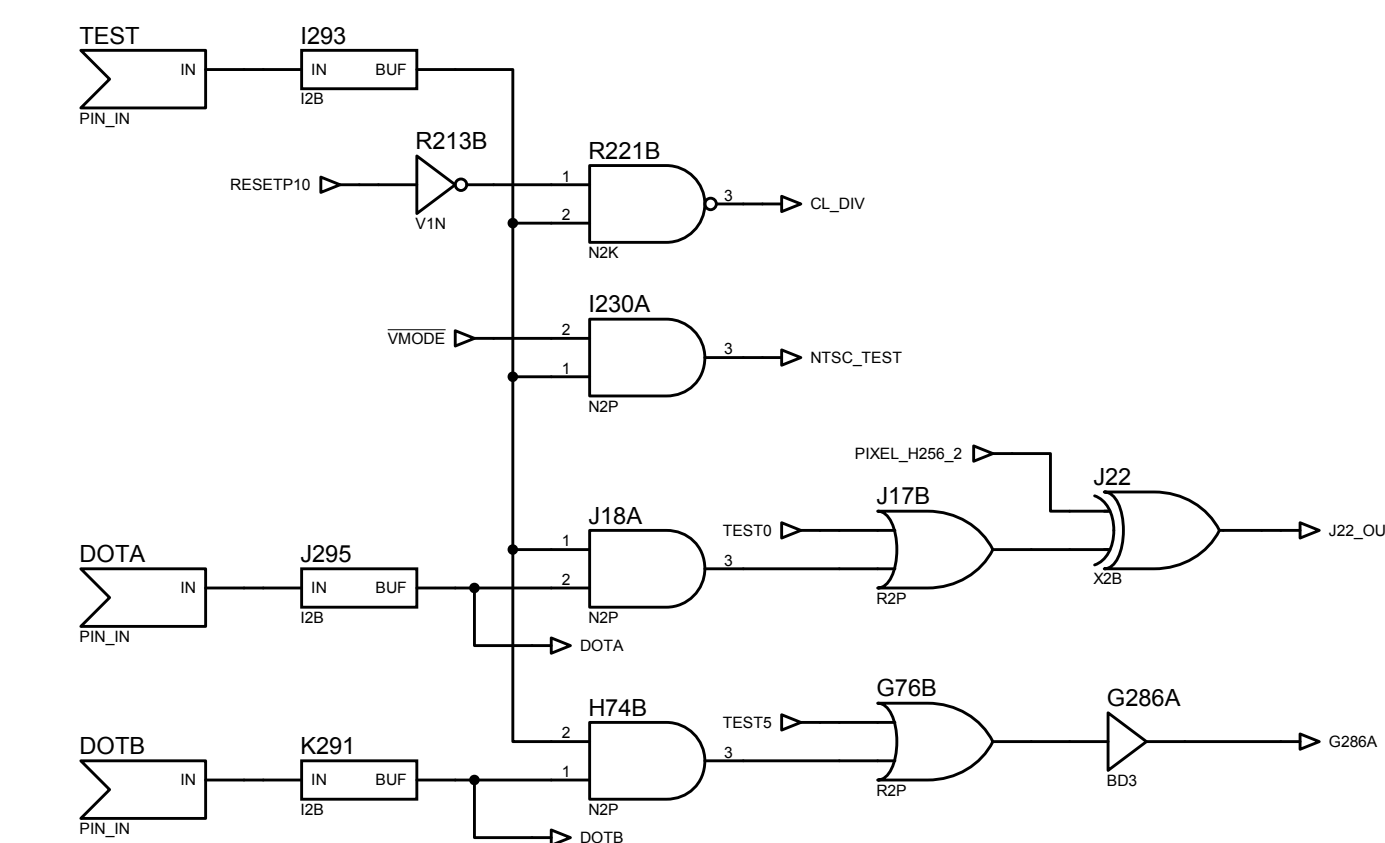
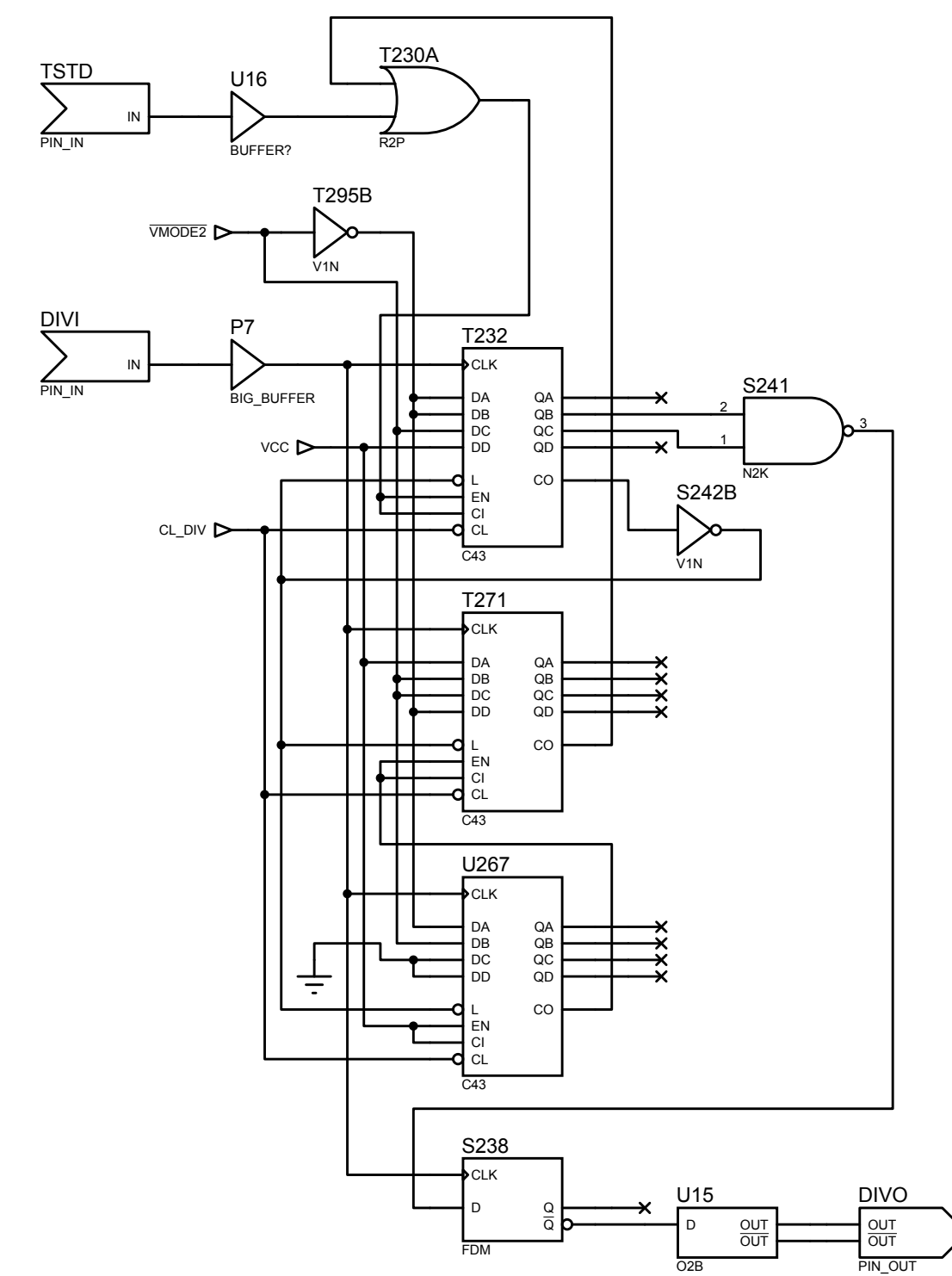
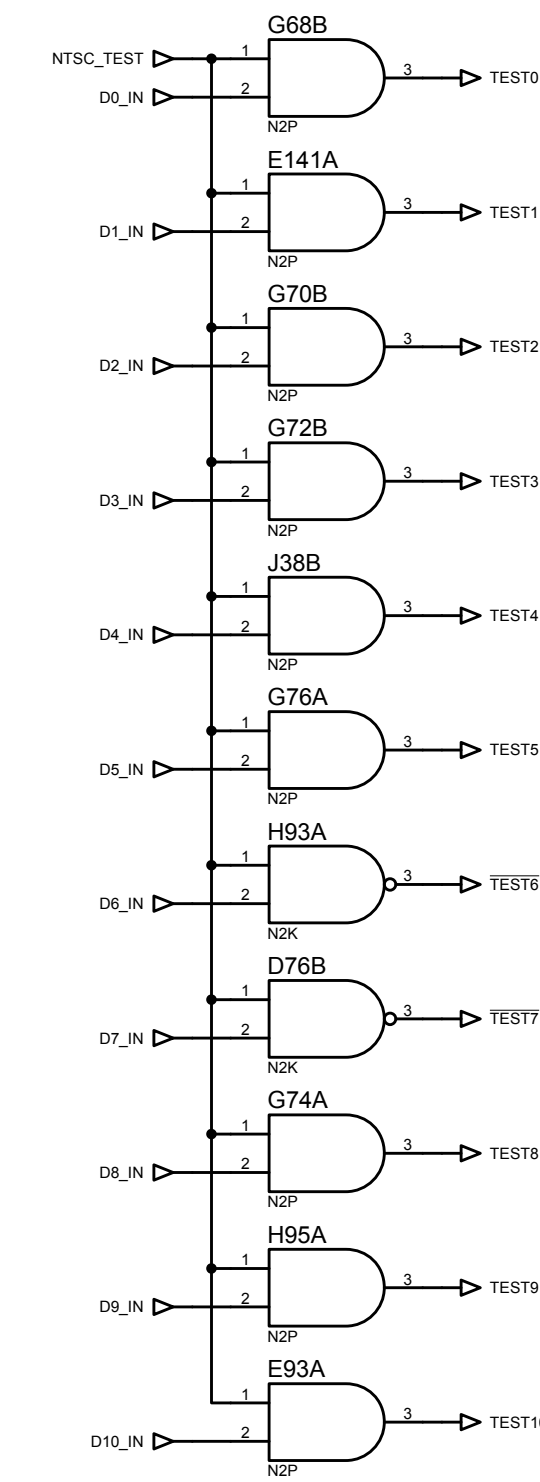
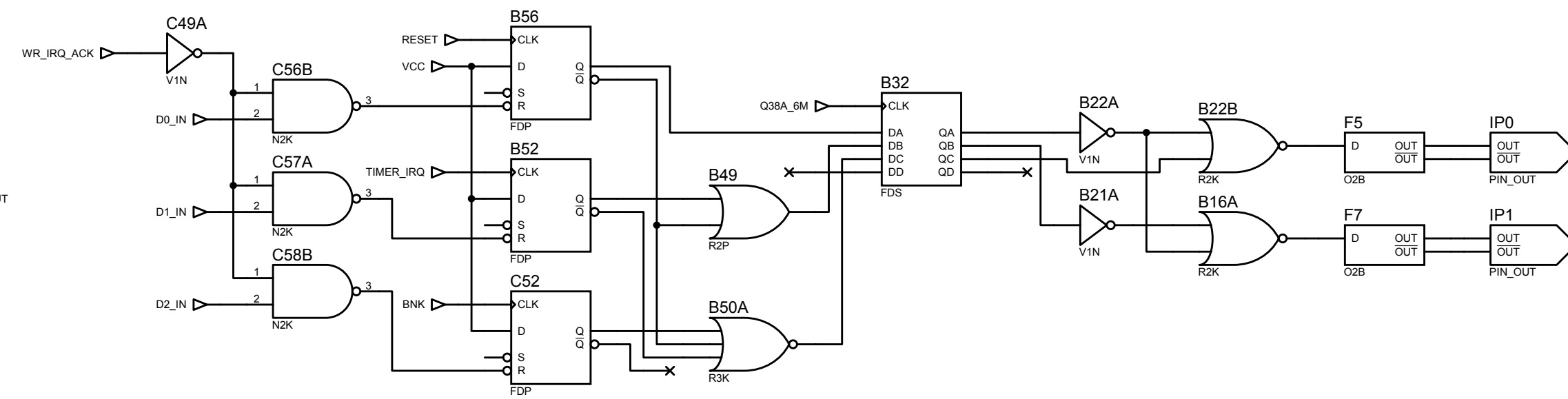


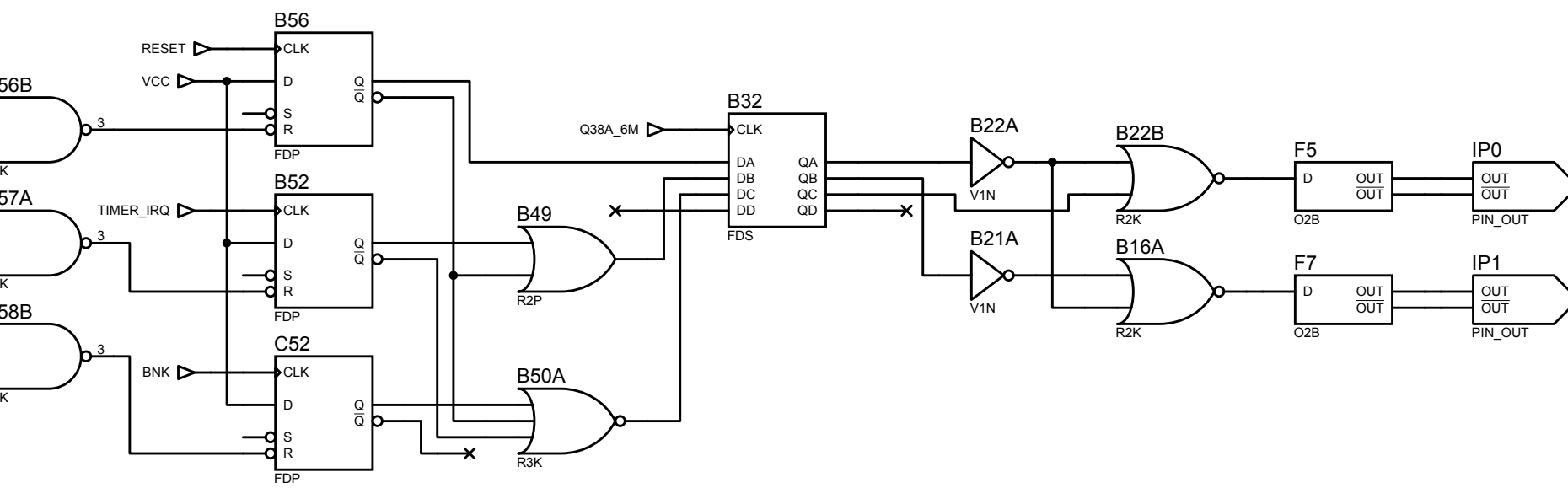
Test enables



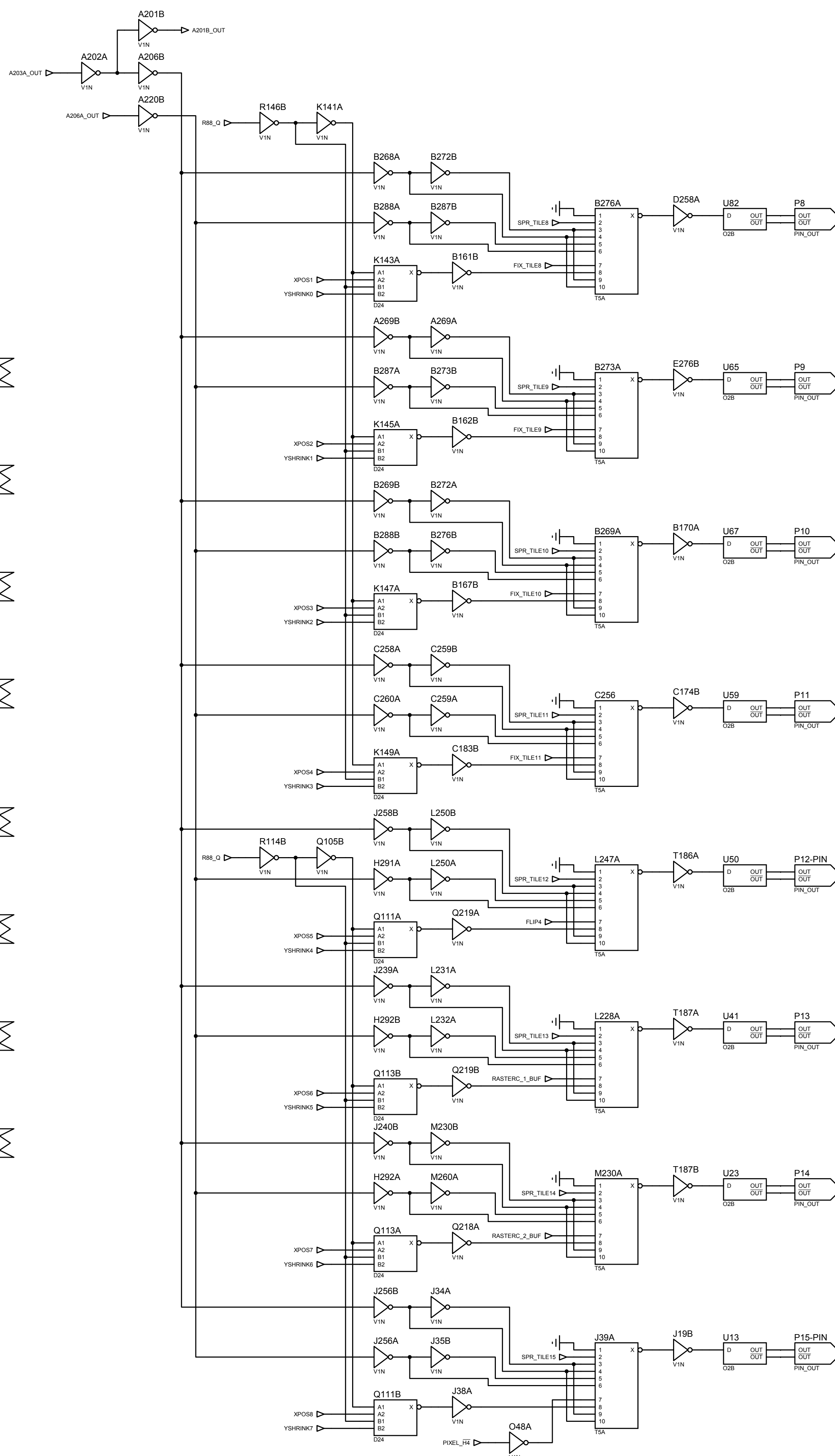
REG_IRQACK

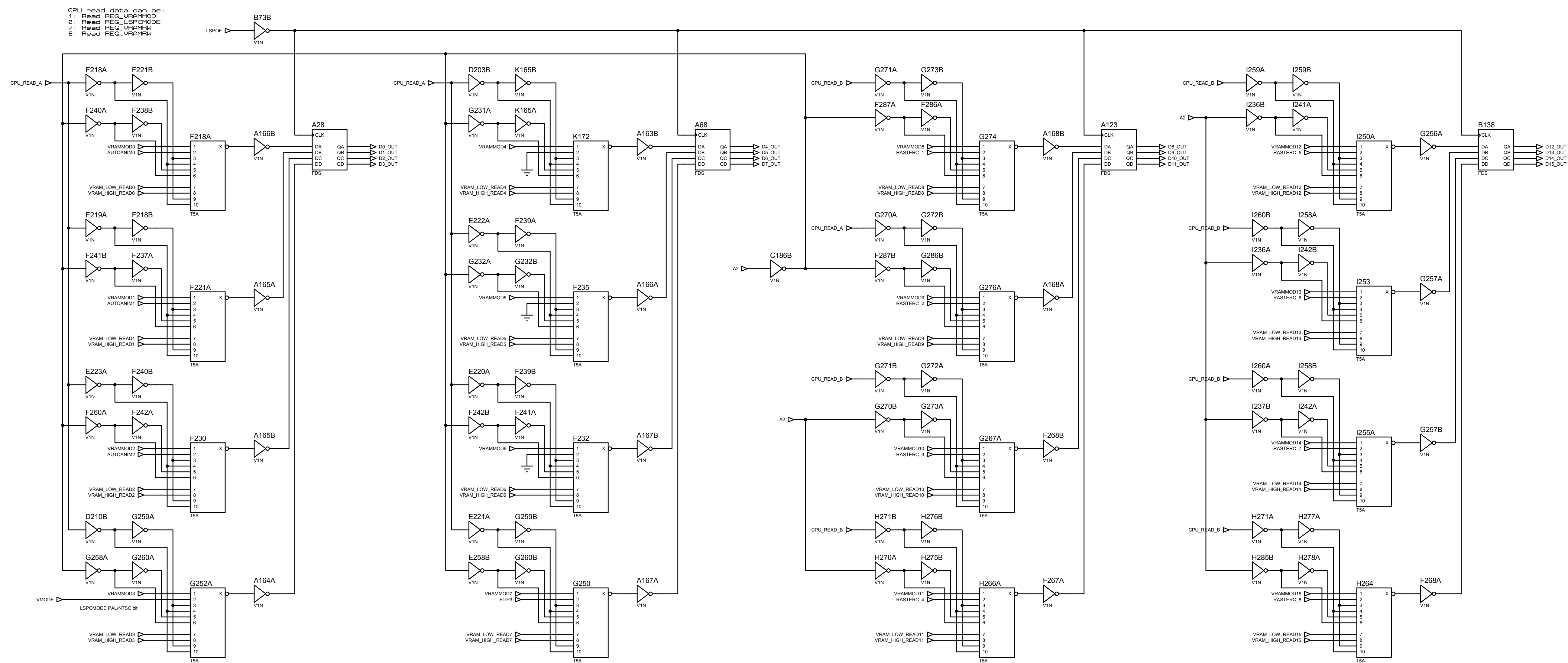


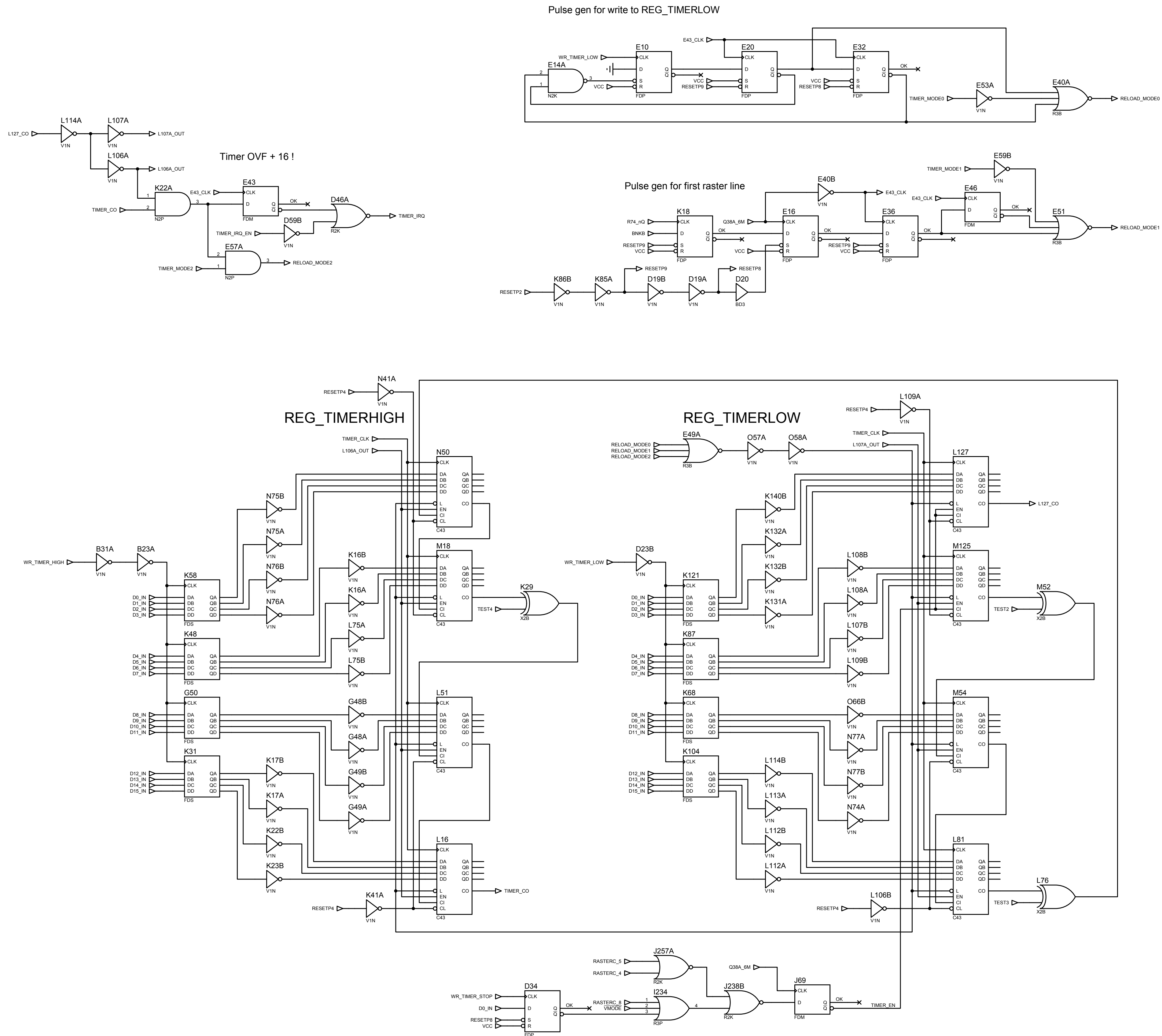
Actually FD3s or FD5s



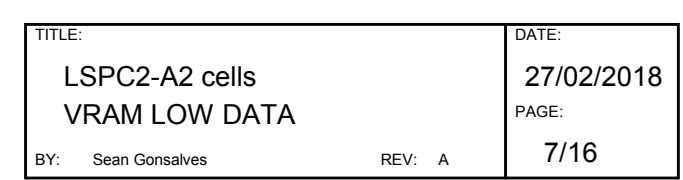
FILE:	LSPC2-A2 cells	DATE:	27/02/2018
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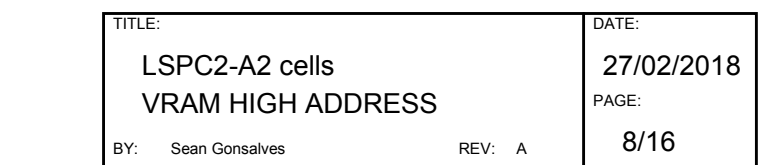


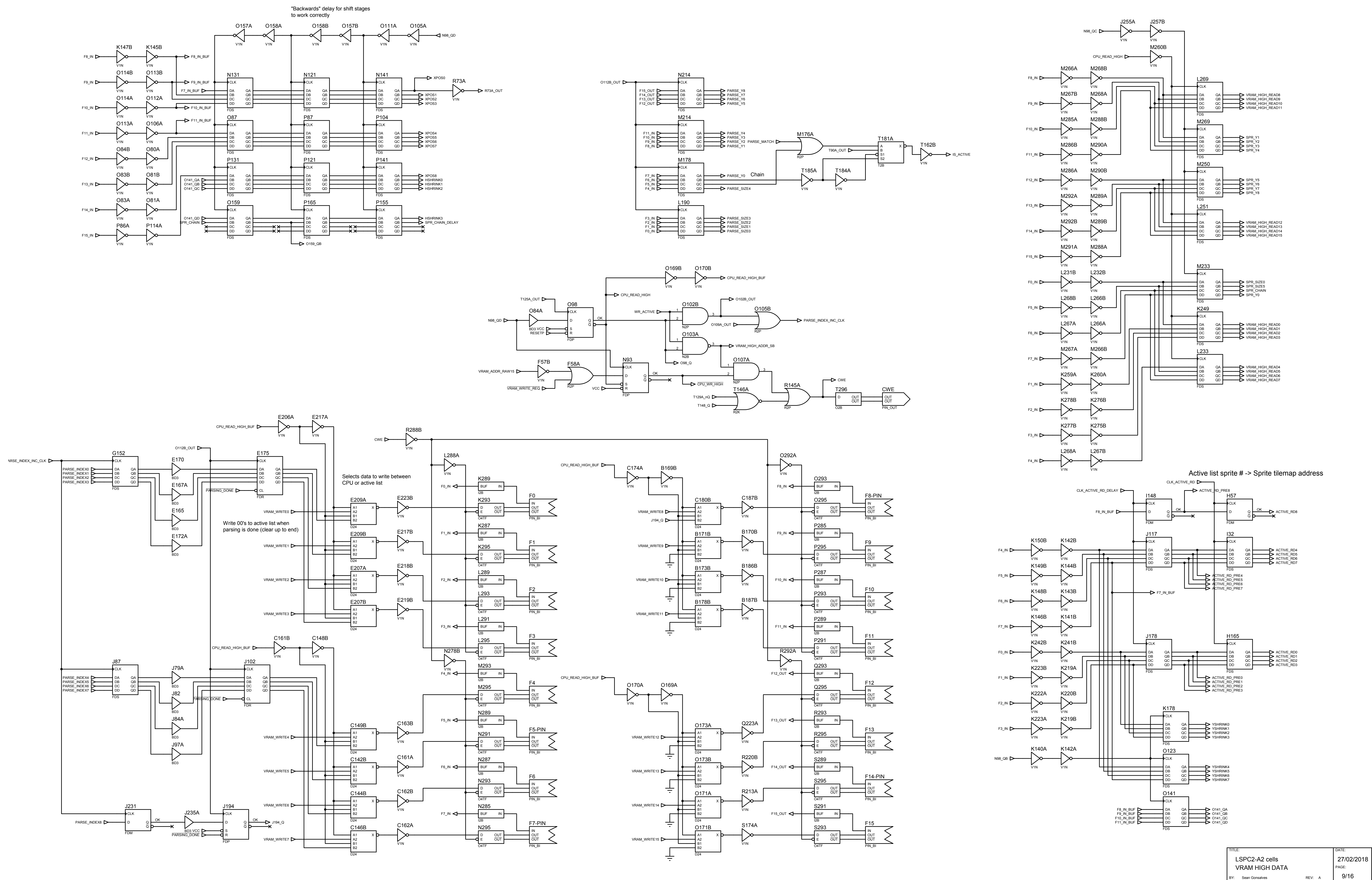


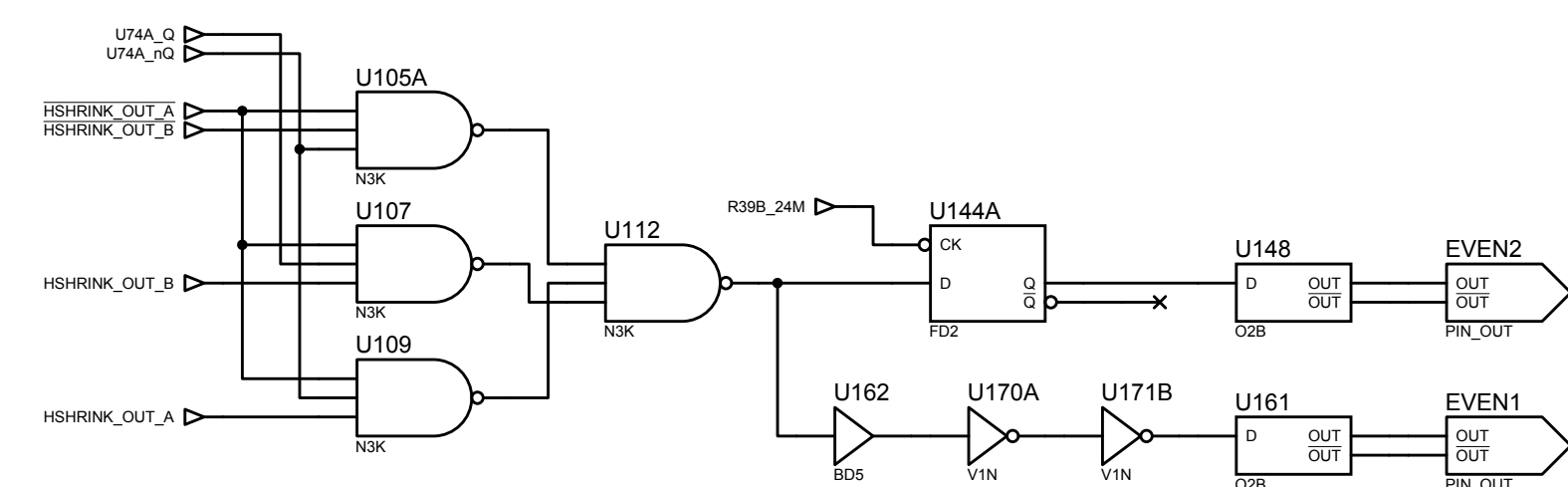
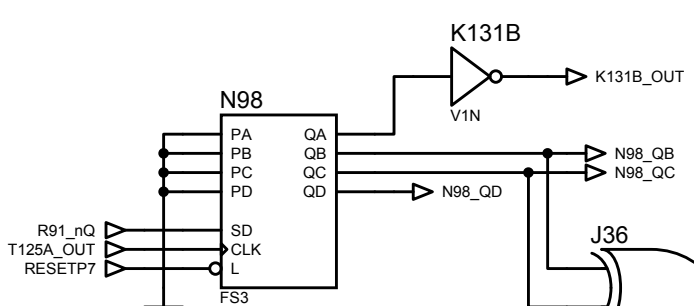
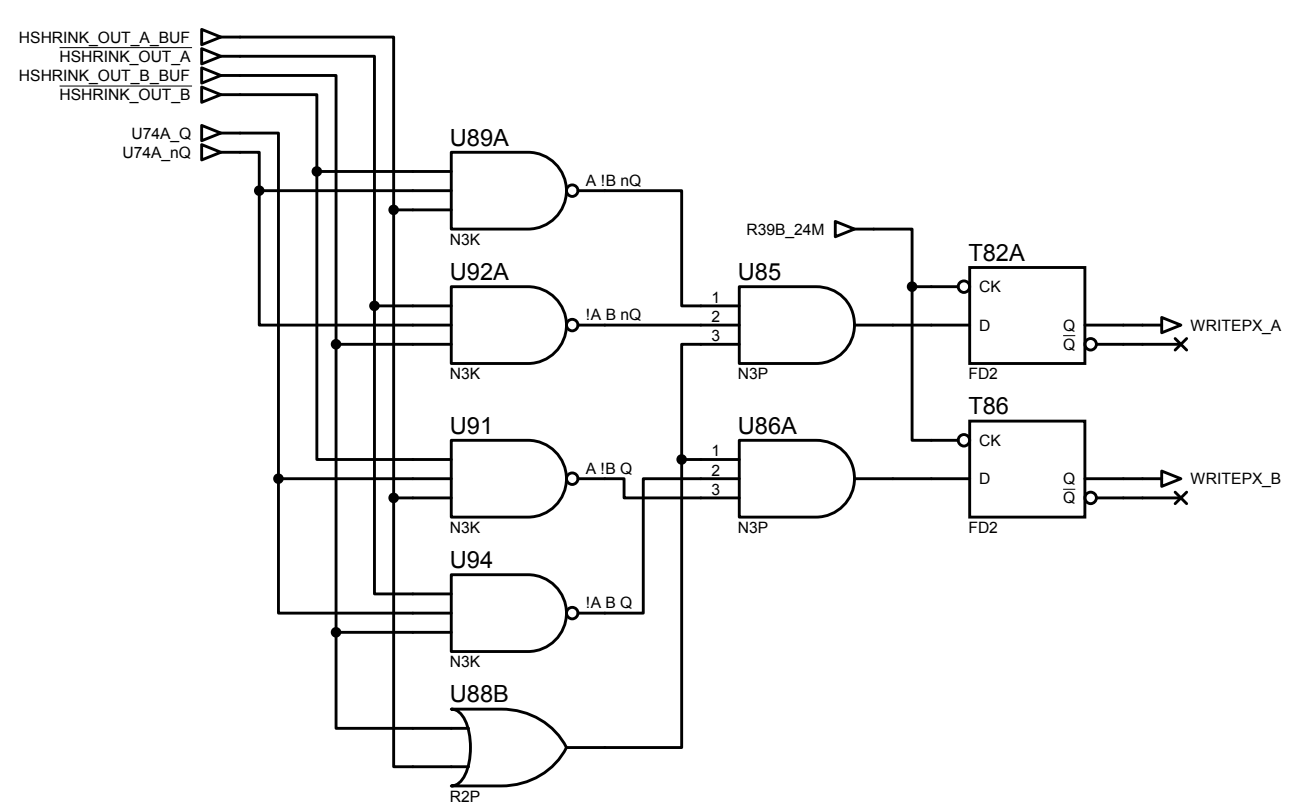
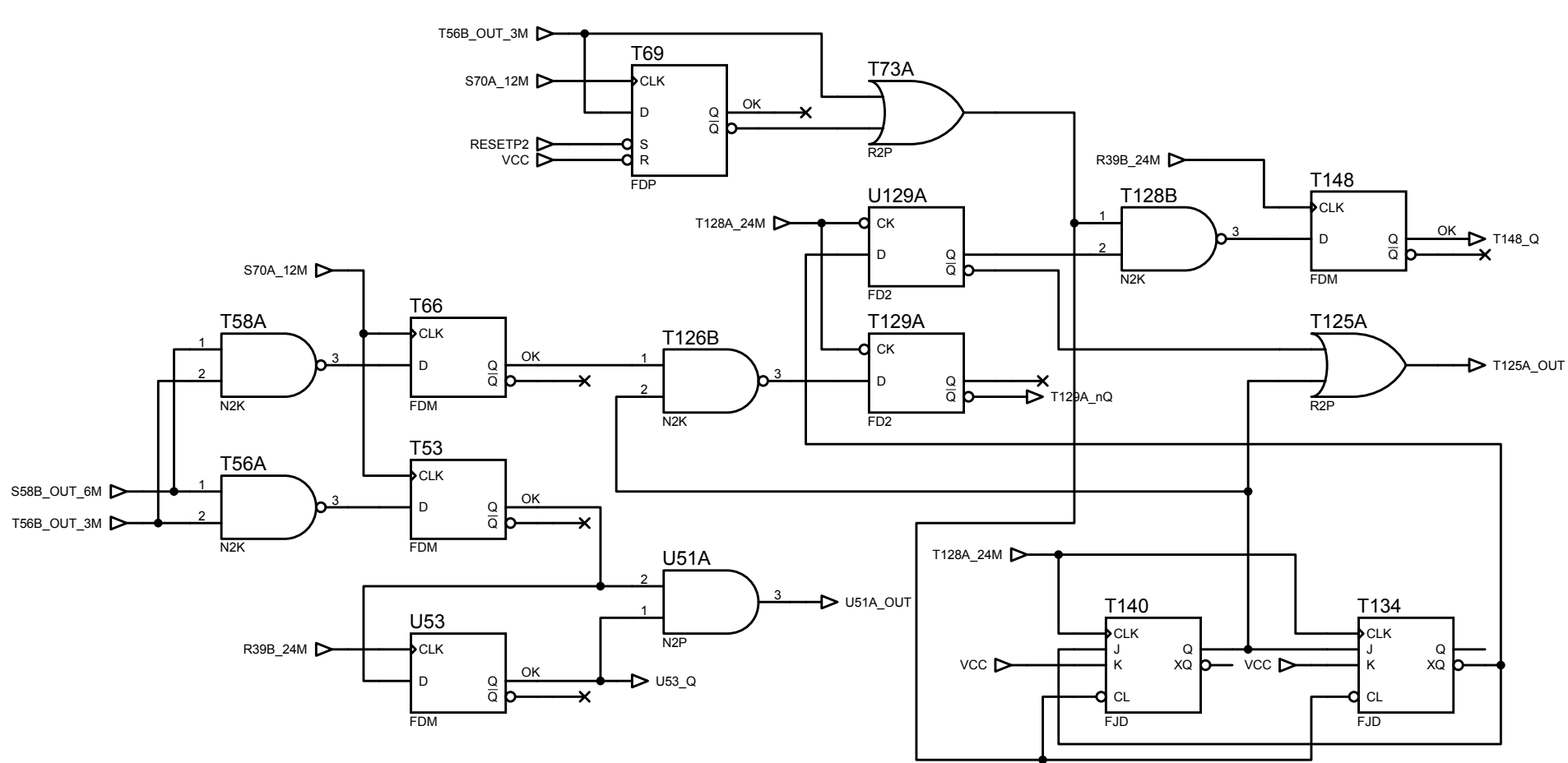


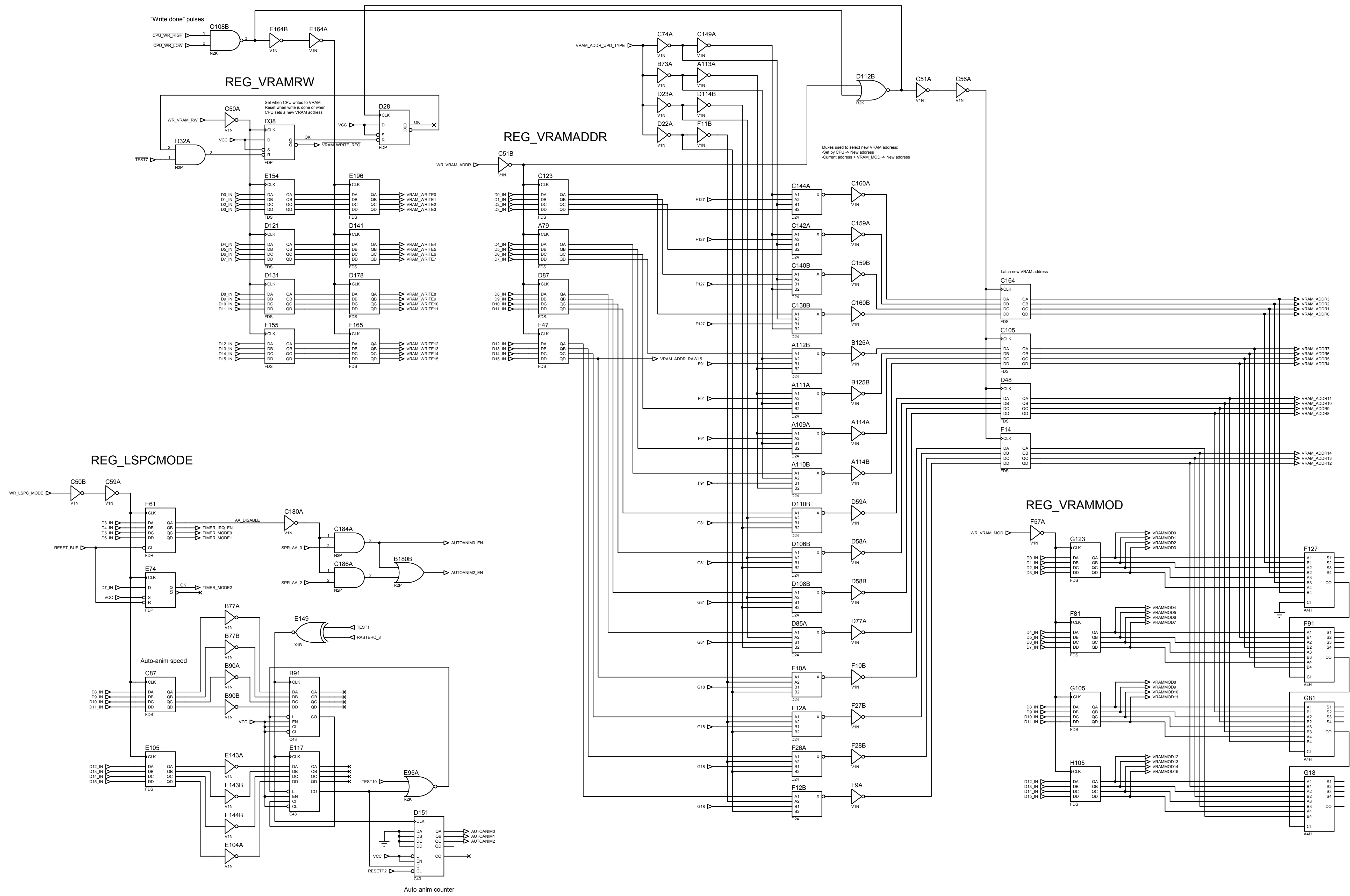


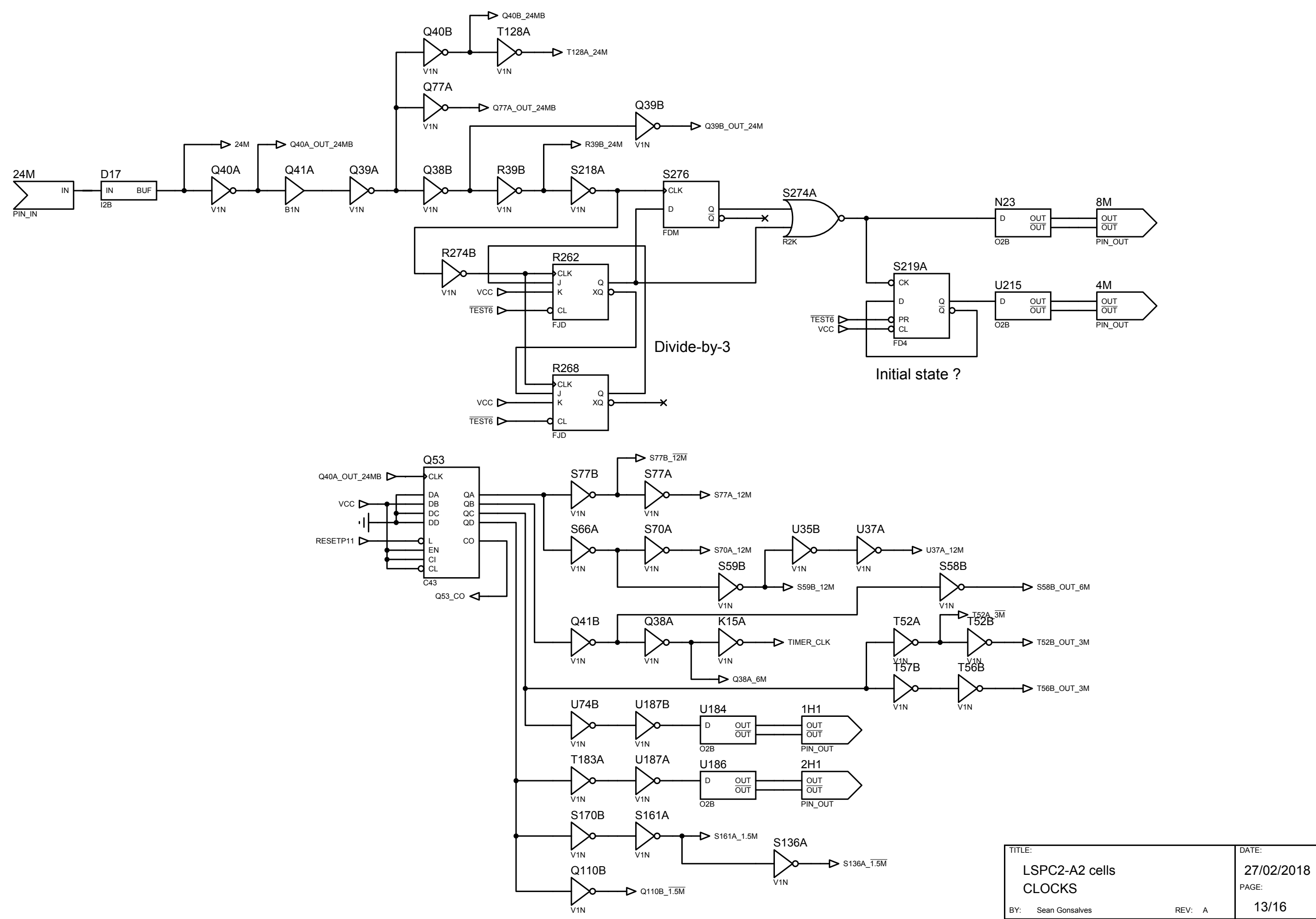


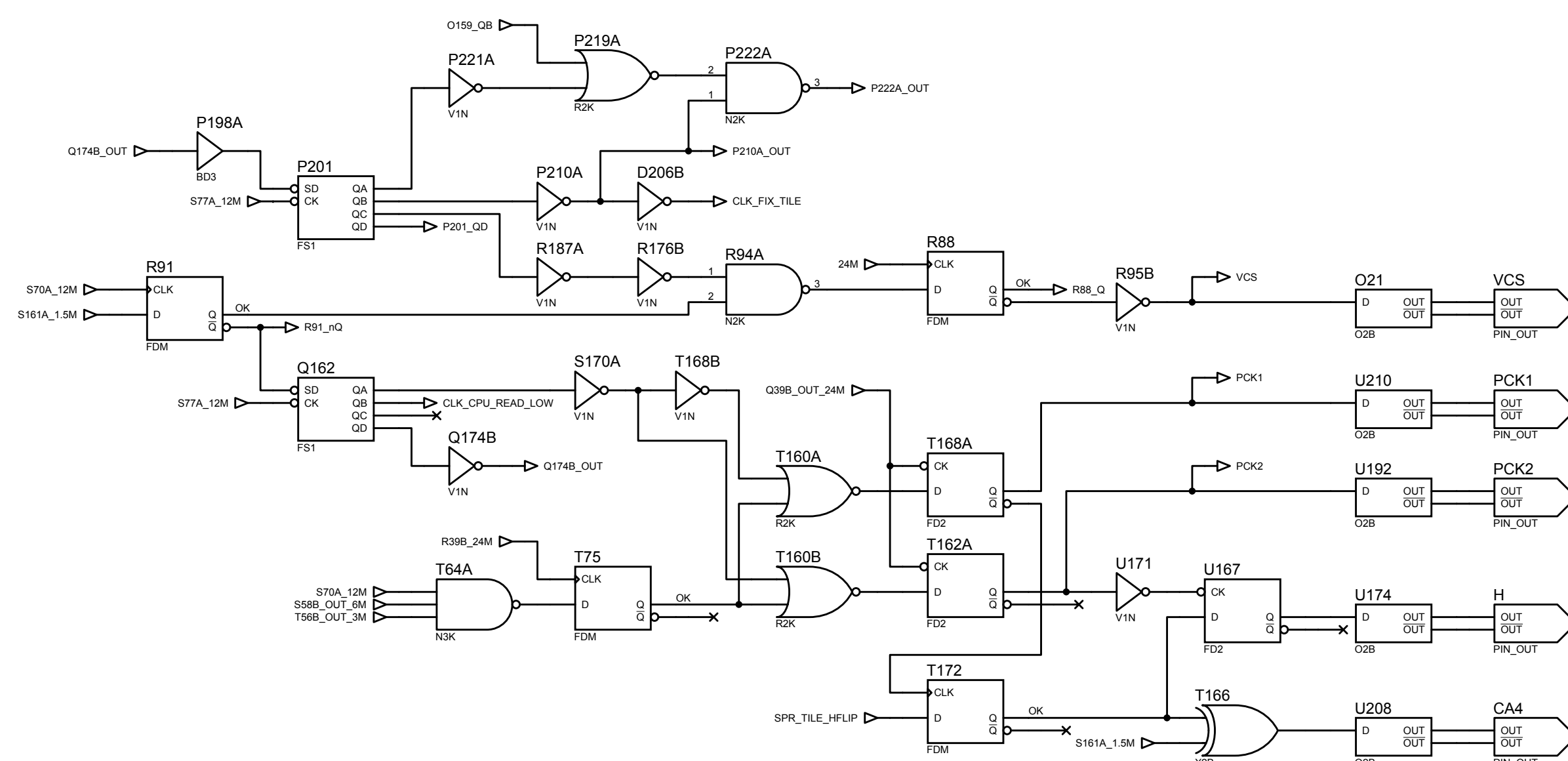
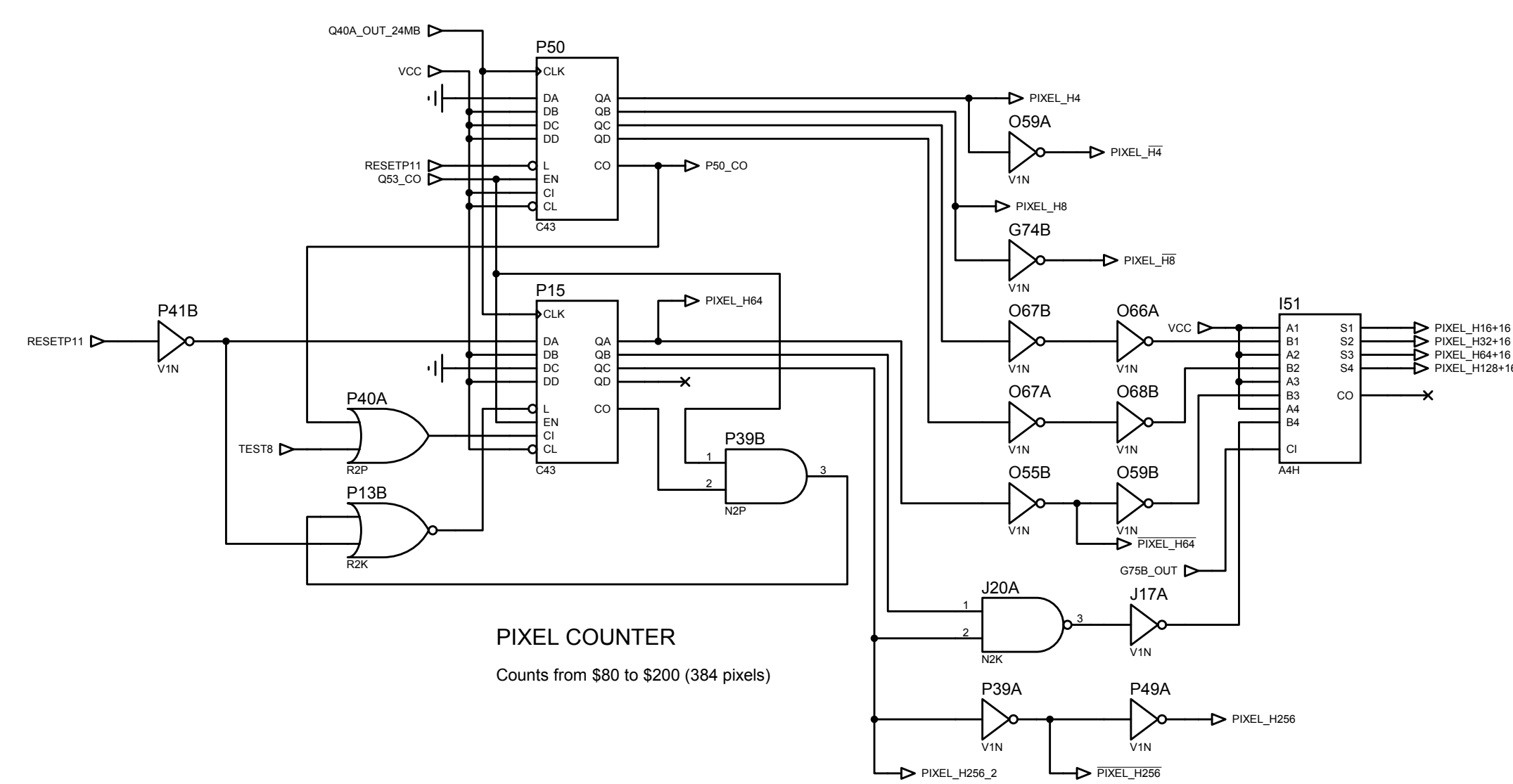
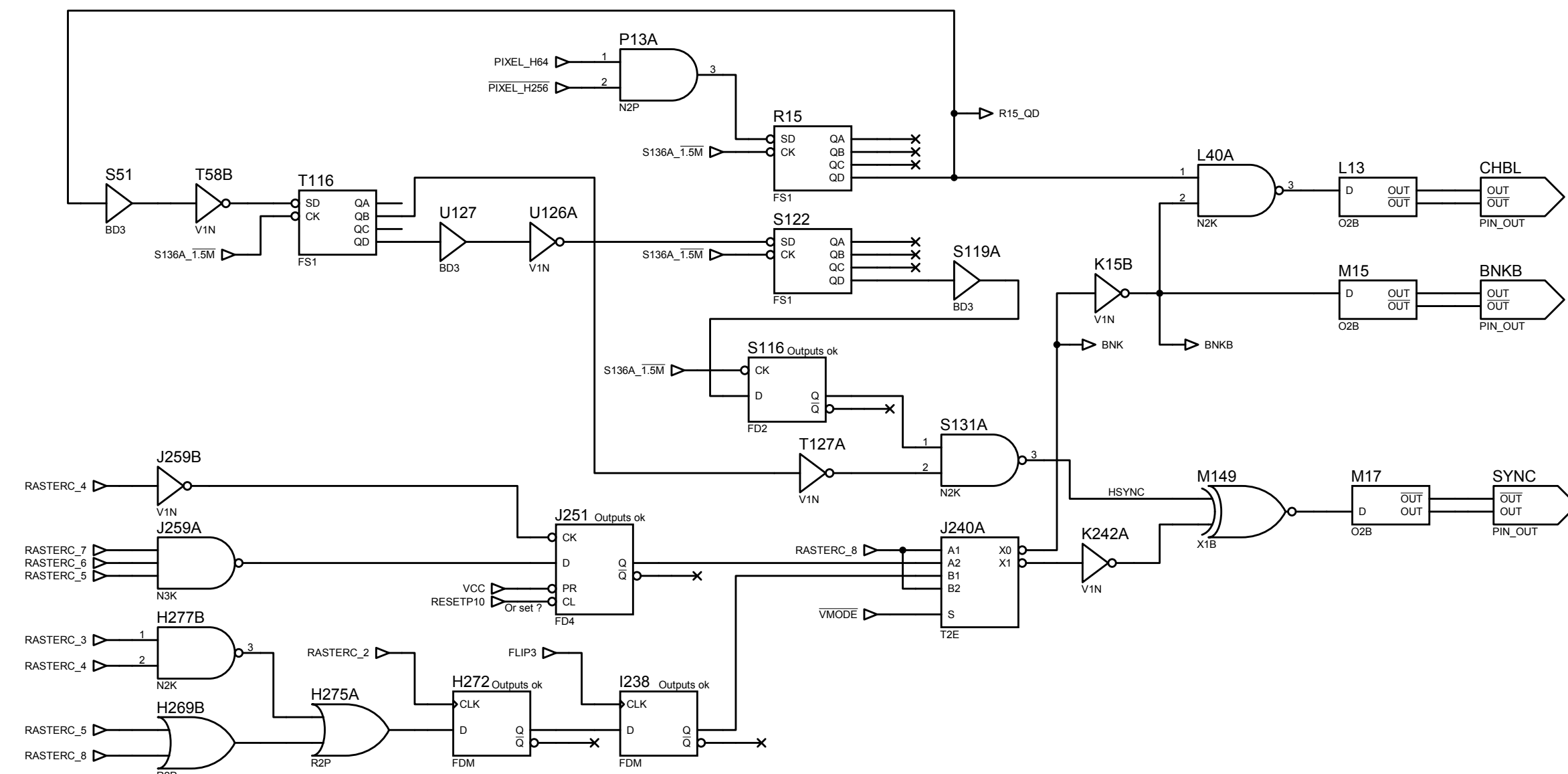


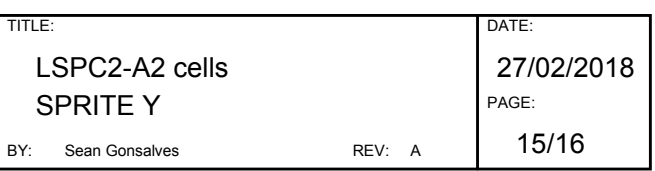




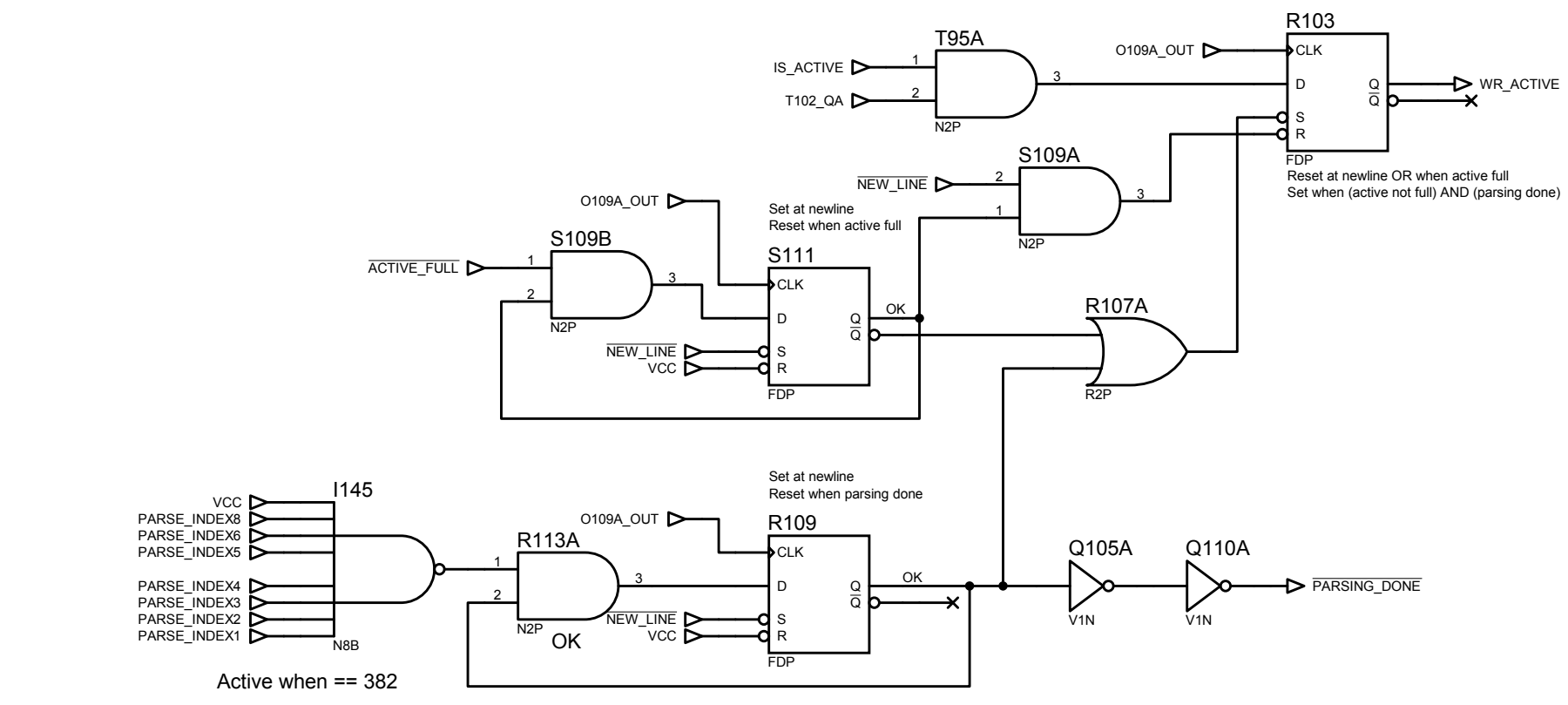




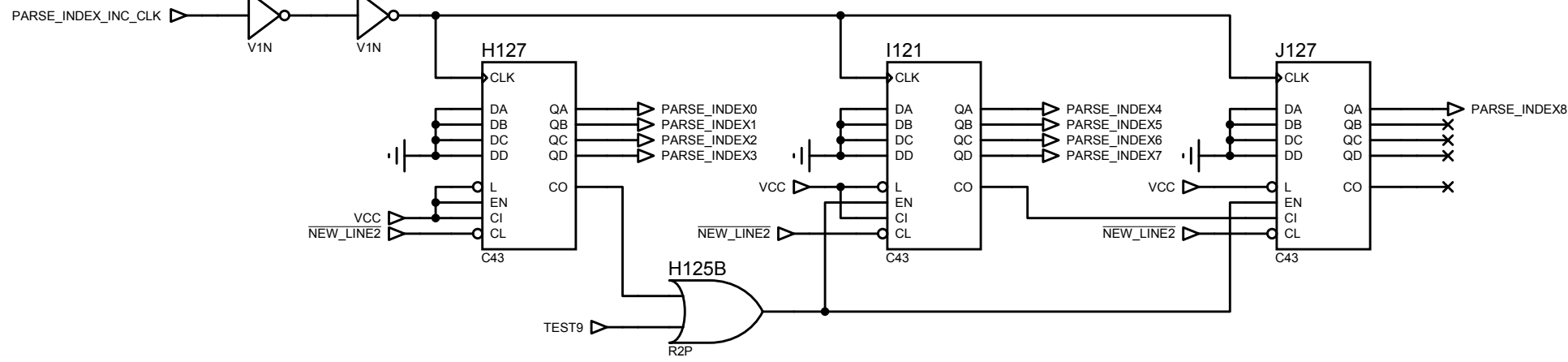




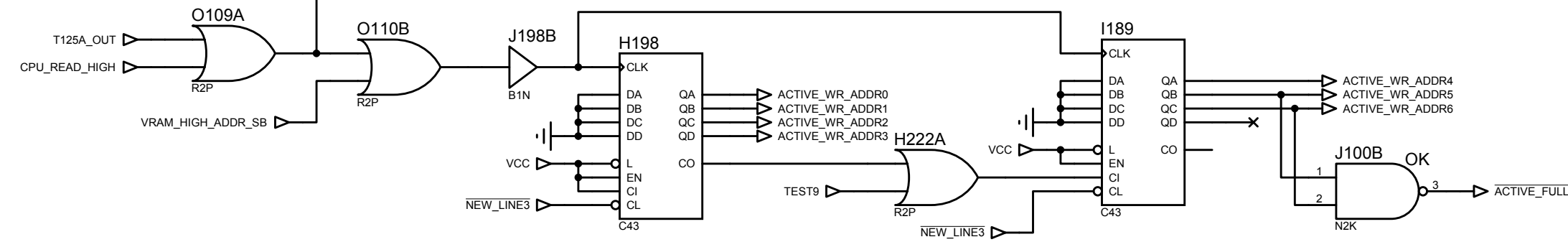
Parsing state control



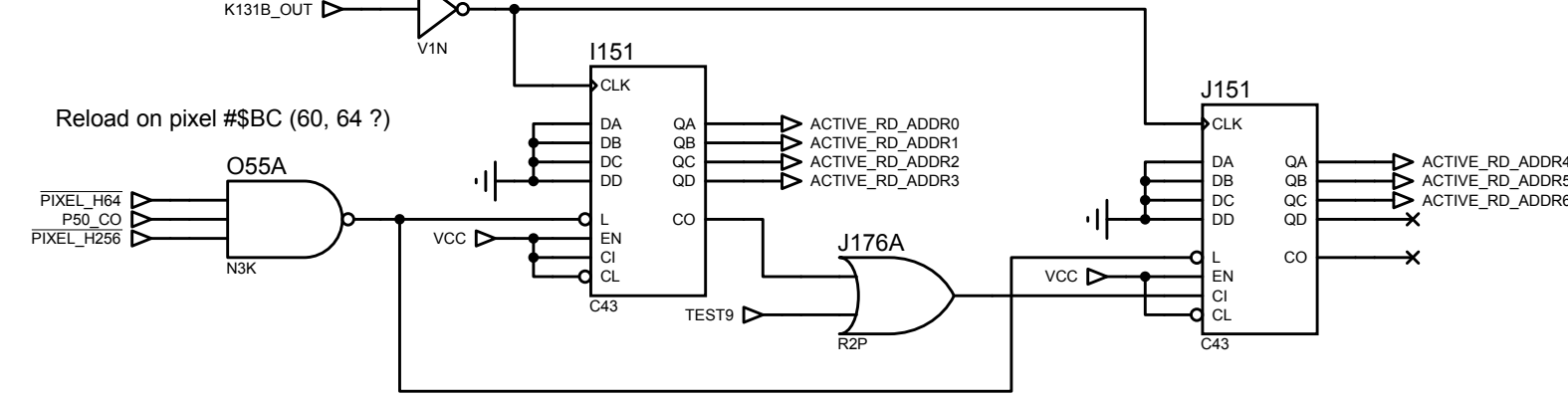
Parsing address counter



Active list write counter



Active list read counter



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