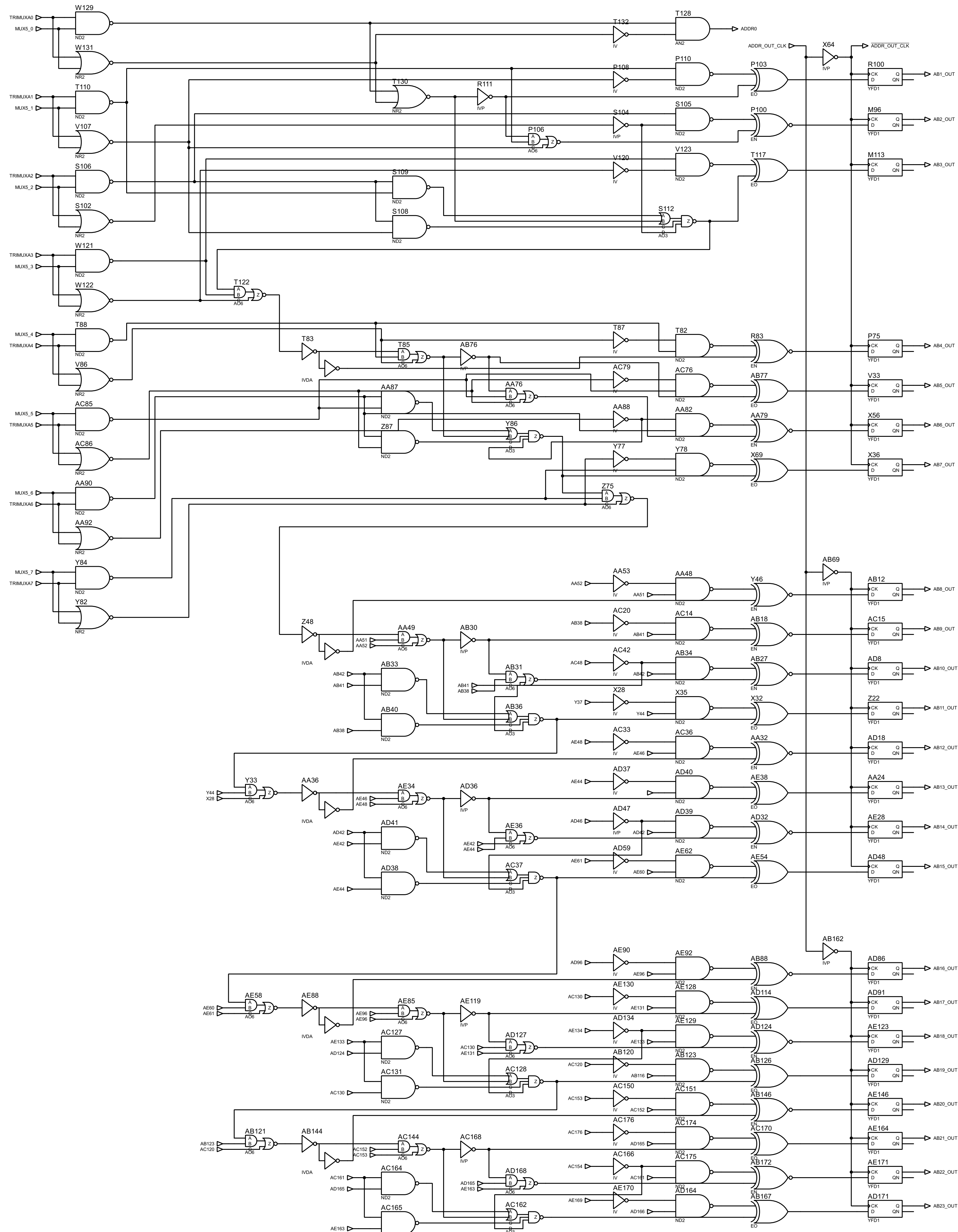
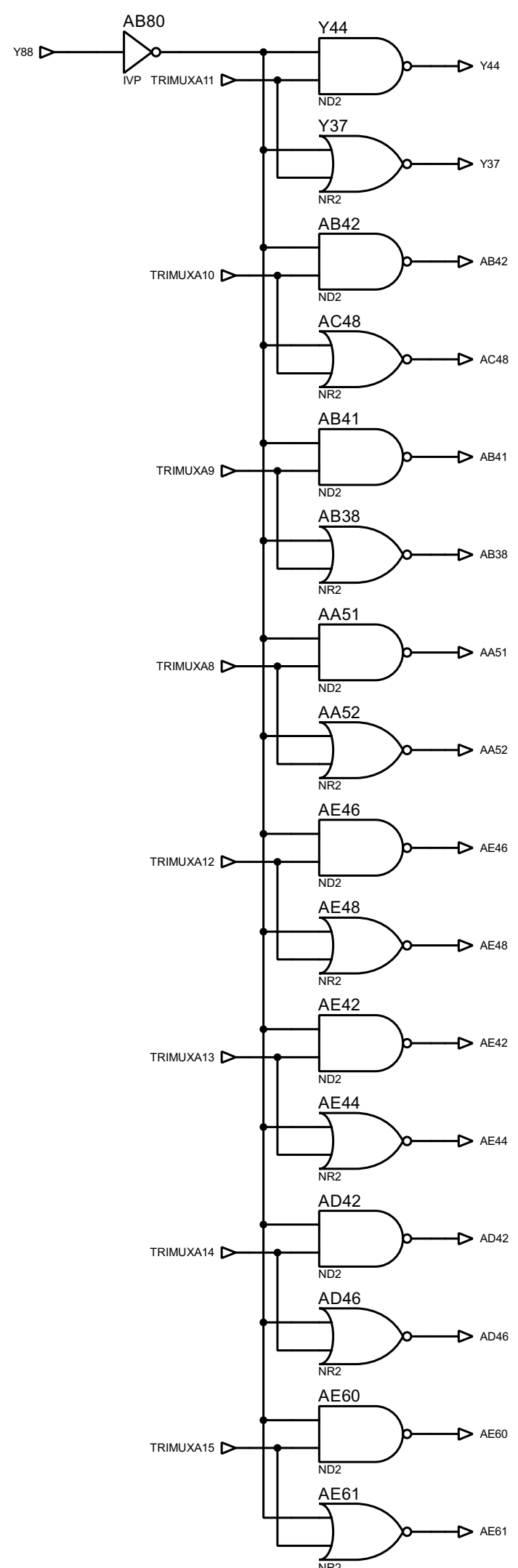
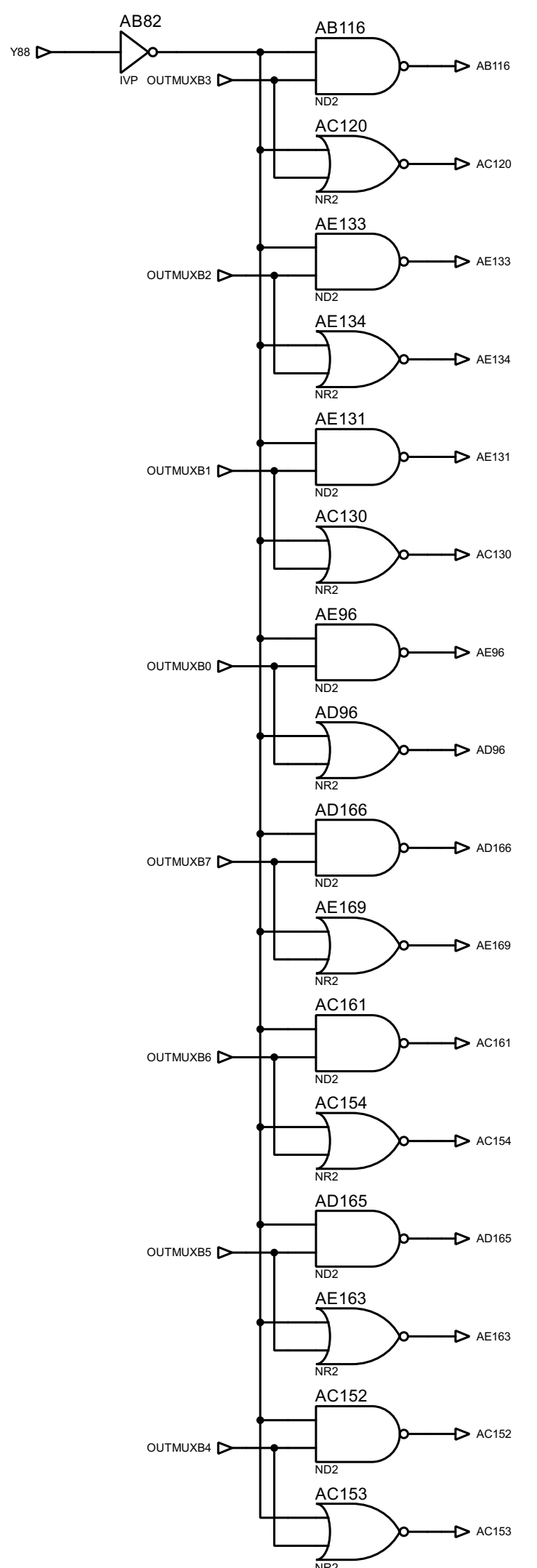
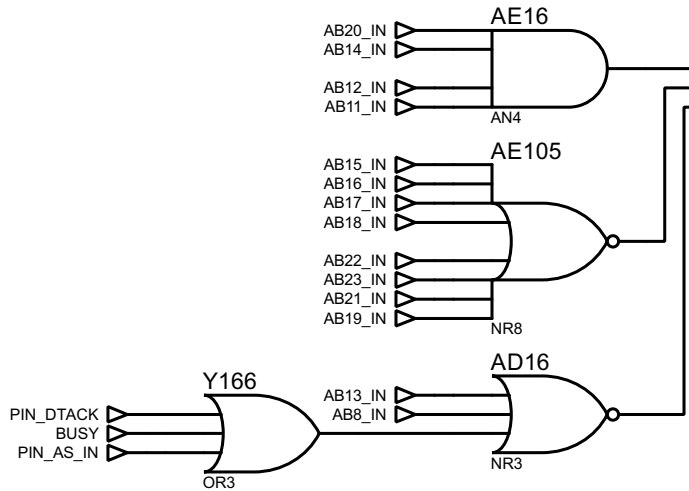
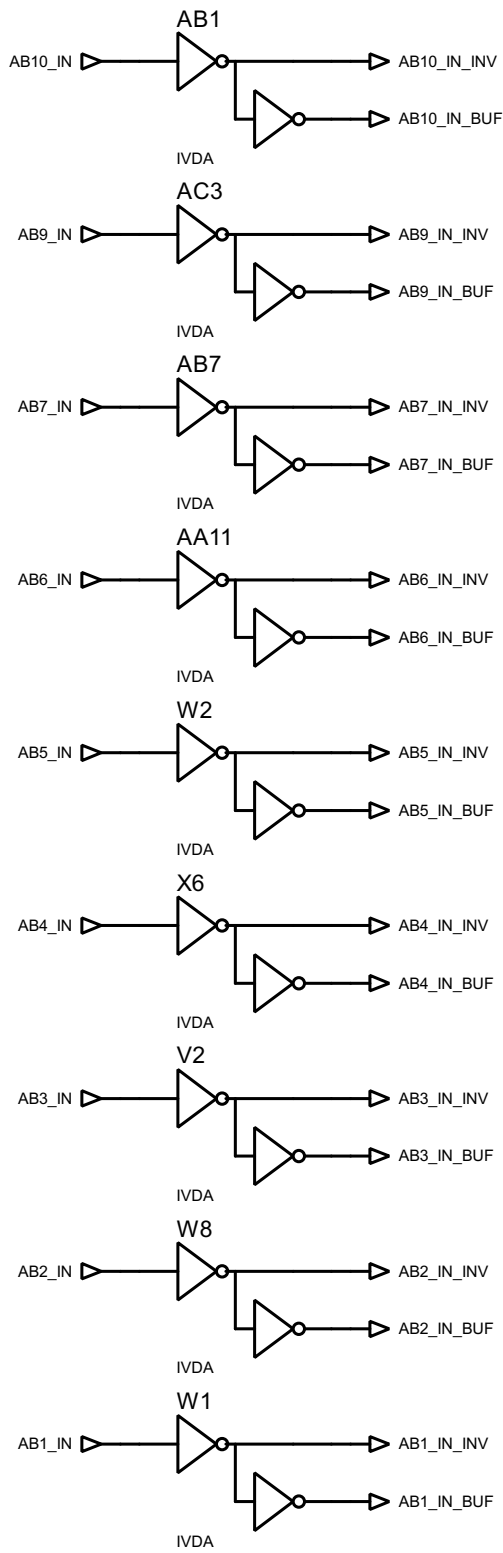


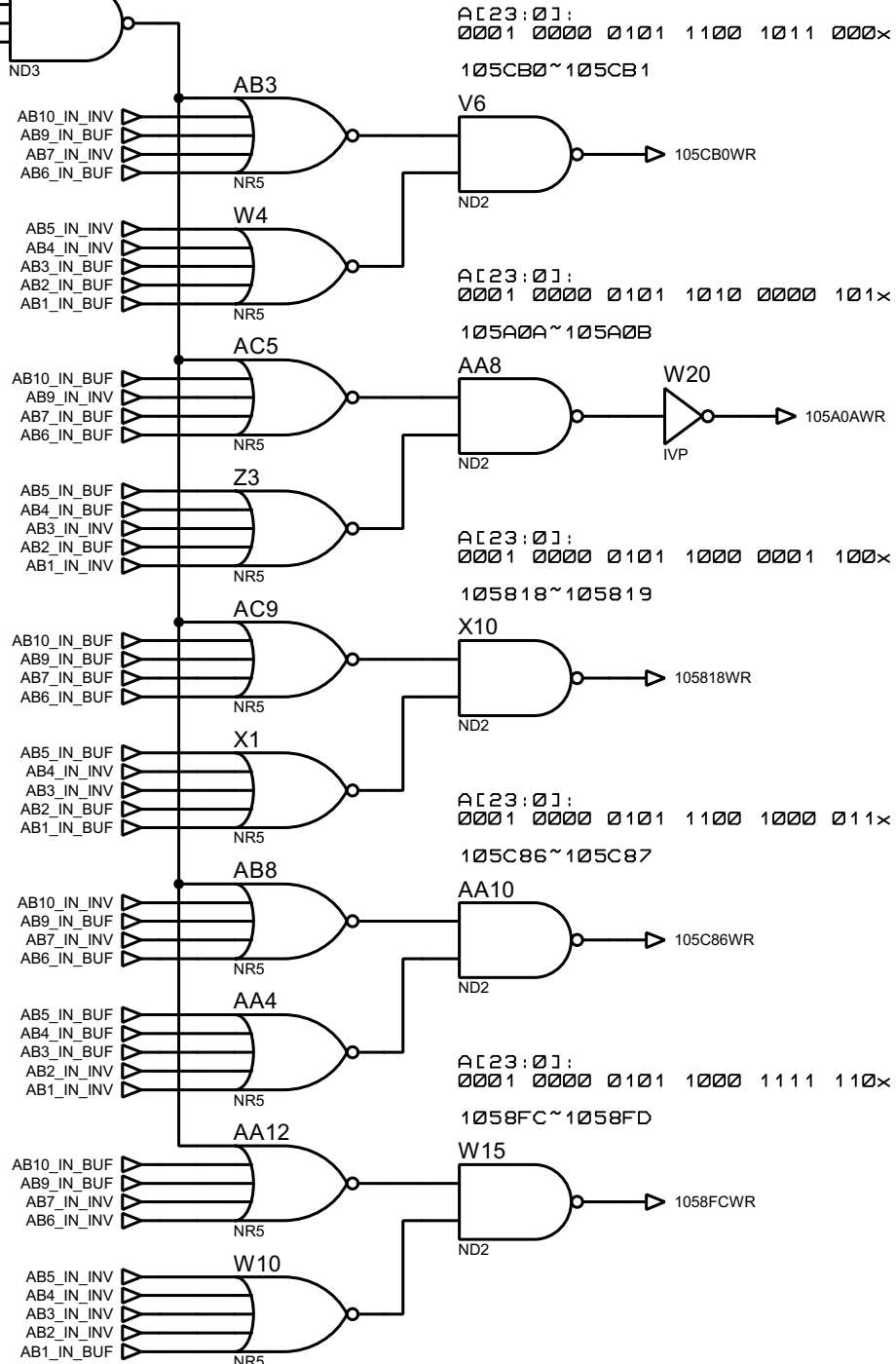
This is just a $8 + 24 = 24$ bits
adder/subtractor, probably to
do $\text{addr_out} = \text{addr_base} \pm \text{addr_counter}$

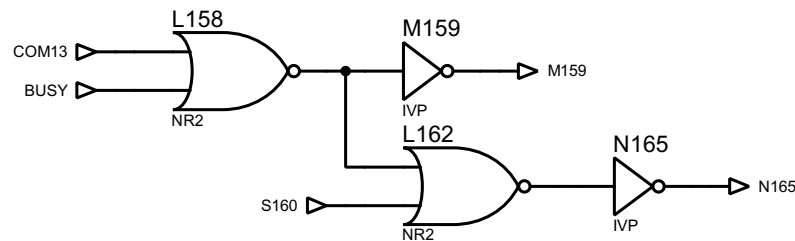
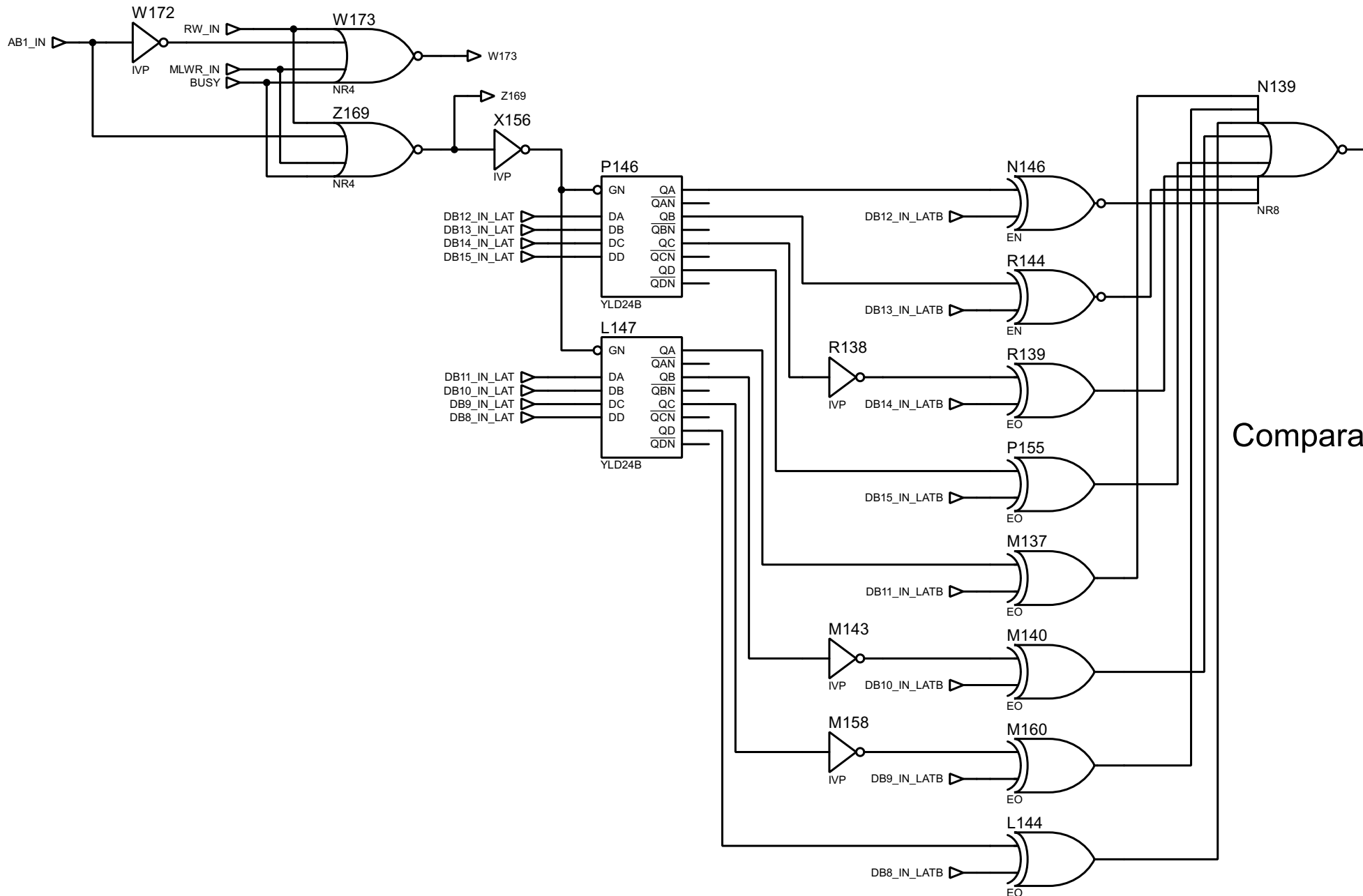
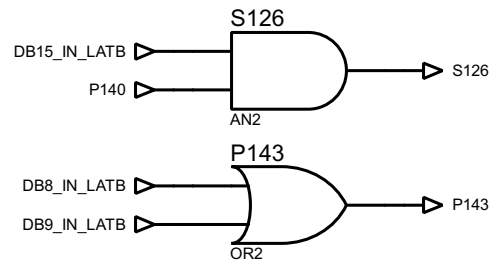
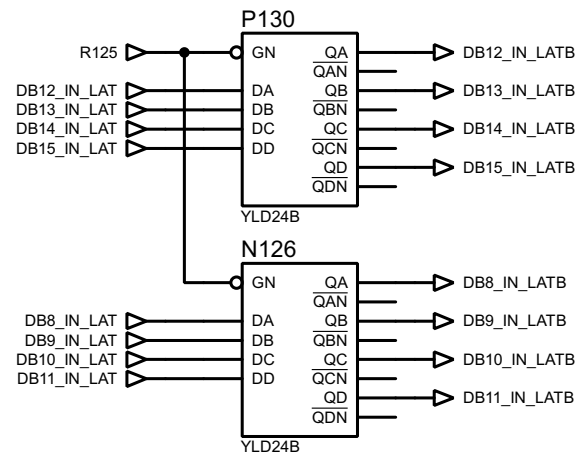
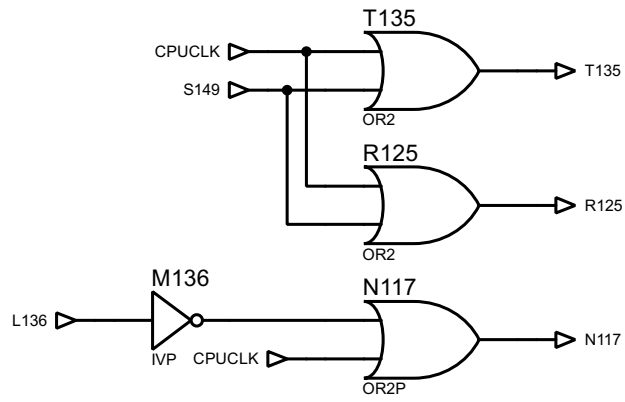
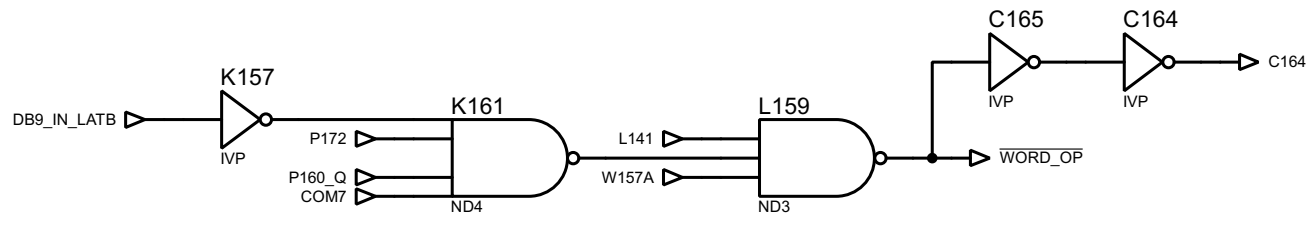


Interception of RAM access at fixed addresses
Sunset Riders uses this
Works on writes AND reads ?

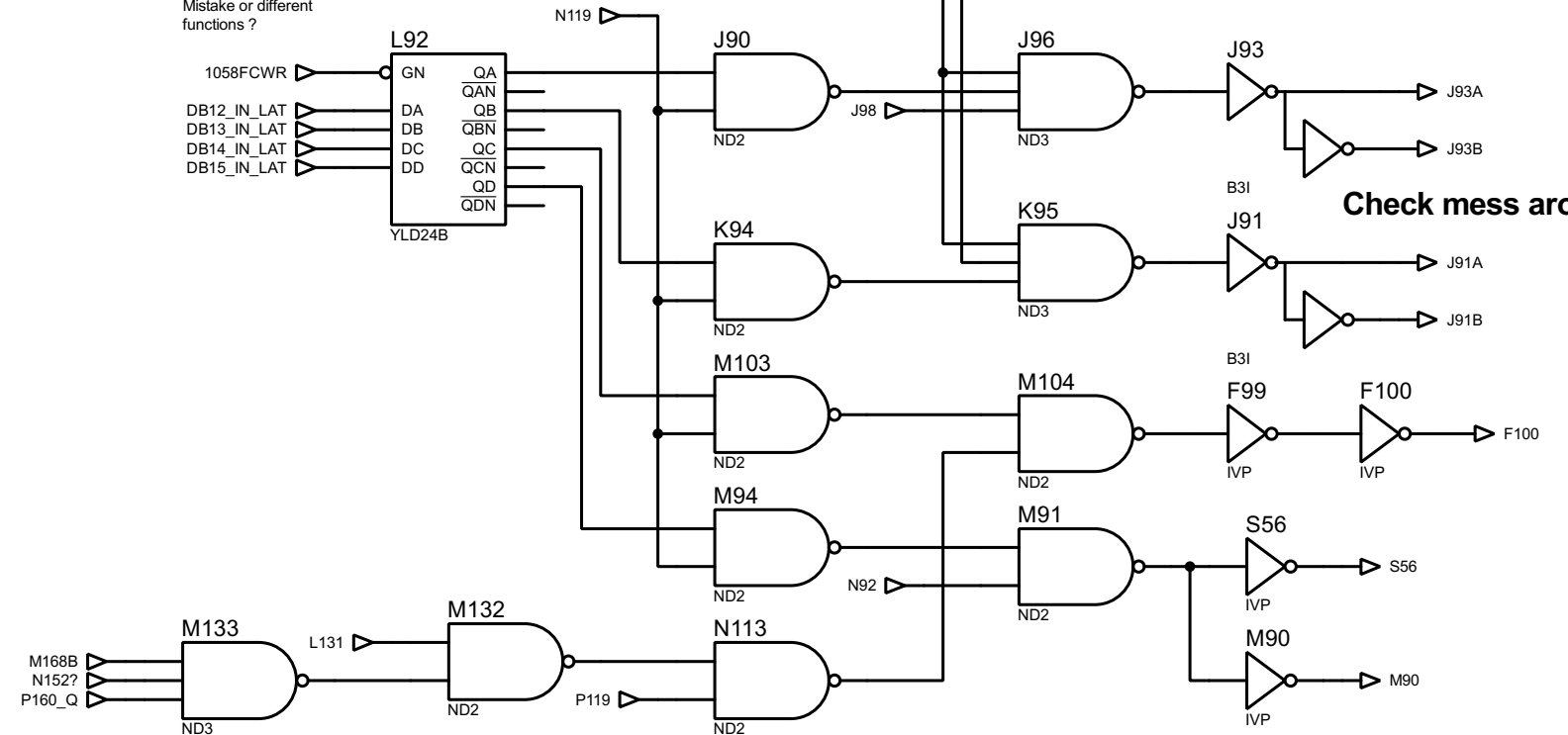


AC23:01:
0001 0000 0101 1xx0 xxxx xxxx
105800~1058FF
105A00~105AFF
105C00~105CFF
105E00~105EFF





There are two 4-bit latches that catch DB[15:12]_IN_LAT on 1058FCWR (W15)! Mistake or different functions?



Check mess around here

