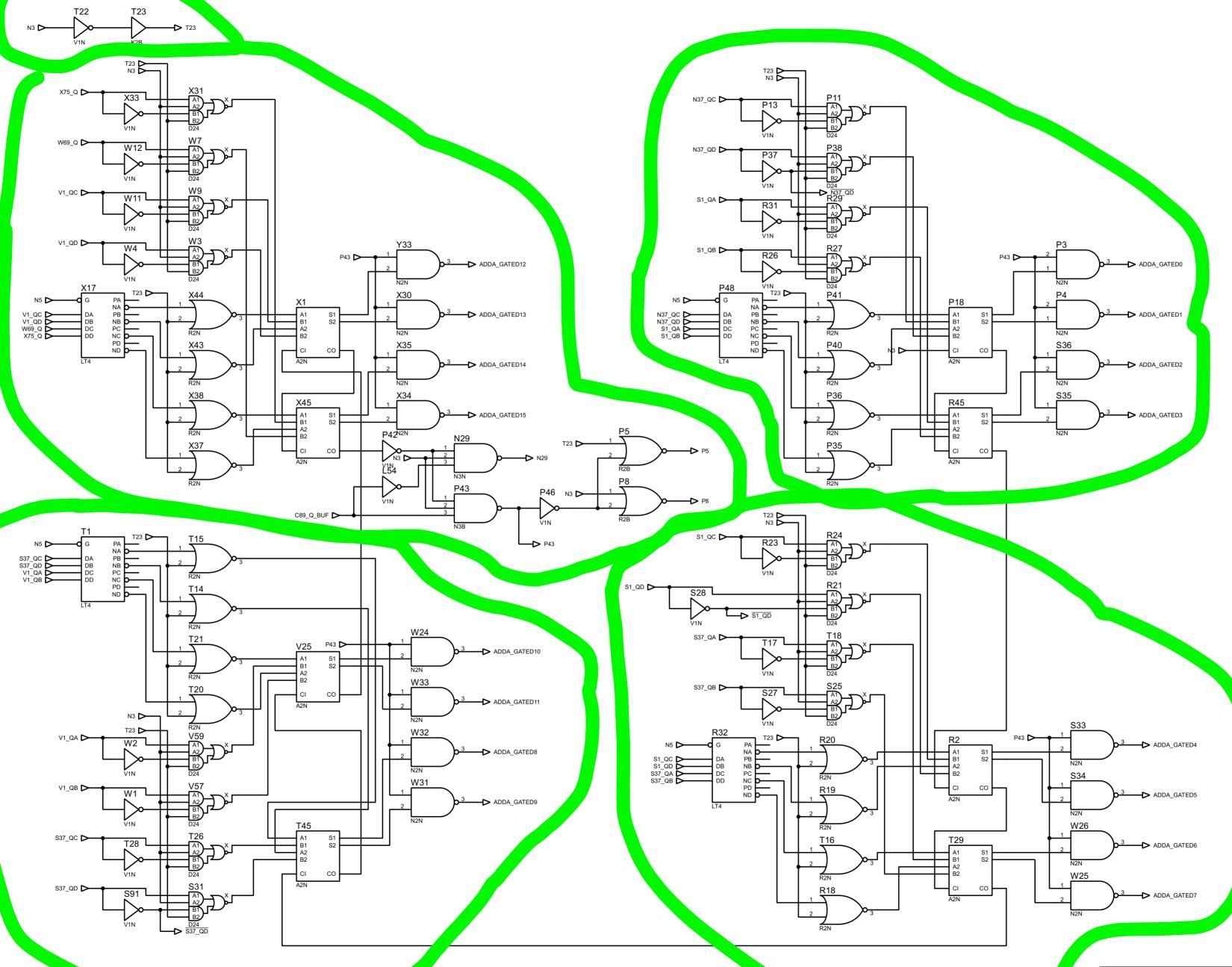


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BY:	Sean Gonsalves	REV:	Pre	1/12



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Roland R15229844 / MB654119

ADDER A

BY: Sean Gonsalves

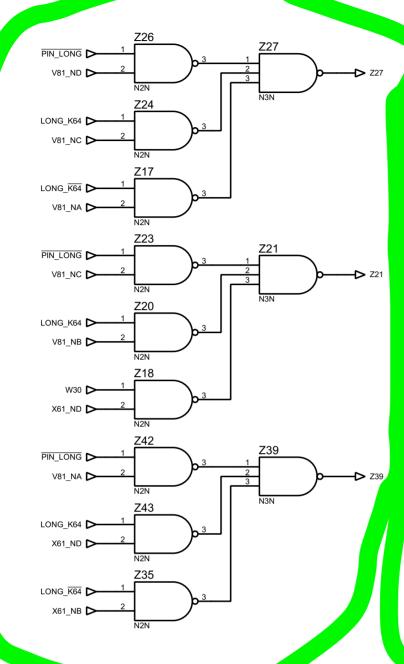
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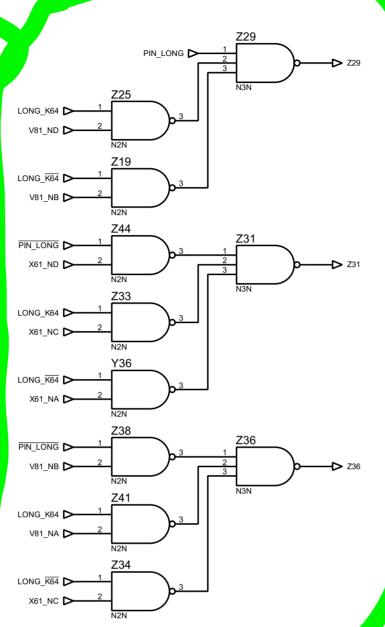
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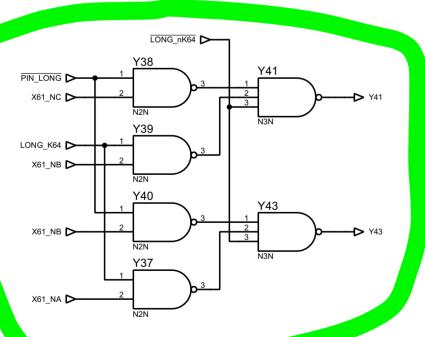
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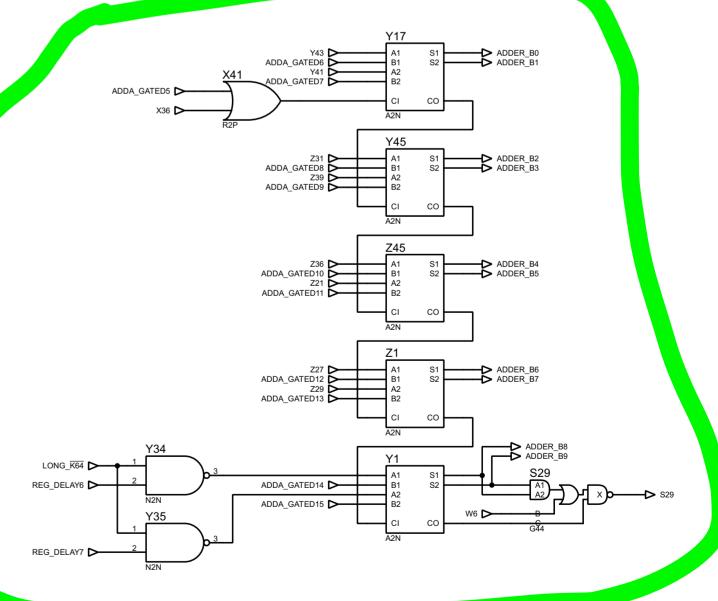
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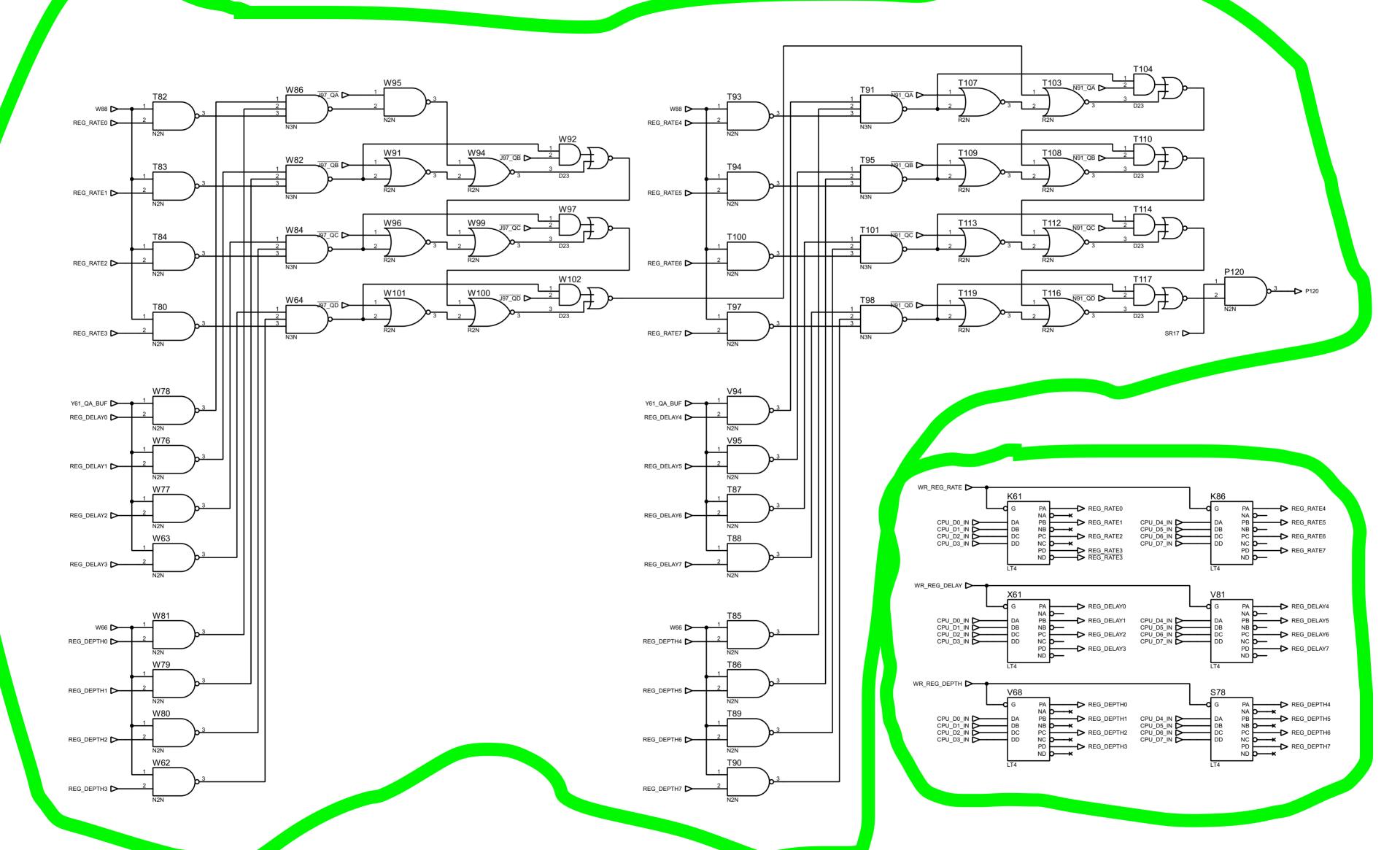




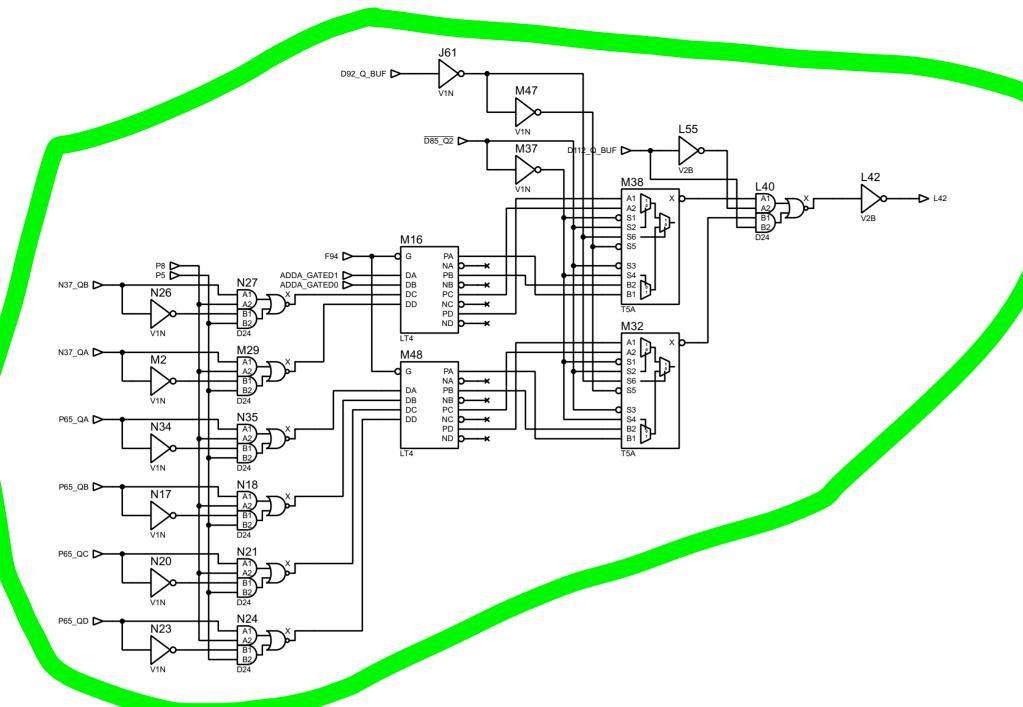


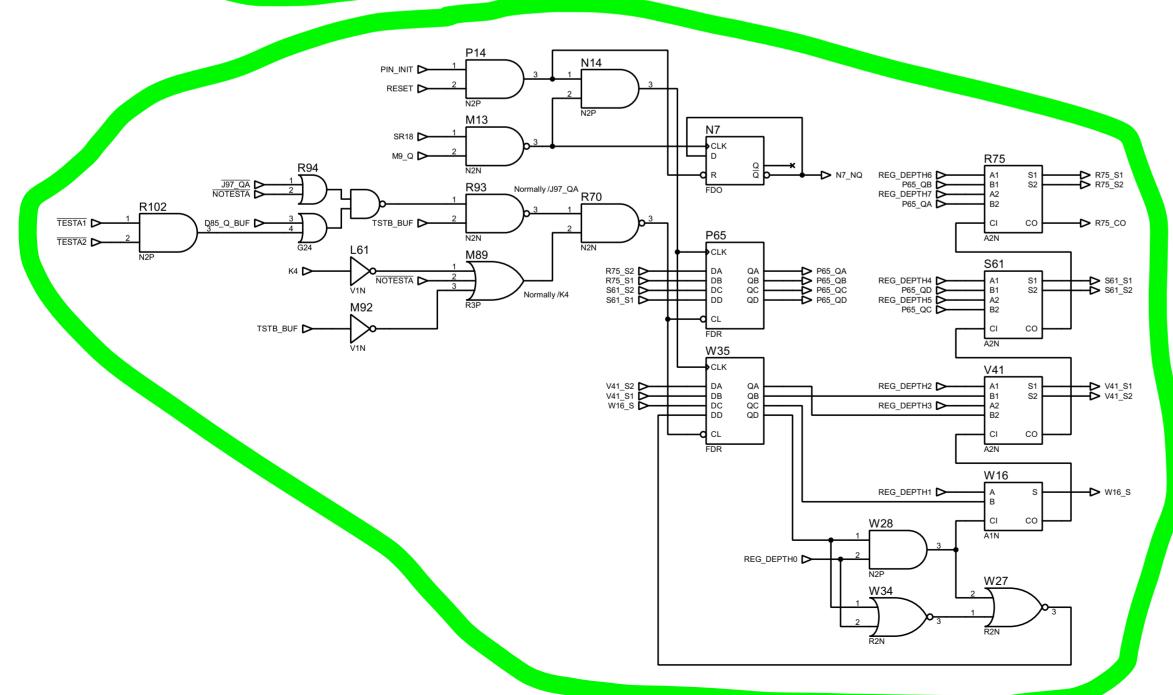


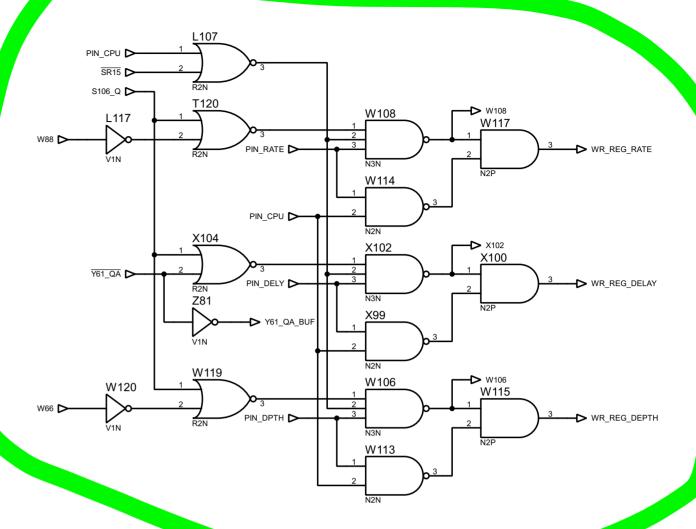
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Roland R15229844 / N	20/07/2024	
ADDER B	PAGE:	
BY: Sean Gonsalves	REV: Pre	3/12

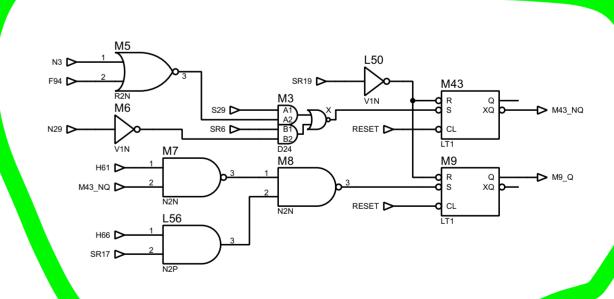


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BY:	Sean Gonsalves	REV:	Pre	4/12

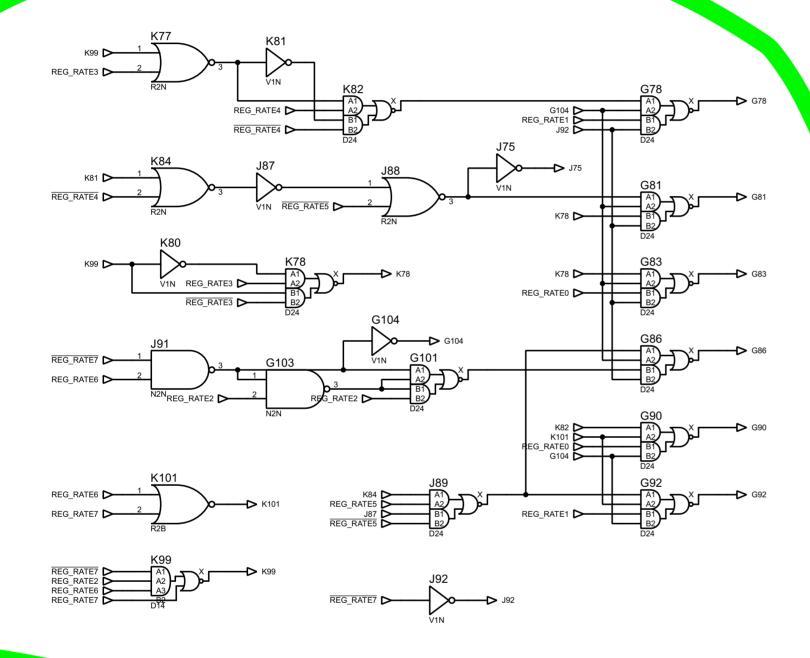


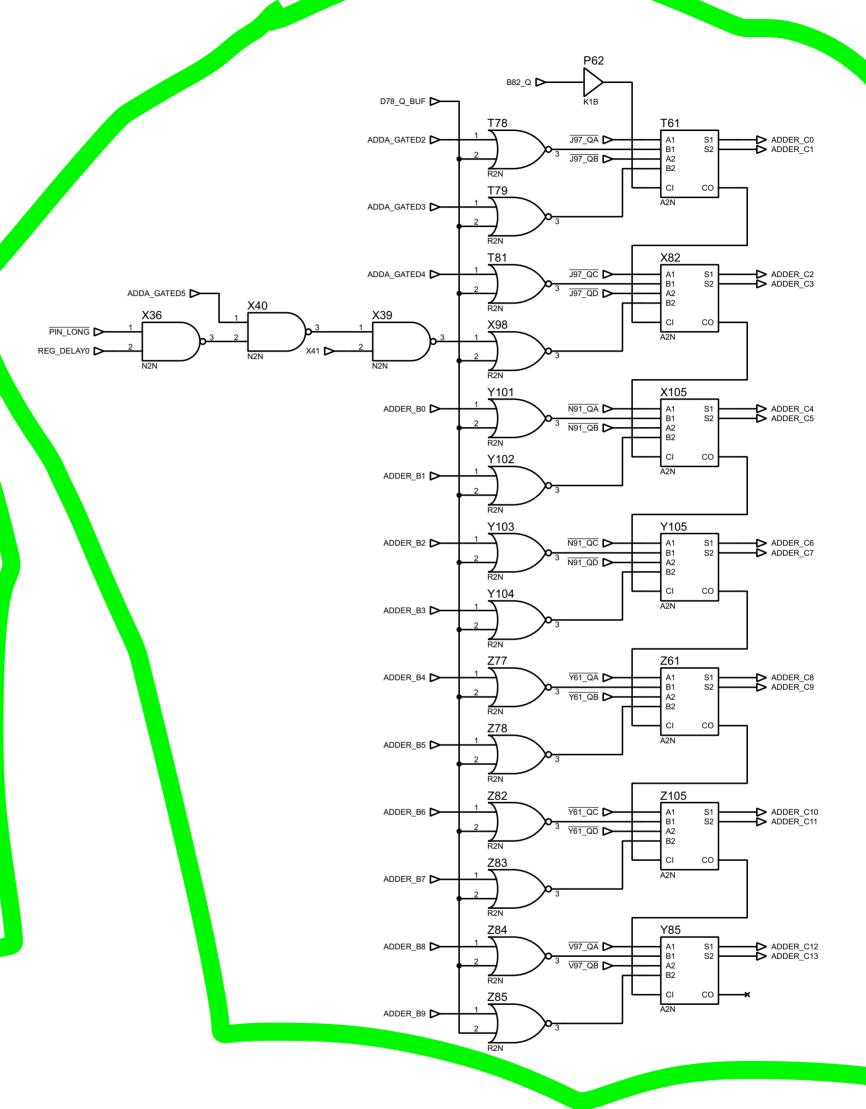




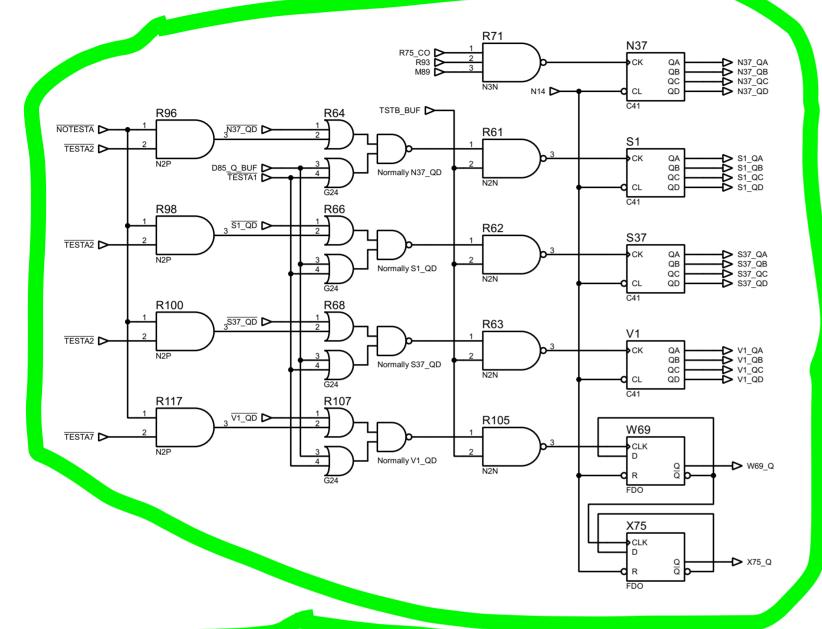


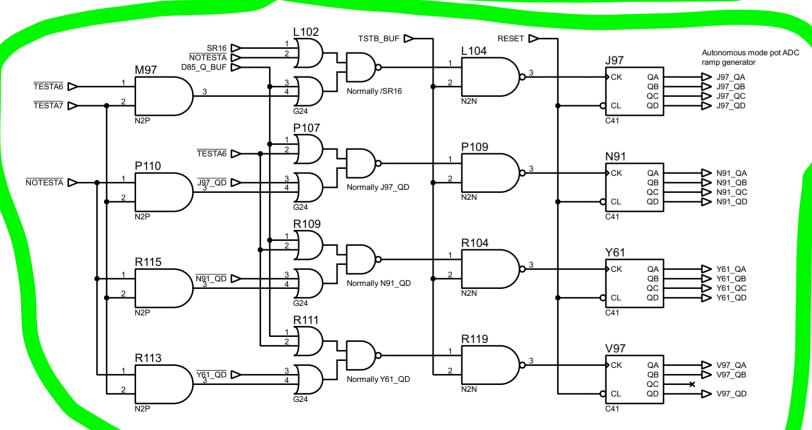
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ADDER D	PAGE:	
BY: Sean Gonsalves	REV: Pre 5/	12

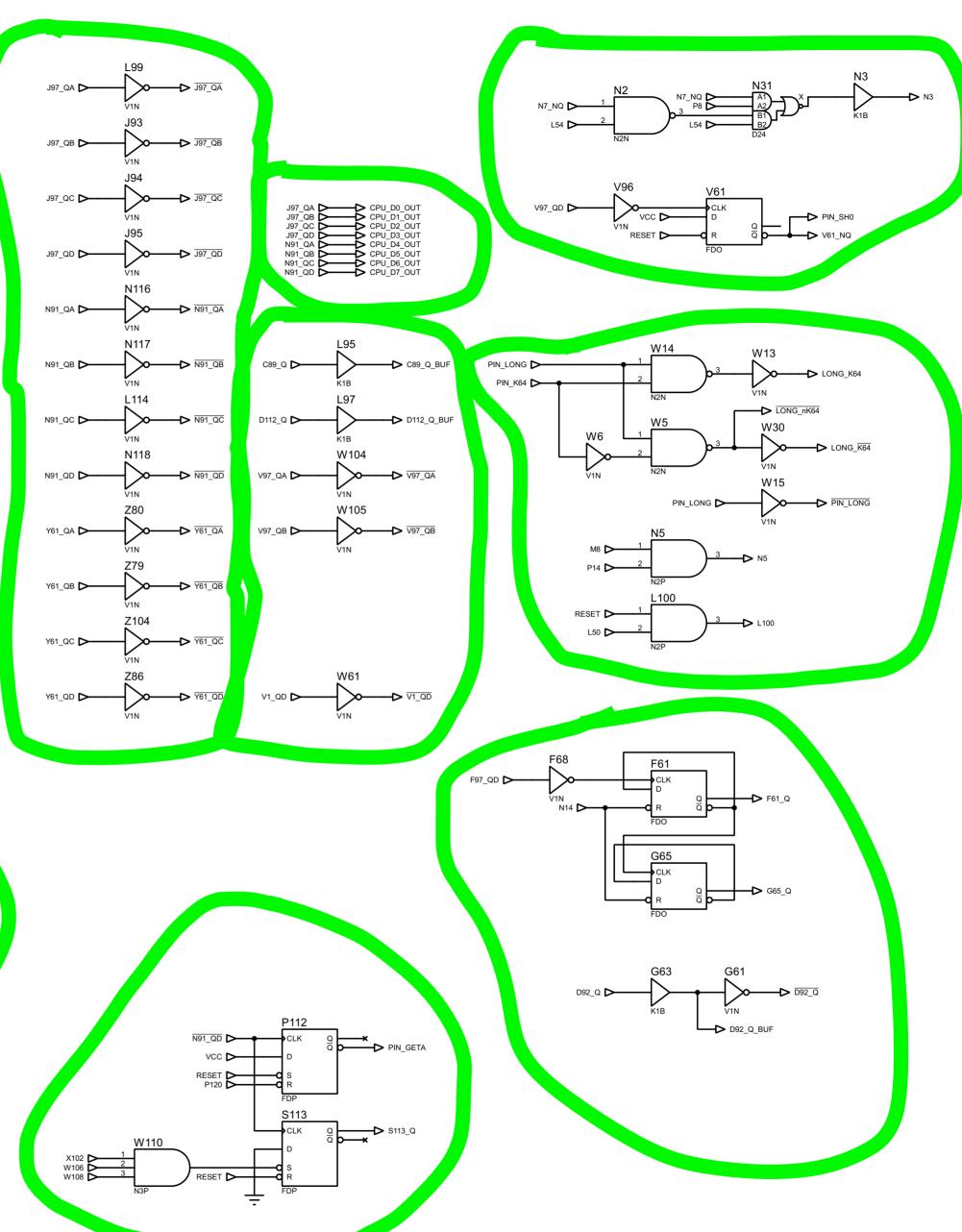




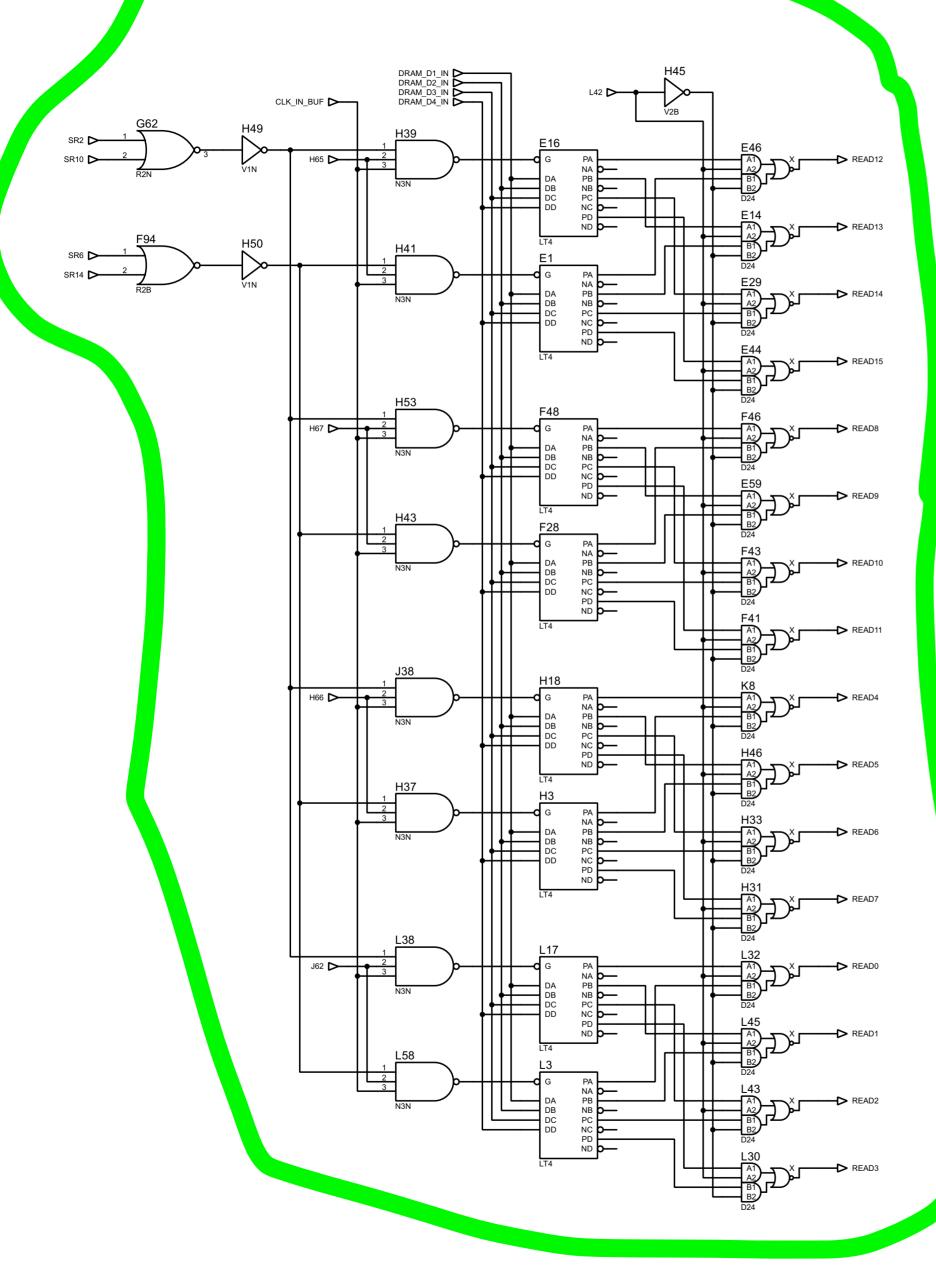
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Α	DDER C			PAGE:
BY:	Sean Gonsalves	REV:	Pre	6/12

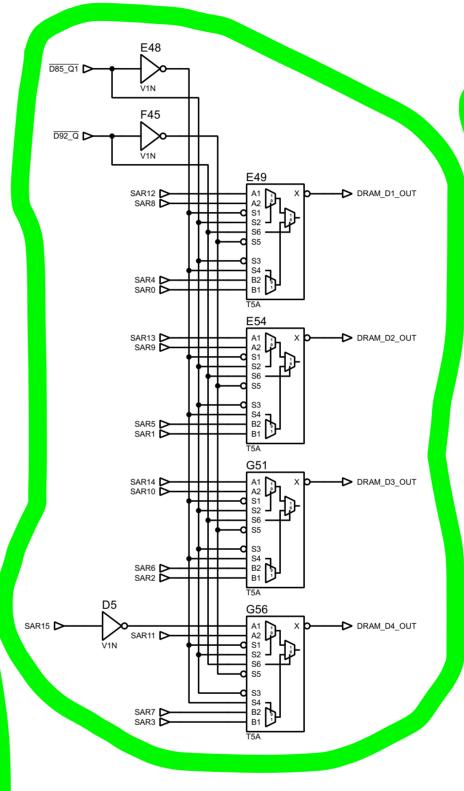


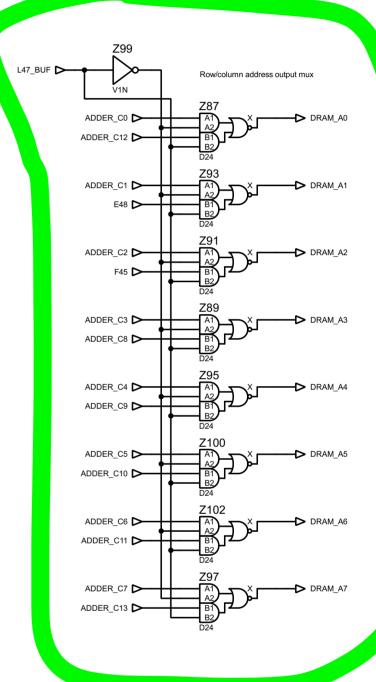




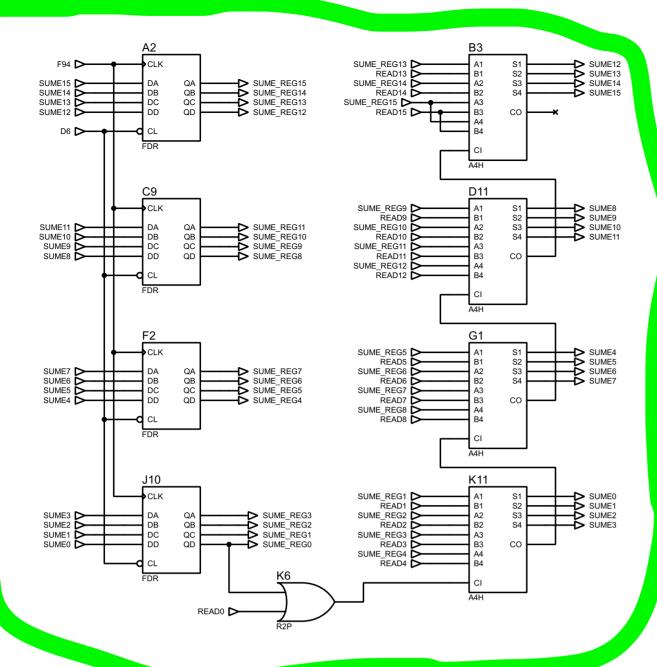
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l N	<b>IISC</b>			PAGE:
BY:	Sean Gonsalves	REV:	Pre	7/12

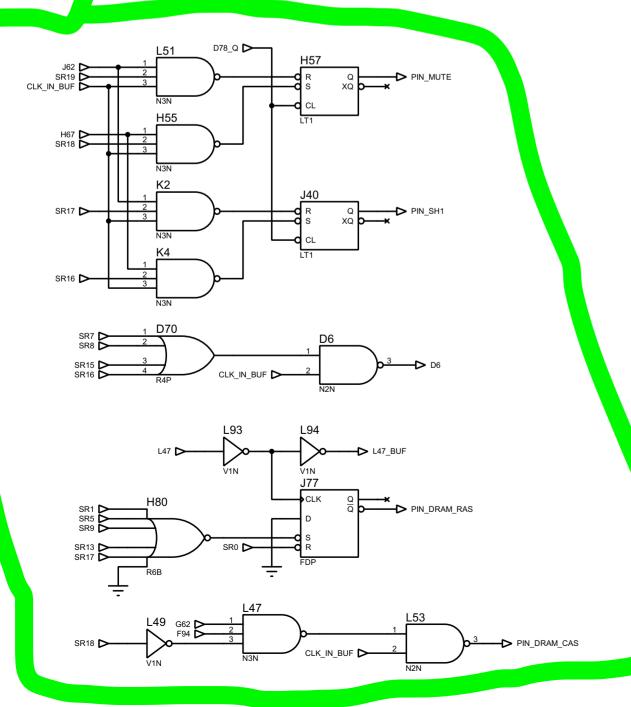


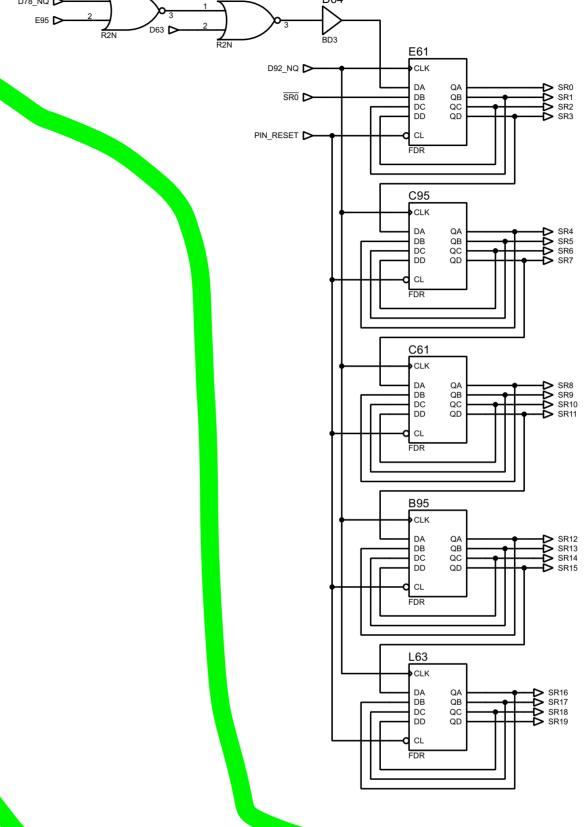


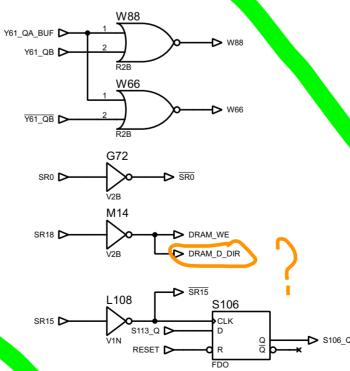


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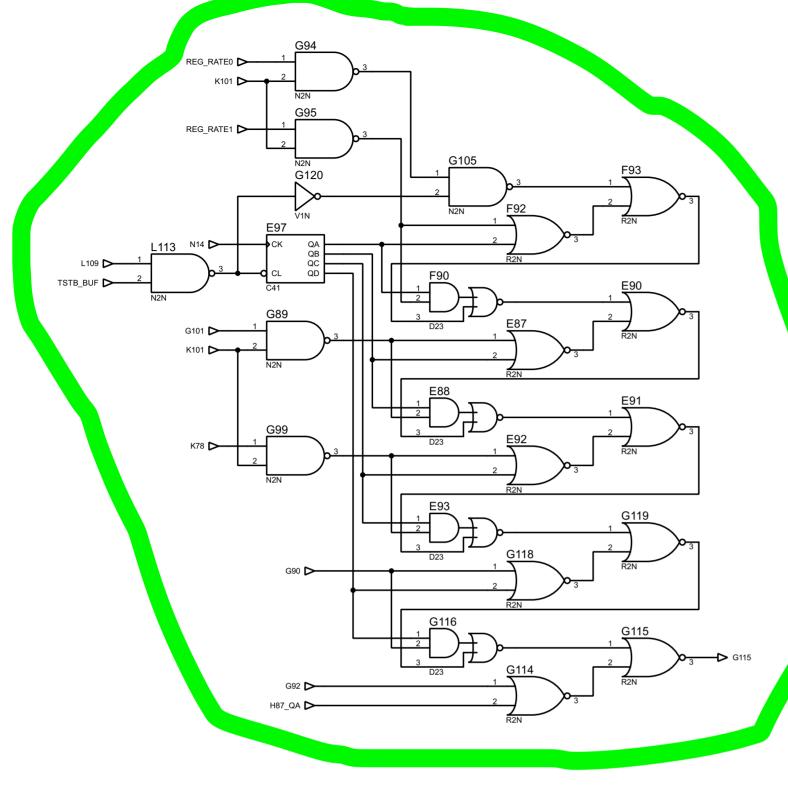


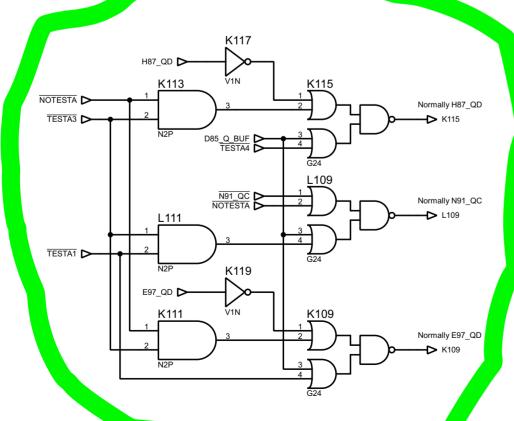


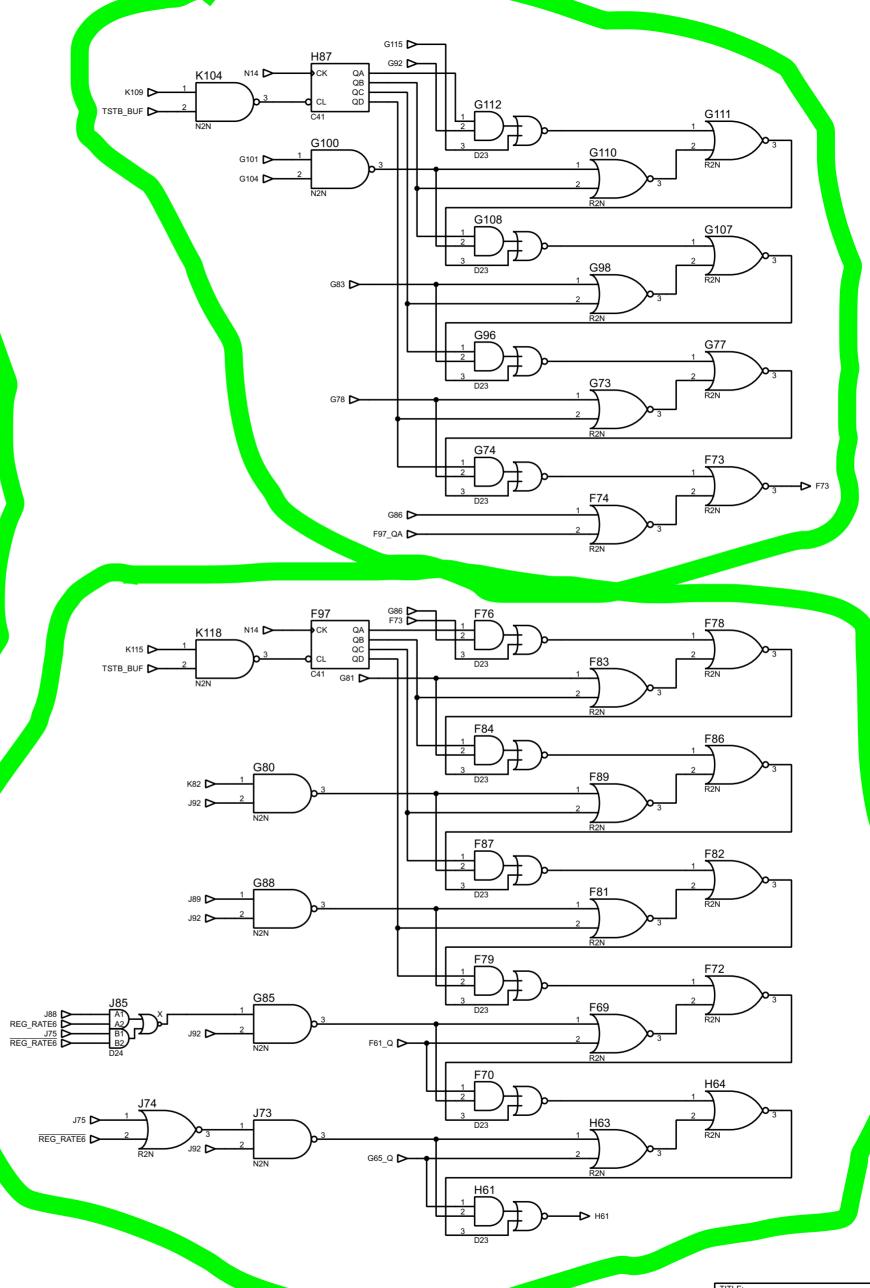




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Α	DDER E			PAGE:
BY:	Sean Gonsalves	REV:	Pre	9/12

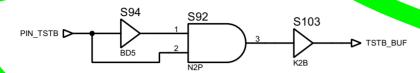


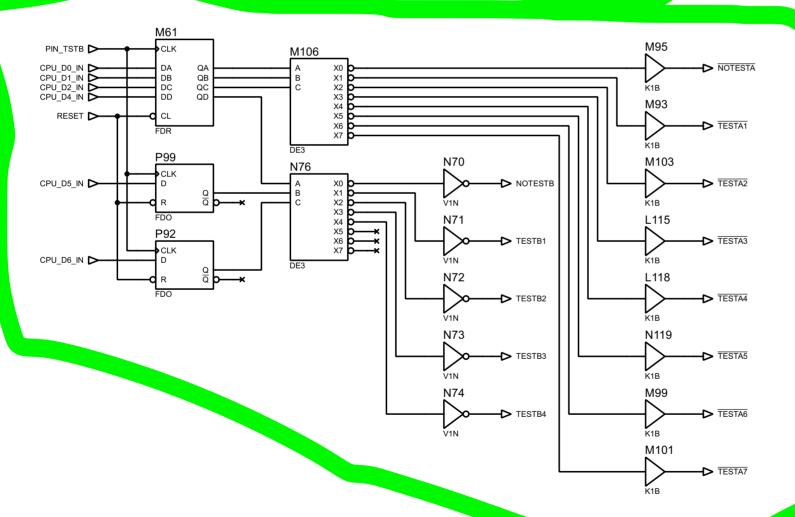


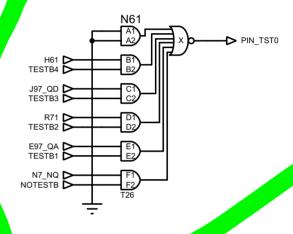


Roland R15229844 / MB654119
RATE MAG COMP
BY: Sean Gonsalves REV: Pre DATE:

20/07/2024
PAGE:
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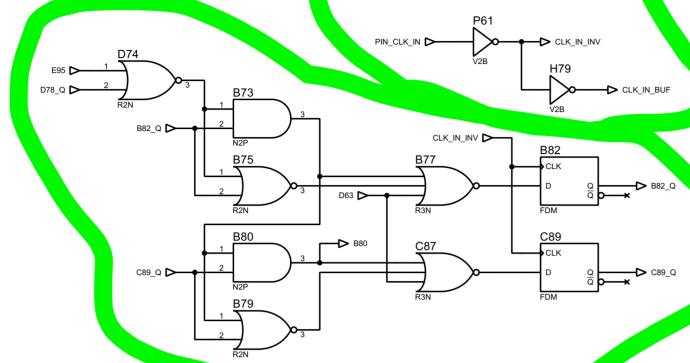
TITLE	:			DATE:
R	oland R15229844 / M	1B65411	9	20/07/2024
т	EST MODE			PAGE:
BY:	Sean Gonsalves	REV:	Pre	11/12

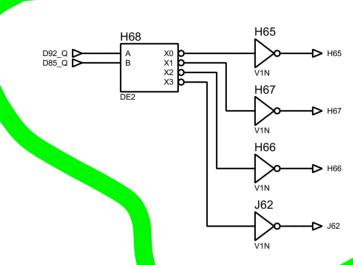
High: IO\* outputs Low: IO\* inputs

PIN\_OE PIN\_IO\_DIR

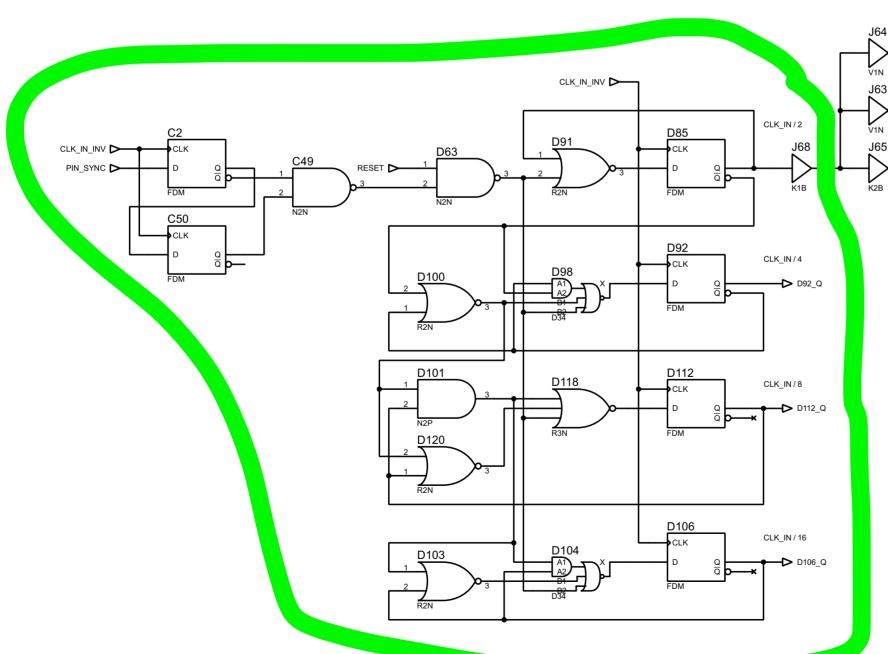
High on DEP-5 (CPU peripheral mode)
PIN\_CPU CPU\_D\_DIR

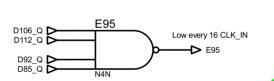
## PIN\_CLK\_IN PIN\_CLK\_OUT

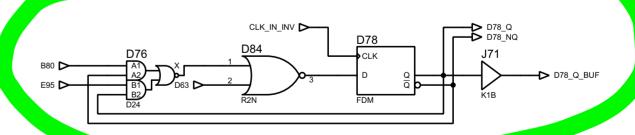




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TITLE:			DATE:	
F	Roland R15229844 / MB654119		20/07/2024	
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BY:	Sean Gonsalves	REV:	Pre	12/12