









SPI_CS 1 CS# SPI_MISO 2 DO IO2 GND GND	C26 100nF VCC 7 103 6 SPI CLK DI					
SPI Flash电路						

◇嘉立创EDA		V1.0	A2	嘉立创EDA				
		VER	SIZE	PAGE	1	0F	1	
Reviewed		Matrix Clock						
Drawed				'				
Page	Main Board			Part Nu	ımber			
Seriema CTG		Wattix Clock		Create	Date	2022-10-03		
Schematic		Matrix Clock		Update	Date	2022-10-10		