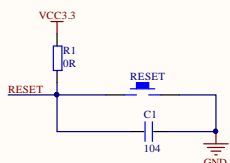
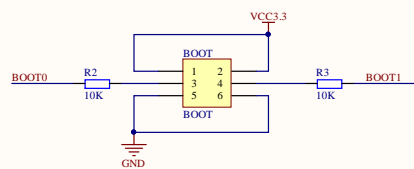


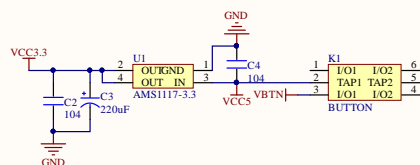
A



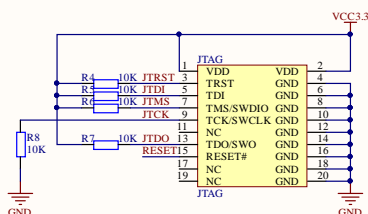
B

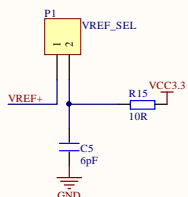


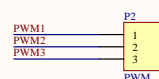
C

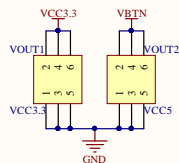


D

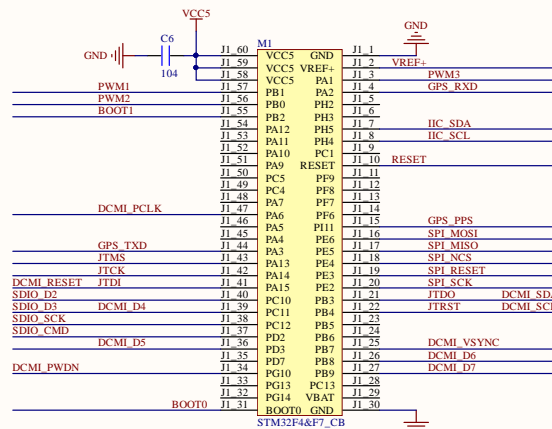




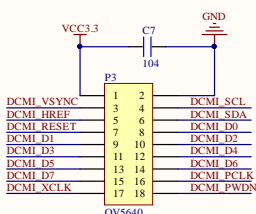


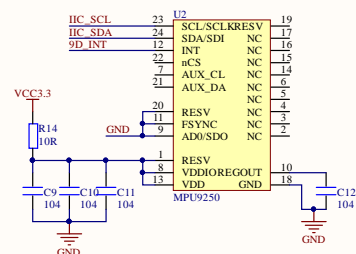


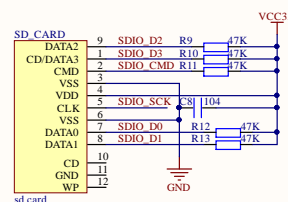
9

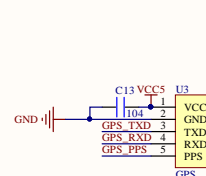


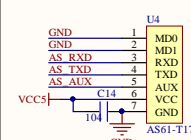
1



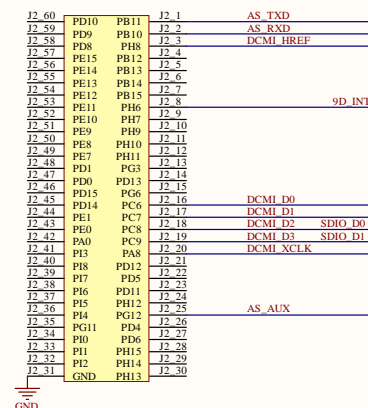




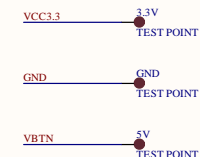




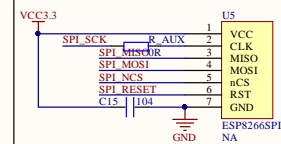
4



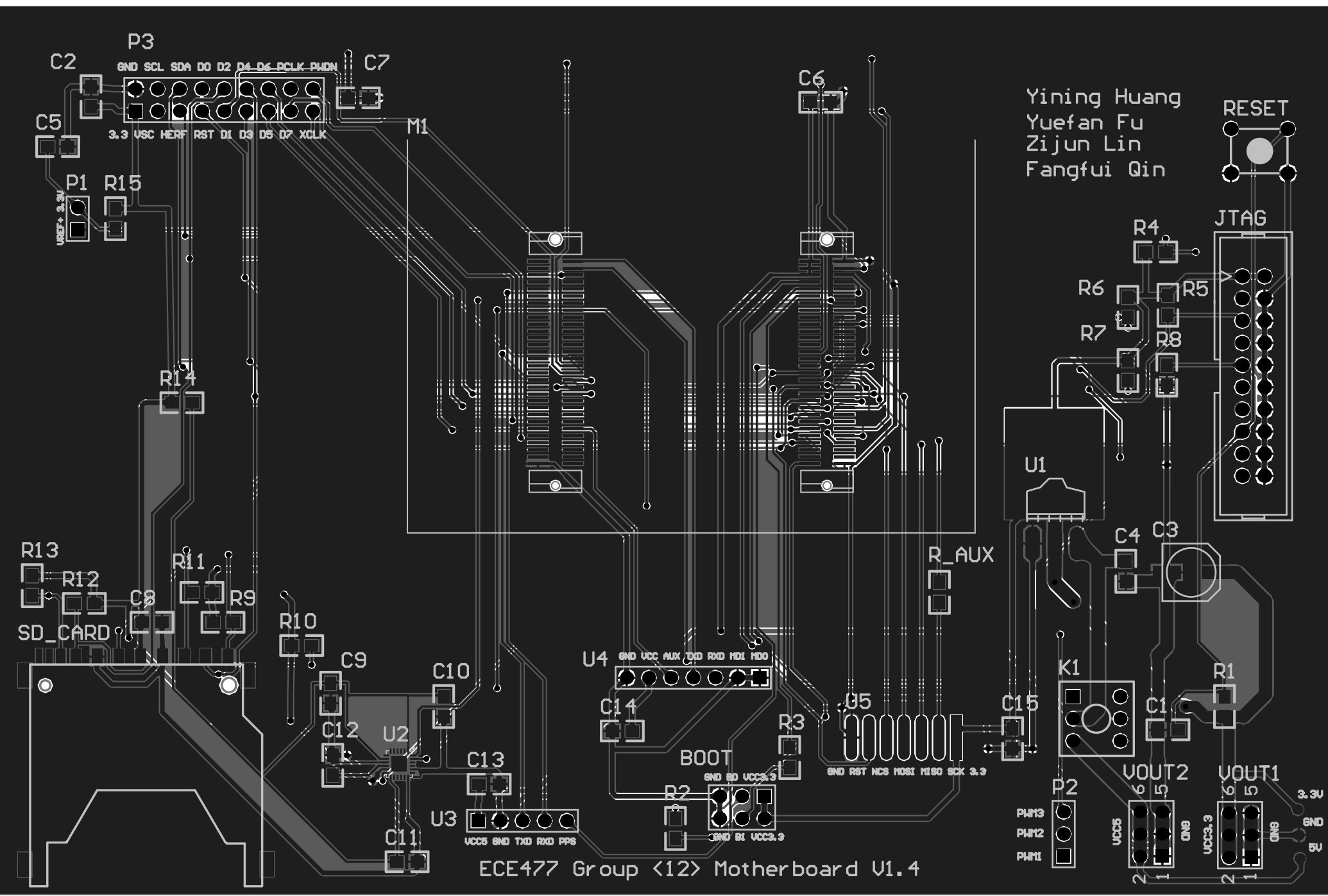
1



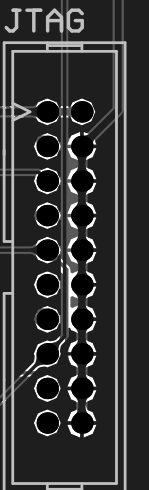
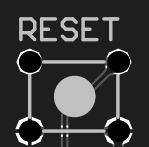
1



Title			
Size	Number	Revision	
Tabloid			
Date:	19/11/01	Sheet	of
Drawn by:	Collins, J. Info & Link Sch Dev	Drawn by:	



Yining Huang
Yuefan Fu
Zijun Lin
Fangfui Qin



ECE477 Group <12> Motherboard V1.4

Comment	Description	Designator	Footprint	LibRef	Quantity
TEST POINT	测试点	3.3V, 5V, GND	TP-1.5	TEST-POINT	3
BOOT	Header, 3-Pin, Dual row	BOOT	HDR2X3	Header 3X2	1
104		C1, C2, C4, C6, C7, C8, C9, C10, C11, C12, C13, C14, C15	0805	C	13
220uF		C3	CC6.56.5	CAP	1
6pF		C5	0805	C	1
JTAG		JTAG	JTAG_20	JTAG	1
BUTTON		K1	BTN8.5*8.5	BUTTON	1
STM32F4&F7_CB	F429/746 Core Board	M1	STM32F4&F7_CB	STM32F4&F7_CB	1
VREF_SEL	Header, 2-Pin	P1	HDR1X2	Header 2	1
PWM	Header, 3-Pin	P2	HDR1X3	Header 3	1
OV5640	Header, 9-Pin, Dual row	P3	HDR2X9	Header 9X2	1
0R		R1, R_AUX	0805	R	2
10K		R2, R3, R4, R5, R6, R7, R8	0805	R	7
47K		R9, R10, R11, R12, R13	0805	R	5
10R		R14, R15	0805	R	2
KEY_M		RESET	KEY_M	KEY_M	1
sd card		SD_CARD	SDCARD_L	sd card	1
AMS1117-3.3		U1	AMS1117	AMS1117	1
MPU9250	9轴运动处理传感器	U2	QFN24-t	MPU9250	1
GPS	Header, 5-Pin	U3	HDR1X5	Header 5	1
AS61-T17	Header, 7-Pin	U4	HDR1X7	Header 7	1
NA		U5	Edge Connector7	ESP8266SPI	1
VCC3.3	Header, 3-Pin, Dual row	VOUT1	HDR2X3	Header 3X2	1
VCC5	Header, 3-Pin, Dual row	VOUT2	HDR2X3	Header 3X2	1