

# ECE 554 Lab 1 Start Guide

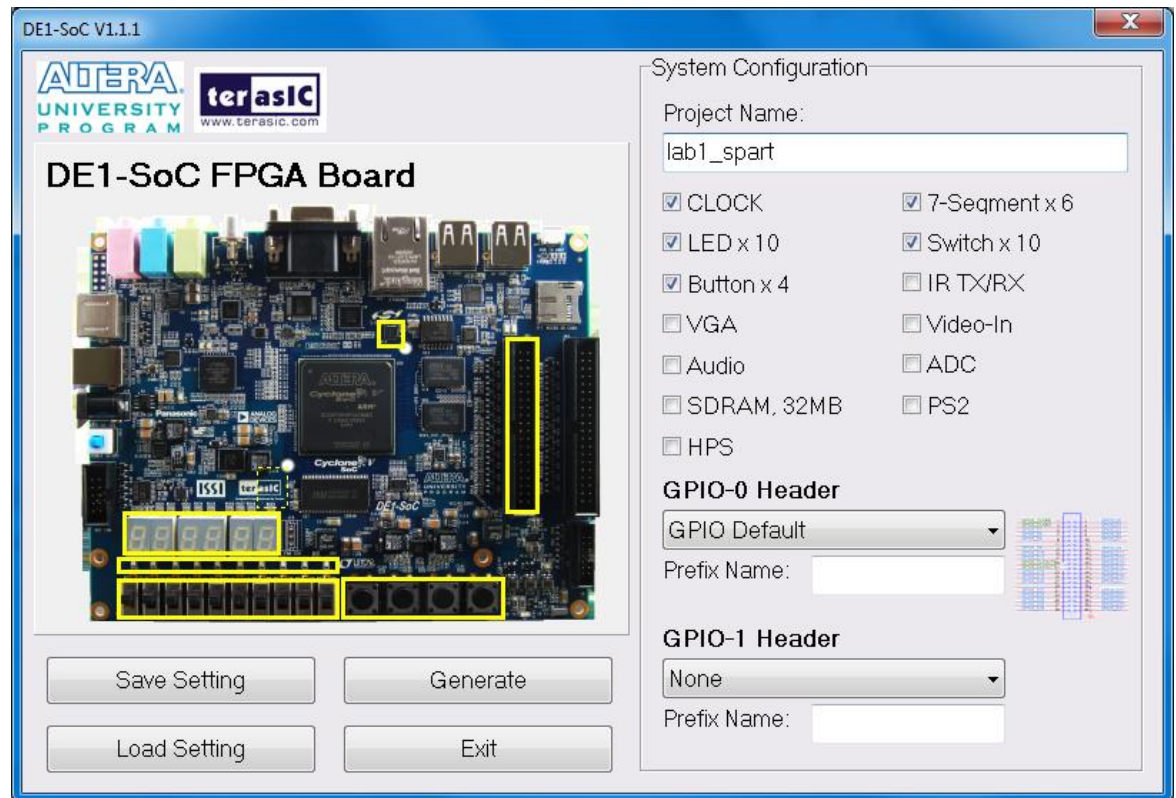


# Generate project file using system builder

- Set project name to “lab1\_spart”

- Select
  - clock
  - LED
  - Button
  - 7-segment
  - Switch

- Set GPIO-0 to  
GPIO Default



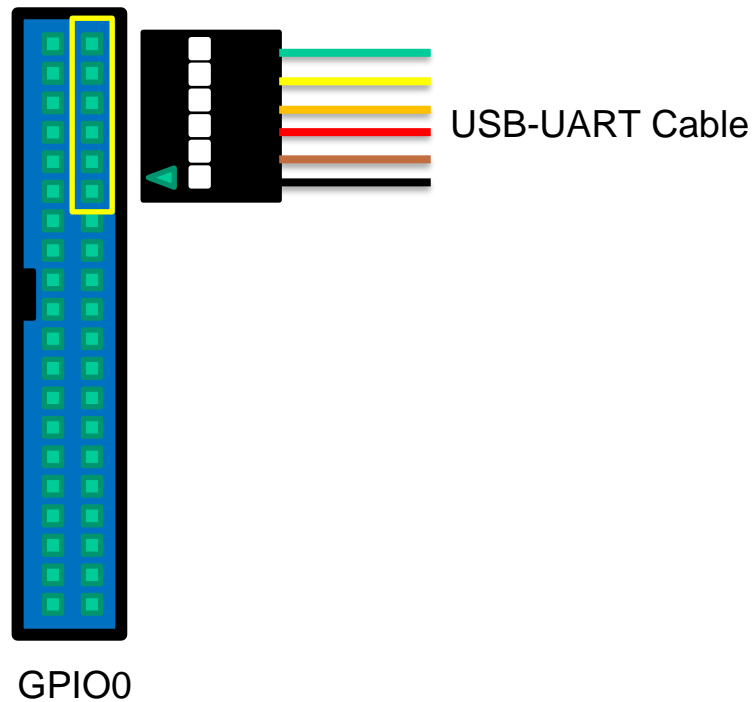
# Copy to your own folder

- Copy the generated project files to your own folder
- Copy “lab1\_spart.v” from the starter kit into your project folder
- Look through lab1\_spart.v to see what board resources have been used in this lab (e.g., how to set baud rate with switches, etc.)
- Use lab1\_spart.v as your top level design and finish spart and driver module by your self.



# Test your design

- Download your design into the FPGA and following the picture to connect the USB-UART cable.



# Use PuTTY to communicate with spart

- Follow the picture to set putty in Serial mode.
- Note that serial line number may not be COM1, follow the following steps to check:
  1. Press win key, type “device manager”
  2. Find the port number of “USB Serial port (COMX)” under “Ports (COM & LPT)”

