


# CURRICULUM VITAE

## A. Demographic Information

<b>Name</b>	Fu Wei	<b>Sex</b>	Male	
<b>Birth place</b>	Hubei, PRC	<b>Birthday</b>	Dec-26-1978	
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## B. Education Information

M.S, HIT Robot Research Institute, Department of Mechatronics, Harbin Institute of Technology, PRC	Sep/2002----Jul/2004
B.S, Department of Mechanics, Harbin Engineering University, PRC	Sep/1998----Jul/2002

## C. Working and Research Experience

1	Senior Application Engineer in Advantest (Singapore) Pte. Ltd (2012.4~today)	<ul style="list-style-type: none"> <li>• Provide residential technical consultation to Product Development and Test Engineering group at Qualcomm Test Center of Excellence (TCE) in Singapore. Develops test solutions for design verification of highly integrated Mobile Station Modem (MSM)/ Mobile Data Modem (MDM), power management, analog and mixed signal ASICs designed by QCT. Work with Design, Foundry, Program Management, Yield and Manufacturing teams to create, commercialize and support QCTs products.</li> </ul>
2	Senior Application Engineer in Verigy Ltd (2010.8~2012.4)	<ul style="list-style-type: none"> <li>• Provide pre/post-sales technical support for our Semiconductor Test Systems V93000 to new and existing customers and global accounts in Singapore: Global Foundries (Chartered Semiconductor Manufacturing), States ChipPAC, Ardentec(Singapore) and ASE (Singapore) .</li> <li>• Understand customers' IC testing needs and propose testing solutions to meet those needs. Lead test program development/productivity improvement projects based on customer's requirement, Resolve customer's pre/post production test program issues to minimize tester system V93000 down time, time to high volume manufacturing.</li> <li>• Deliver training programs to enable the customer's engineers to better utilize the test system. Develops and execute support plans as part of the customer account team.</li> </ul>
3	Senior Test Engineer in ST-Ericsson Asia Pacific Pte Ltd. (2009.7~2010.7)	<p>USB1.1/USB2.0 and OTG products ATE test development with Credence Quartet , Logic100 , and Duo series of device test systems in a Sun Workstation, SunOS, UNIX, and C code environment. Create ATE test setup (test program, loadboard and/or probecard) Schematic capture of hardware designed and review layout before fabrication. Characterize new silicon and analysis performance. Support sample testing for initial sample shipments and qualification tests. Release test setup to production. Monitor and resolve ramp up issues. Continuously improve test setup for better yield and lower test cost.</p>

4	Senior Engineer in Chartered Semiconductor Manufacturing Ltd. (2007.9~2009.2)	Testing development and support for new product wafer sorting: BCM6511A0, BCM4325A0 and BCM3563. OTPTV Products and memory testing support, yield enhancement and failure analysis, eFuse wafers' blowing and sensing with tester UltraFlex/Flex, Verigy HP93000 and Credence D10.
5	Application Engineer in Teradyne (ShangHai) Corp. (2004.7~2007.9)	<ul style="list-style-type: none"> <li>• Develop semiconductor test solutions for world-wide customers: UTStarcom-BBIC project (UT8860), LP3990 (Linear Voltage Regulator) for National Semiconductor, PCF50606(Power management Device) for Philips. SAR project (low-power, Multi-channels CMOS analog to digital converter) for Analog Devices.</li> <li>• Attend customer team and develop test solutions for design verification: A3718/M3021(CCD signal processor) evaluation for Sonny Atsugi in Tokyo. ABS (Automotive Braking System) ASIC testing development for Infineon in Germany/Austria.</li> <li>• Provide global technical support and consulting: States ChipPAC, UTAC, Freescale and Amkor, Deliver training and seminars to the customer's engineers and company junior engineer.</li> </ul>

#### D. Skills And Abilities

- Have a good Experience in Automated Test Equipment: Catalyst, Flex and Credence Quartet, Logic100, and Duo series and V93000 Pin Scale/Smart Scale. Experience on Semiconductor Testing: Digital, Memory and Mix-Signal and RF.
- Well-informed about using C, VC++, VB, Java and Perl script. Have experience in development of Auto Testing System (tester, handler and thermostream automation).
- Solid understanding of Electronics engineering fundamentals, digital, analog, mixed-signal, power management and RF circuit analysis techniques.
- Able to work confidently with lab equipment like Oscilloscopes, Spectrum analyzers, Logic Analyzers (for mixed signal), Network Analyzers (for RF), etc.
- Strong problem solving skills with some level of project management experience.
- Possess a customer-focus attitude and have experience in customer management.
- Self-driven attitude for continuous learning.

#### E. Training

2004.7~2004.9	Digital / Mix-Signal Test Fundamental
2004.9~2004.10	Catalyst Mixed Signal programming
2004.10~2004.11	TQM(Total Quality Management) and Project Management
2004.11~2004.12	FLEX/microFLEX Mixed Signal Programming
2006.1~2006~1	FLEX/microFLEX Asynchronous Trigger
2008.11~2008.11	T2000 Basic Application
2009.1~2009.1	T2000 Test Class Primer
2010.8~2010.9	V93000 SOC Basic User Training
2010.10~010.11	V93000 SOC Mix-Signal User Training
2011.1~2011.2	V93000 Port Scale RF User Training