Furkan Yazici

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SUMMARY

Electrical Engineering, specializing in back-end app development and data structure, seeking a Software Engineering position

EDUCATION

UNIVERSITY OF ILLINOIS AT URBANA - CHAMPAIGN, GRAINGER COLLEGE OF ENGINEERING — Urbana - Champaign, IL
Bachelor of Science in Electrical Engineering — August 2019 – Present
Expected graduation: May 2022 — GPA: 3.73/4.00

- Related Coursework: Analog Signal Processing, Communications, Digital Signal Processing, Digital Systems Laboratory, Control Systems, Electronic Circuits, Digital IC Design, Embedded DSP Laboratory, Multimedia Signal Processing, Semiconductors

WORK EXPERIENCE

BAYKAR DEFENSE — Istanbul, Turkey
Avionics and Embedded Systems Intern — June 2020 – August 2020

- Researched communication protocols for improving the existing systems
- Designed and simulated power circuits such as voltage regulator circuits using LTSpice
- Studied the design and assembly of various avionic components
- Tested and reported avionic components such as PCBs and power converters for durability and functionality
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ALARKO HOLDING A.S. — Istanbul, Turkey
IT Intern — December 2018 – January 2019

- Sorted IT support emails and forwarded them to relevant IT associates
- Provided technical support for employee hardware and devices
- Created web-based forms for the sub-organizations of the company

RESEARCH

BIOACOUSTICS RESEARCH LABORATORY AT UIUC — Urbana, United States
Lab Assistant — January 2021 – Present

- Took ultrasonic images of phantoms (material used as a reference in scanning organ tissues) using transducers
- Performed frequency analysis of scanned data with a previously developed MATLAB program and reported the results

PROJECTS

SOC WITH NIOS II IN SYSTEM VERILOG

- Established the SPI communication between NIOS II processor and MAX3421E USB
- Implemented a basic ball game that can be played using a USB keyboard to test the functionality of the system
- Established the VGA communication between FPGA and a monitor to demonstrate the game

DC POWER SUPPLY DESIGN

- Designed and simulated AC/DC power supply using nonlinear circuit elements
- Used Zener diode to regulate the output voltage at the operating point of 4.7V
- Filtered the output in order the meet the 2% ripple voltage specification

SKILLS

LANGUAGES AND PROGRAMS: C, C++, System Verilog, Python, MATLAB, LabView, LTSpice, HSPICE, Cadence
LANGUAGES: English and Turkish