

## Fangzhou Ye

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### EDUCATION

- Bachelor in Electronic Science and Engineering: September, 2019-June, 2023.

College of Info. Science and Electronic Engineering, Zhejiang University, Hangzhou, Zhejiang, China.

- Ph.D. in Computer Engineering: August, 2023-Present.

Department of Electrical and Computer Engineering, University of Central Florida, Orlando, FL, US.

Thesis advisor: Dr. Hao Zheng

### RESEARCH INTEREST

- Computer Architecture, Machine Learning, Accelerator Design, AI/ML systems, Efficient AI.

### SKILLS

- Programming languages: C, C++, Python and Pytorch, MATLAB, Verilog.

### MAJOR PROFESSIONAL EXPERIENCE AND LEADING PROJECTS

Student Research Training Program (at the national level) in Zhejiang University      June, 2021 – May, 2022

- Conducted research on **model segmentation** methods for wireless **federated learning**.
- Designed and implemented a **distributed communication system** using PyTorch in Python.

Internship at Office of Global Engagement, Zhejiang University

September, 2021-December, 2021

- Handled general affairs as a student assistant.

Teaching/Research Graduate Assistant, University of Central Florida

August, 2023-Present

- Research Graduate Assistant. Leading projects on accelerating GNN inference/training systems and analyzing sparsity and irregularity in graph workloads.
- Research Graduate Assistant. Working on the large language model system acceleration, with a focus on graph-related topics such as GraphRAG.
- Teaching Graduate Assistant. Assisting with programming languages, signal processing, and digital design courses.

### SELECTED PUBLICATION

#### PAPERS

- **Fangzhou Ye**, Lingxiang Yin, Amir Ghazizadeh Ahsaei and Hao Zheng. "EGMA: Enhancing Data Reuse and Workload Balancing in Message Passing GNN Acceleration via Gram Matrix Optimization", In Proceedings of the 61th Design Automation Conference (DAC) (Top-tier Conference), San Francisco, CA, June 23-27, 2024.
- Amir Ghazizadeh Ahsaei, Lingxiang Yin, Shilin Tian, **Fangzhou Ye**, Fan Yao, Hao Zheng "Rethinking Tiling and Dataflow for SpMM Acceleration: A Graph Transformation Framework", In Proceedings of IEEE/ACM International Symposium on Microarchitecture (MICRO) (Top-tier Conference), Seoul, South Korea, October 18-22, 2025.
- **Fangzhou Ye**, Lingxiang Yin, and Hao Zheng, "Scaling Graph Neural Network Training via Geometric Optimization", in Proceedings of IEEE International Symposium on High-Performance Computer Architecture (HPCA) (Top-tier Conference), Sydney, Australia, January 31-February 4, 2026.

#### PATENTS

- Shi Binpu, **Ye Fangzhou**, Liu Shengli, Chen Haoyang, Yu Guanding, "A distributed communication and method", submitted to China National Intellectual Property Administration (CNIPA) 2022.

## **AWARDS**

- ORCGS Doctoral Fellowship, UCF, 2023-2024.
- Future Faculty Laureates (FFLs) Program for Doctoral Students, UCF, 2024-2026.
- MICRO Student Presentation Grant, MICRO, 2025

## **PROFESSIONAL ACTIVITIES**

Conference Review

- DAC'24, IPDPS'24, ICCAD'24, DAC'25, ISPASS'25, MLSys'26

Journal Reviewer,

- IEEE Transactions on Sustainable Computing (TSUSC); IEEE Transactions on Computers (TC)