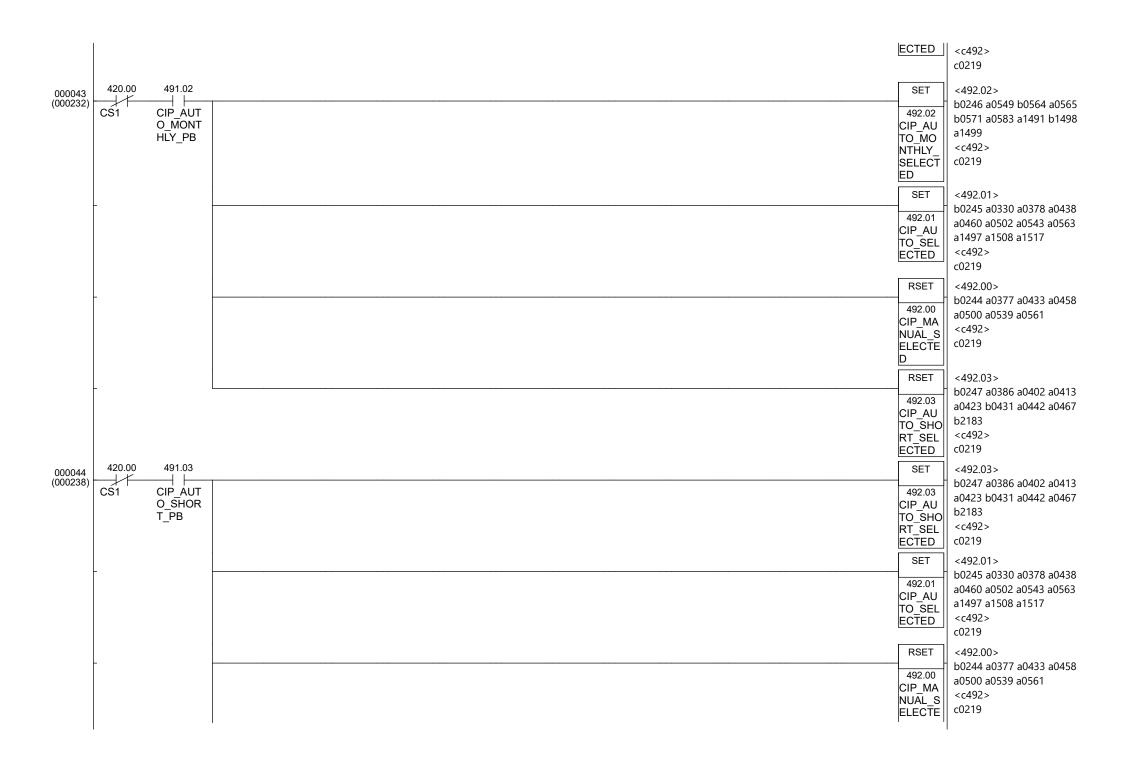
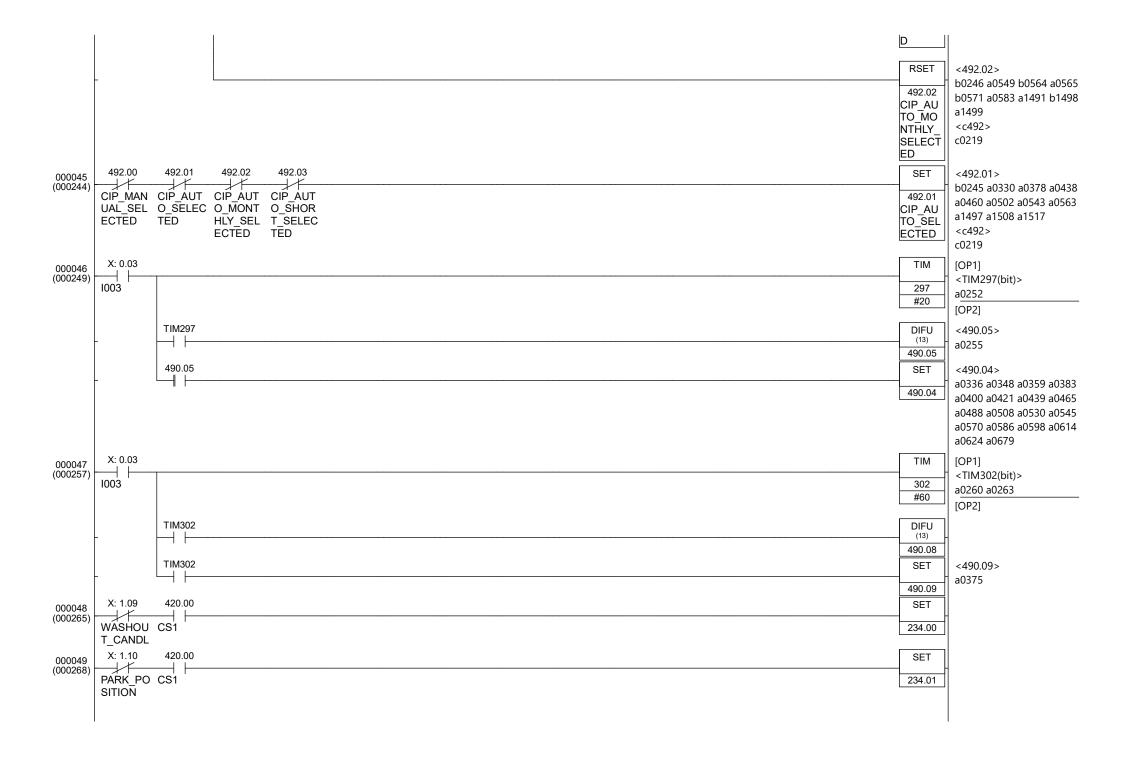
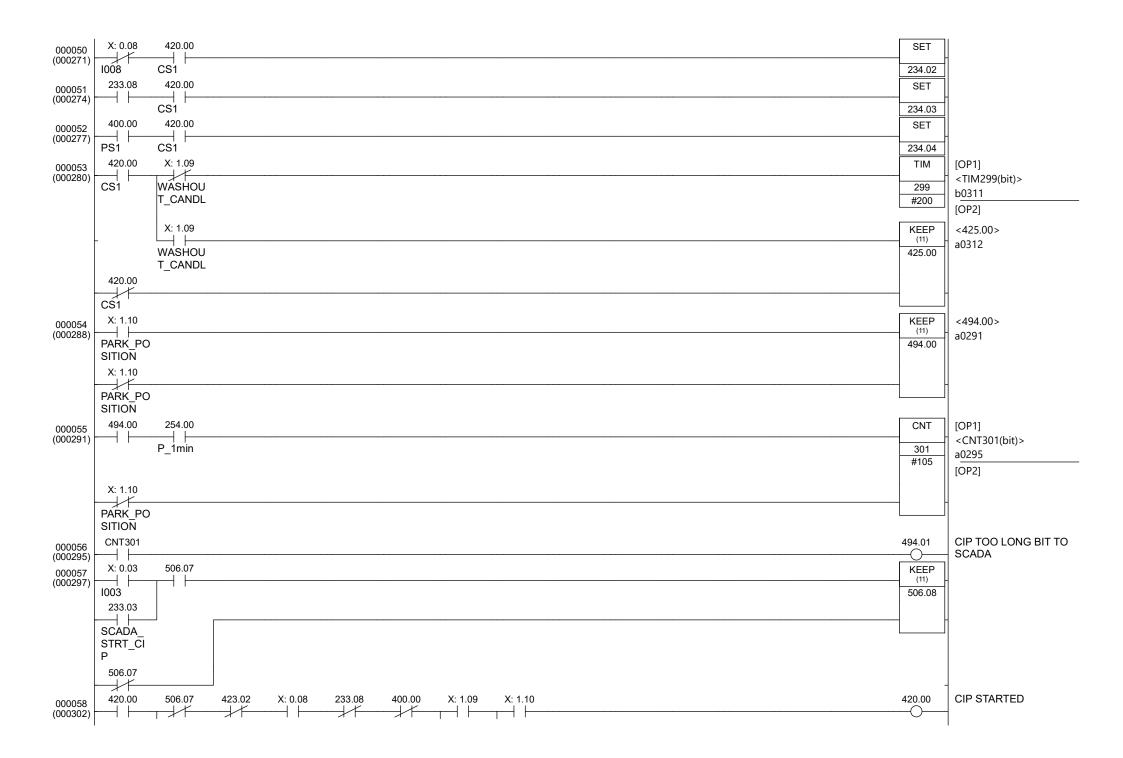
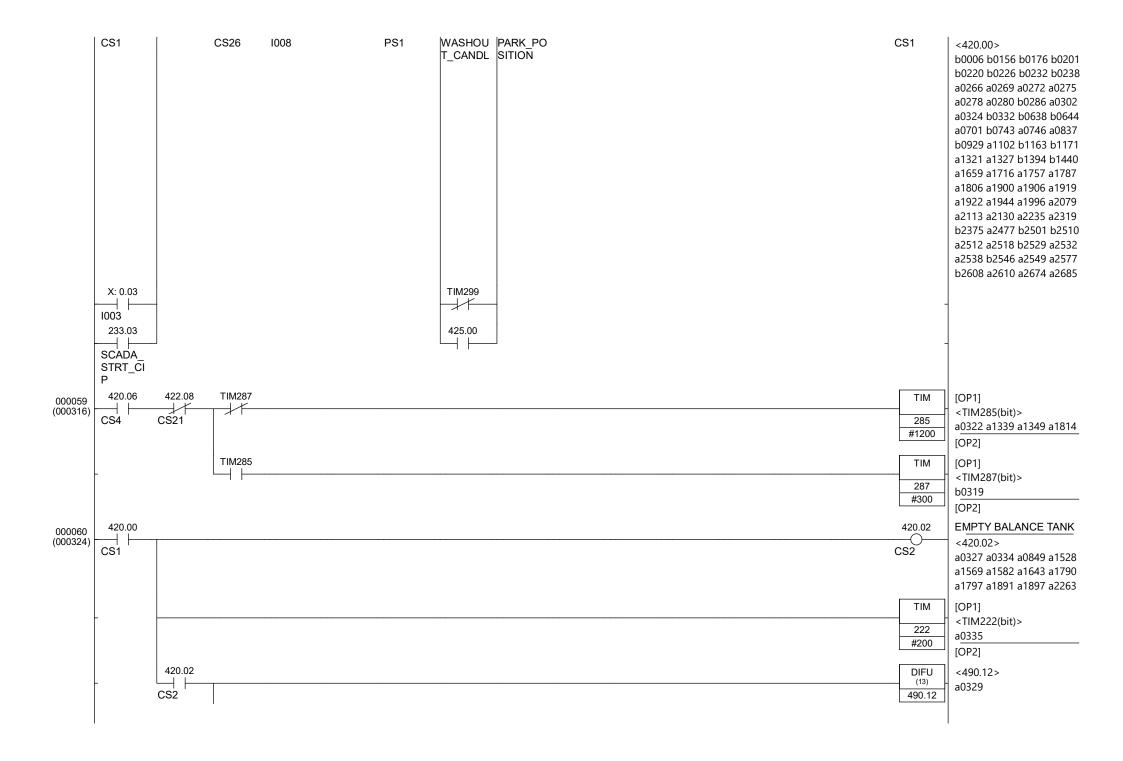


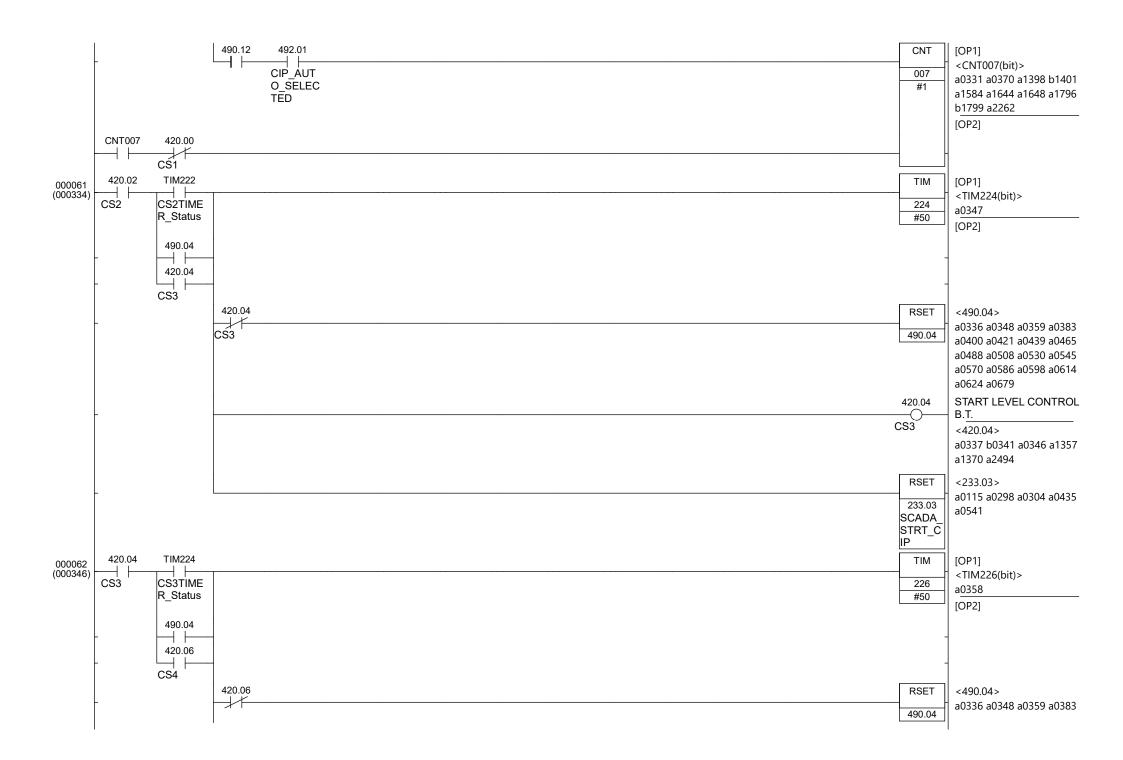
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			RSET	<492.02> b0246 a0549 b0564 a0565
			492.02 CIP_AU TO_MO NTHLY_ SELECT ED	b0571 a0583 a1491 b1498 a1499 <c492> c0219</c492>
			RSET	<492.03>
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	420.00 491.01 CS1 CIP_AUT O_PB		SET	<492.01> b0245 a0330 a0378 a0438
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	_		RSET	<492.00>
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	_		RSET	<492.02> b0246 a0549 b0564 a0565
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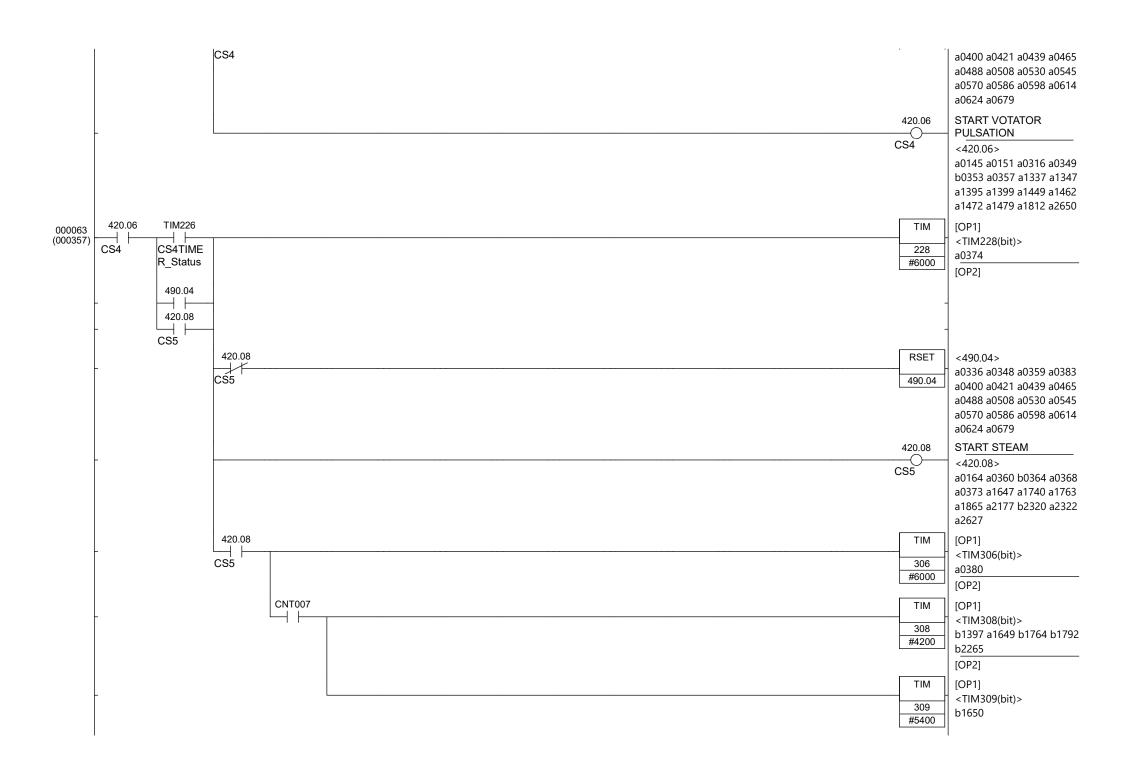


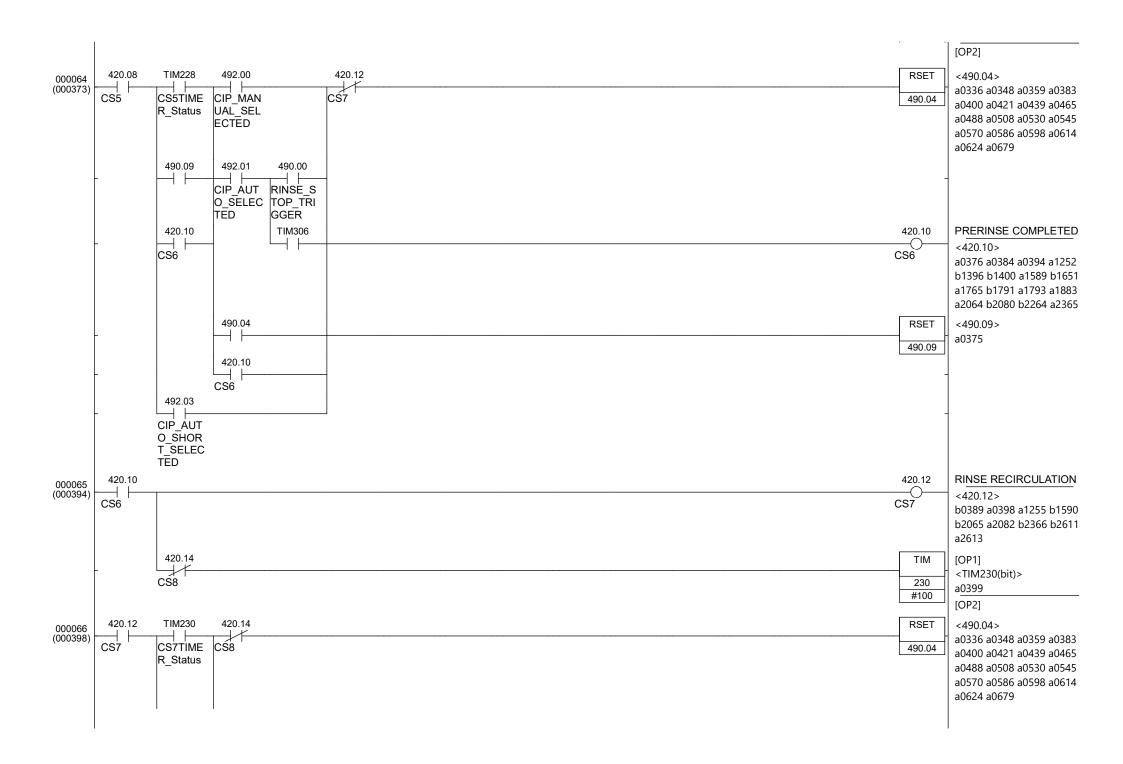


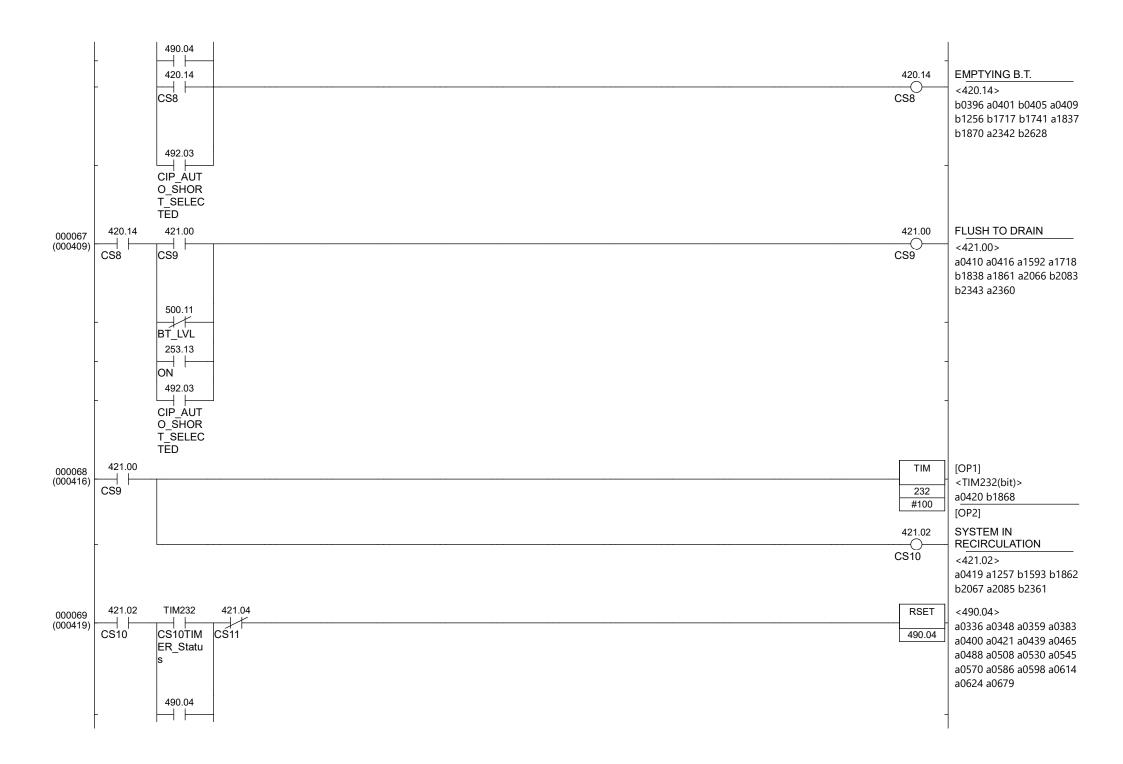


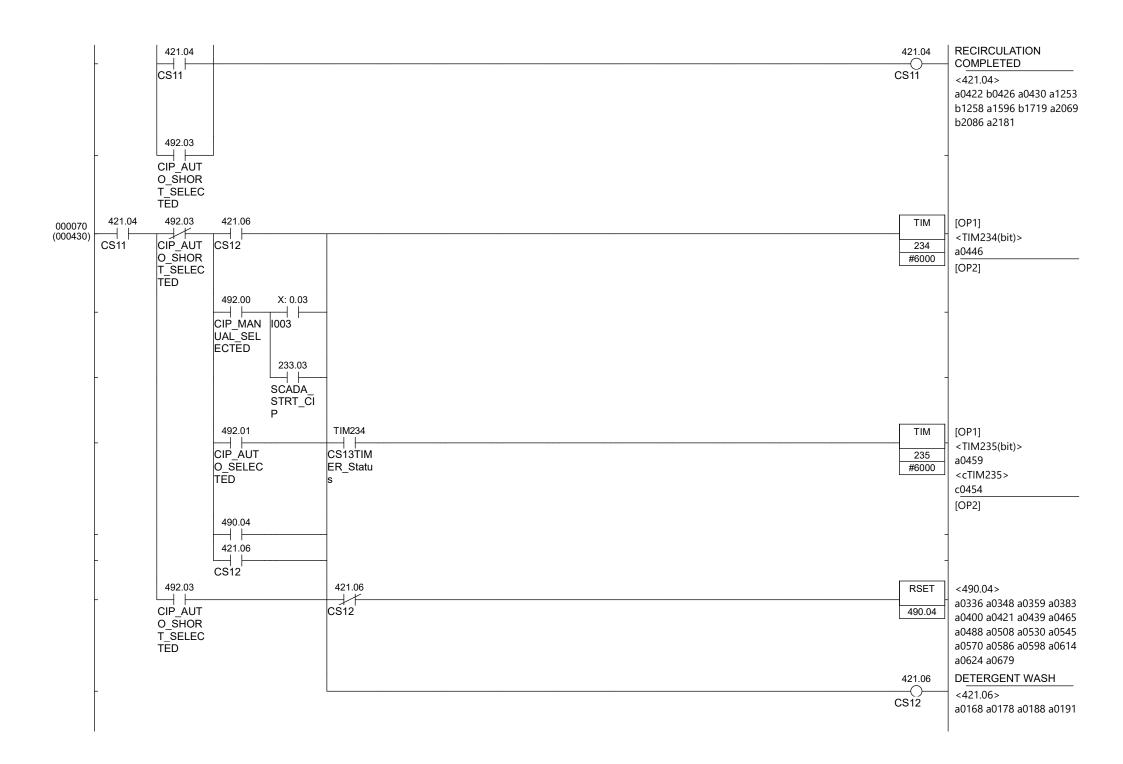


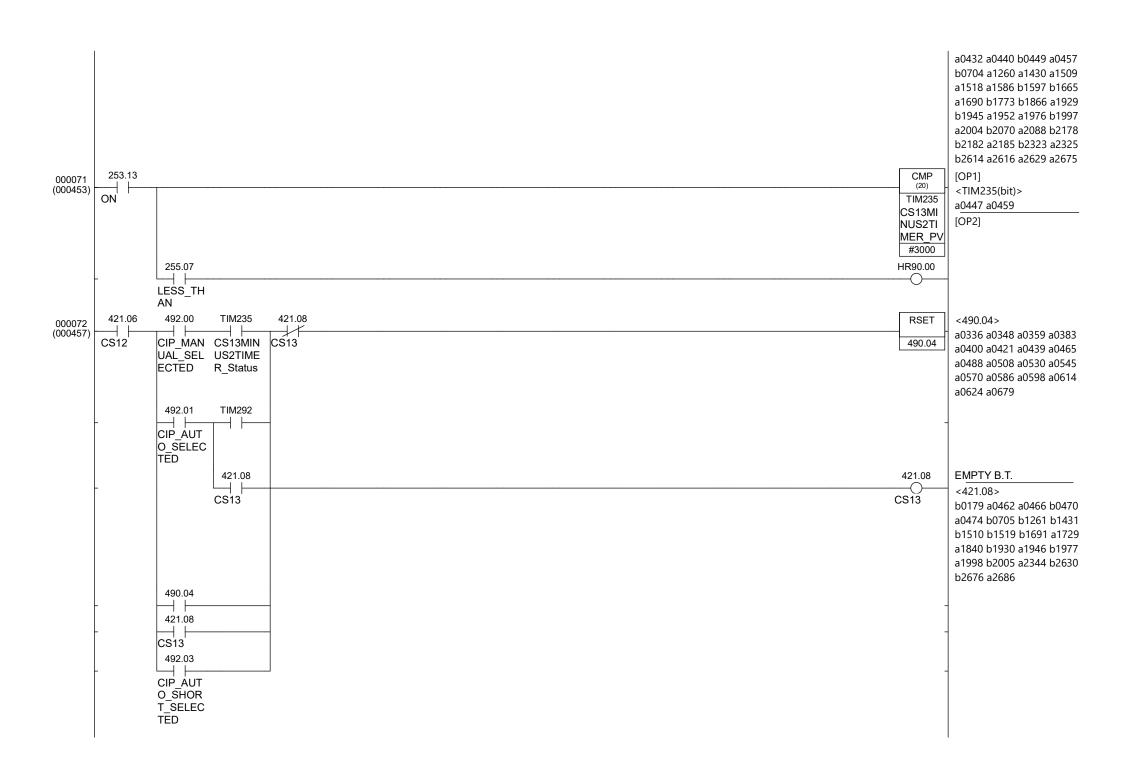


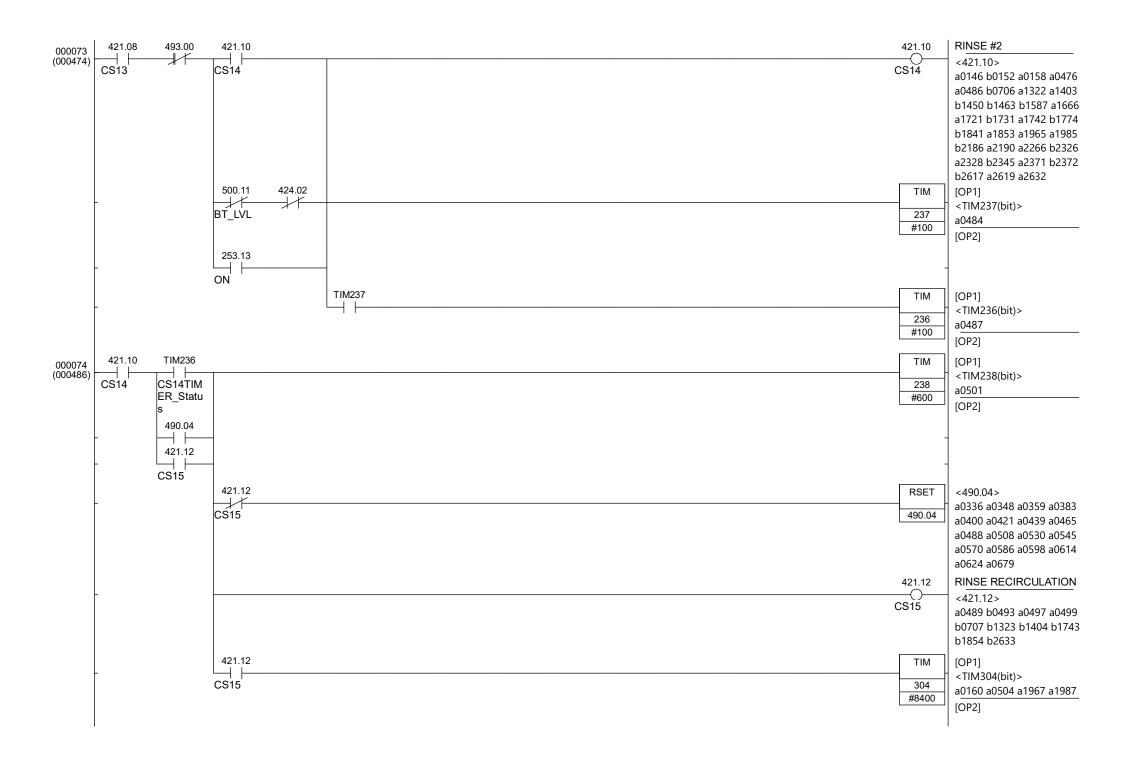


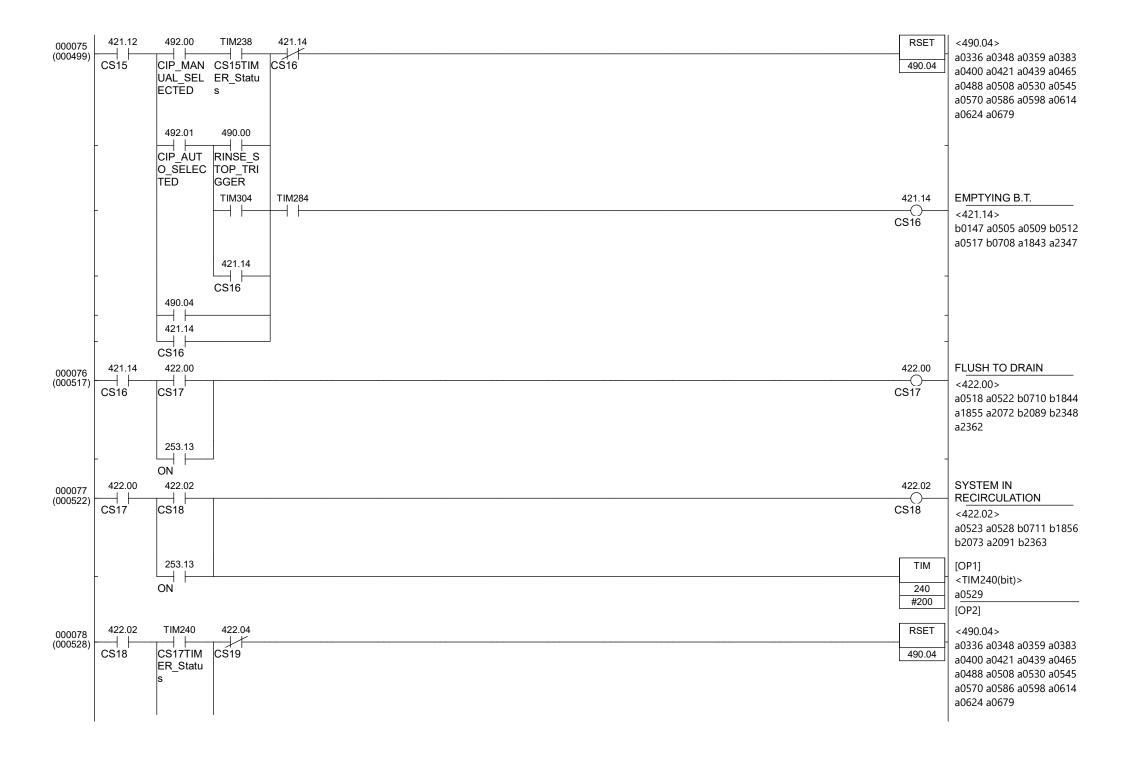


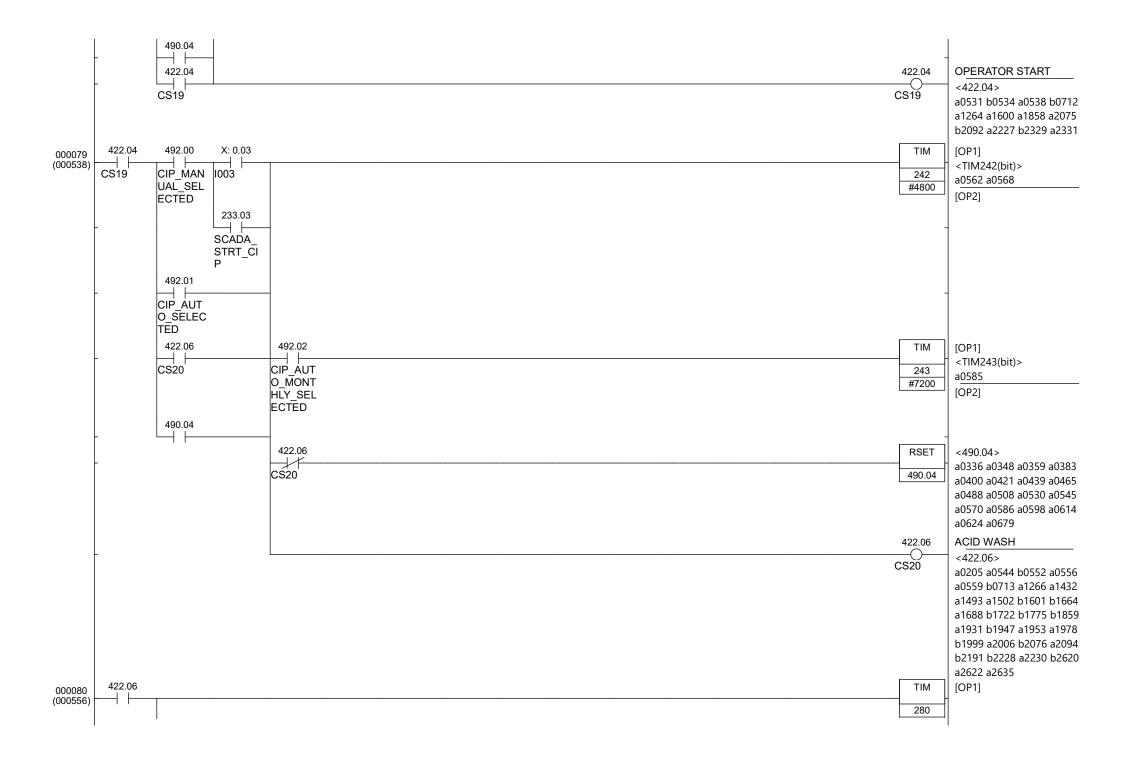


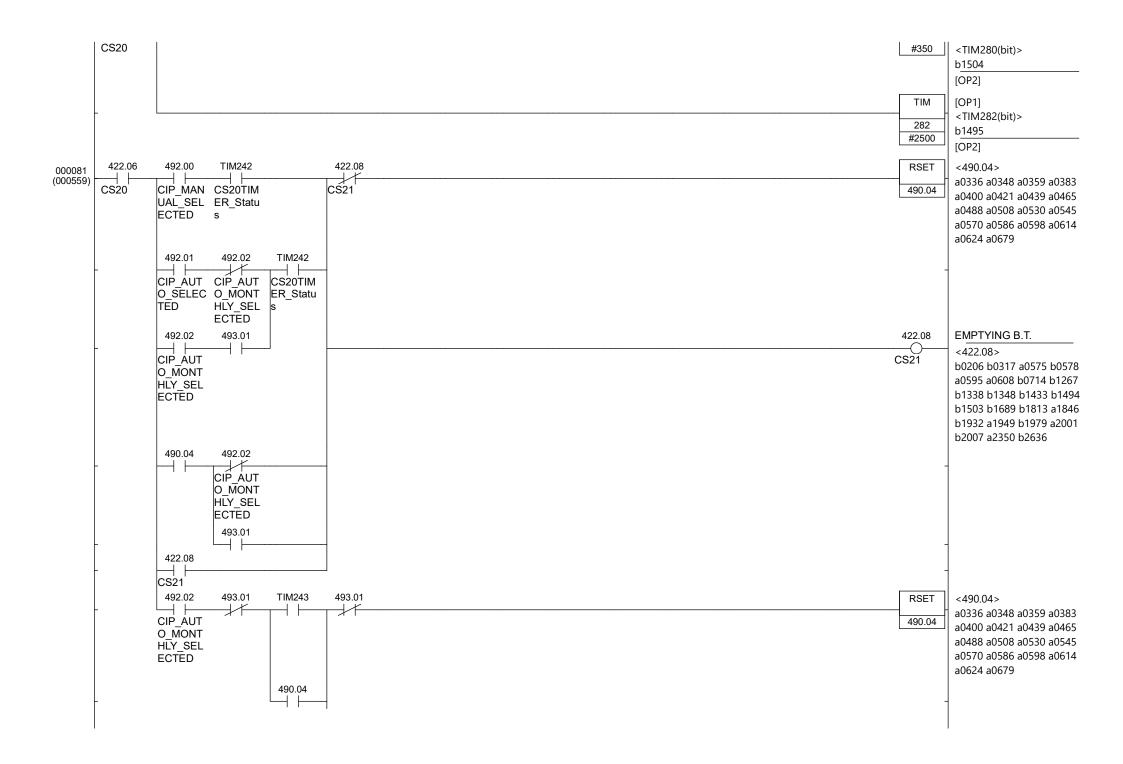


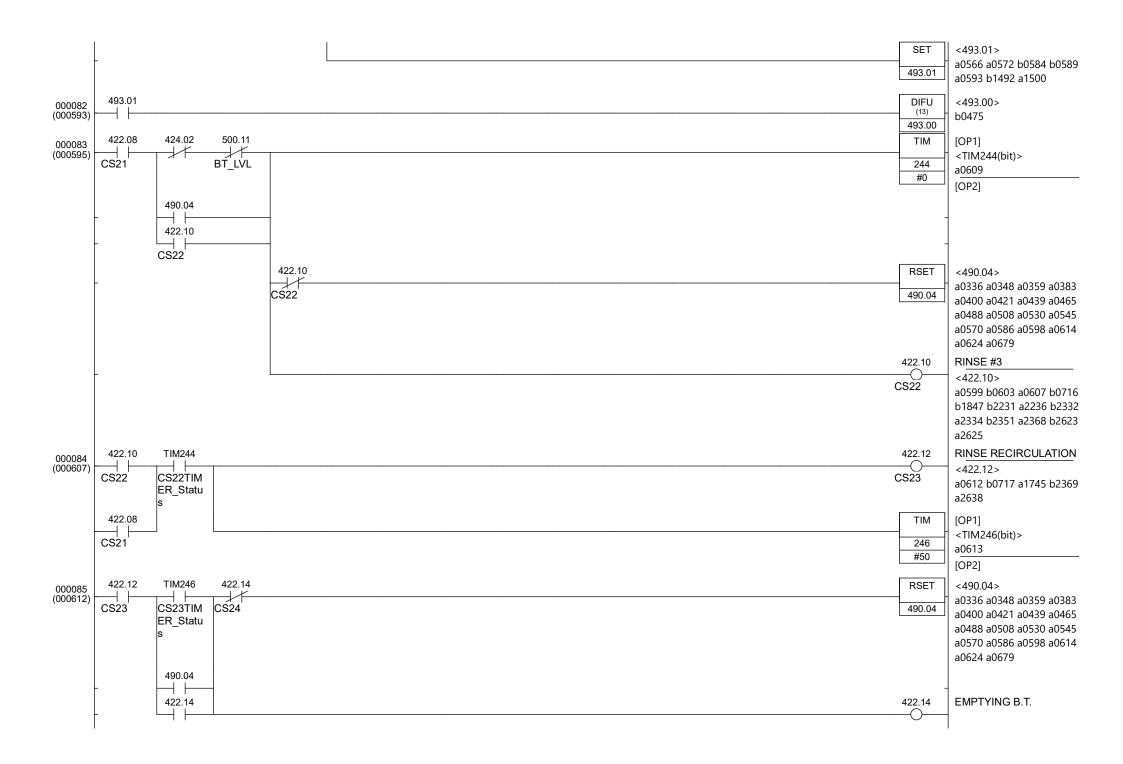


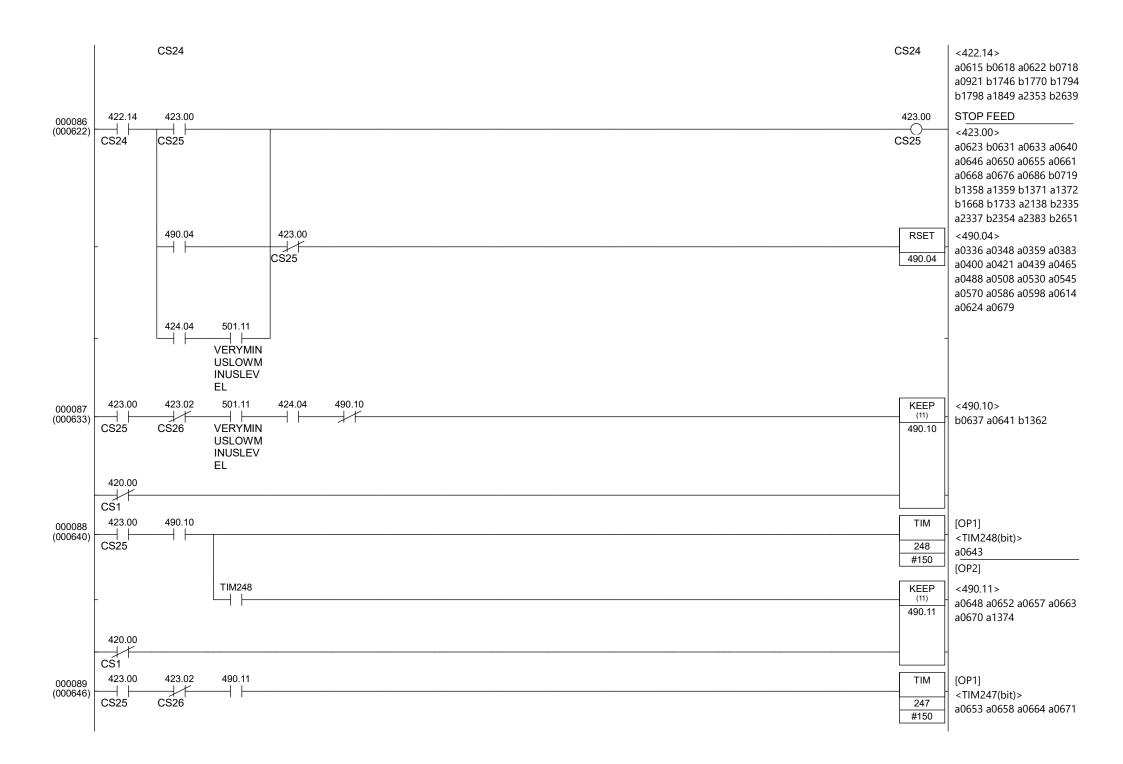


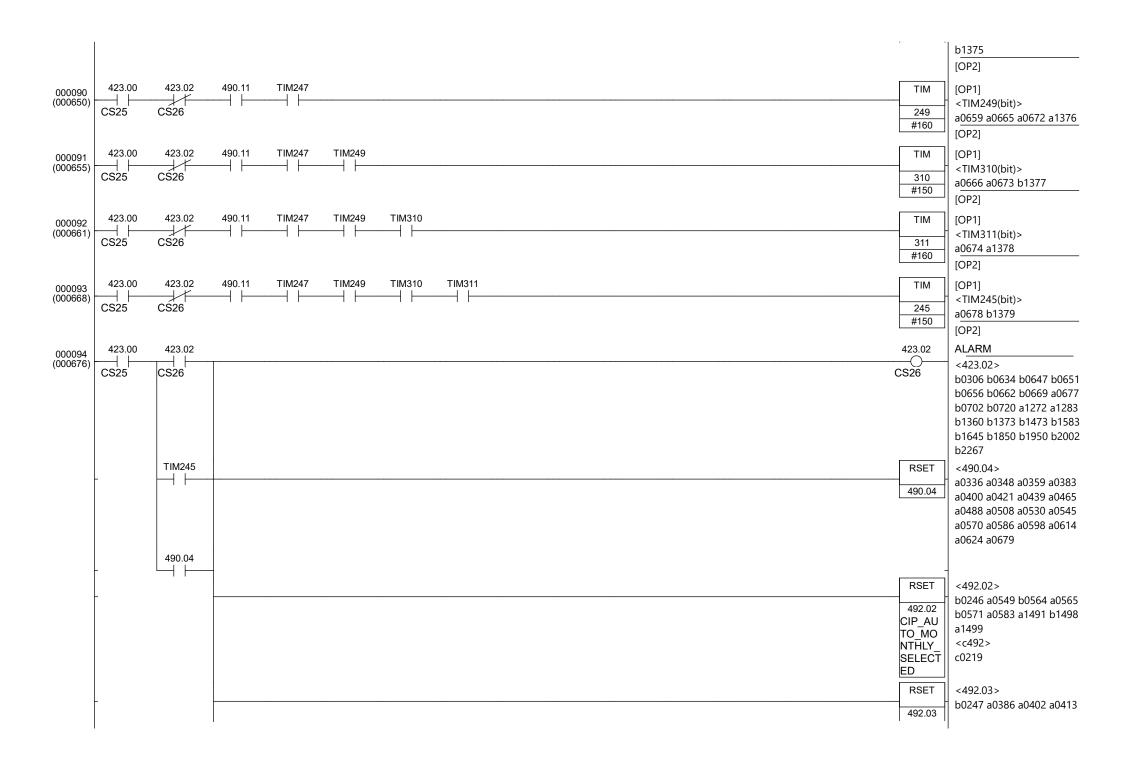


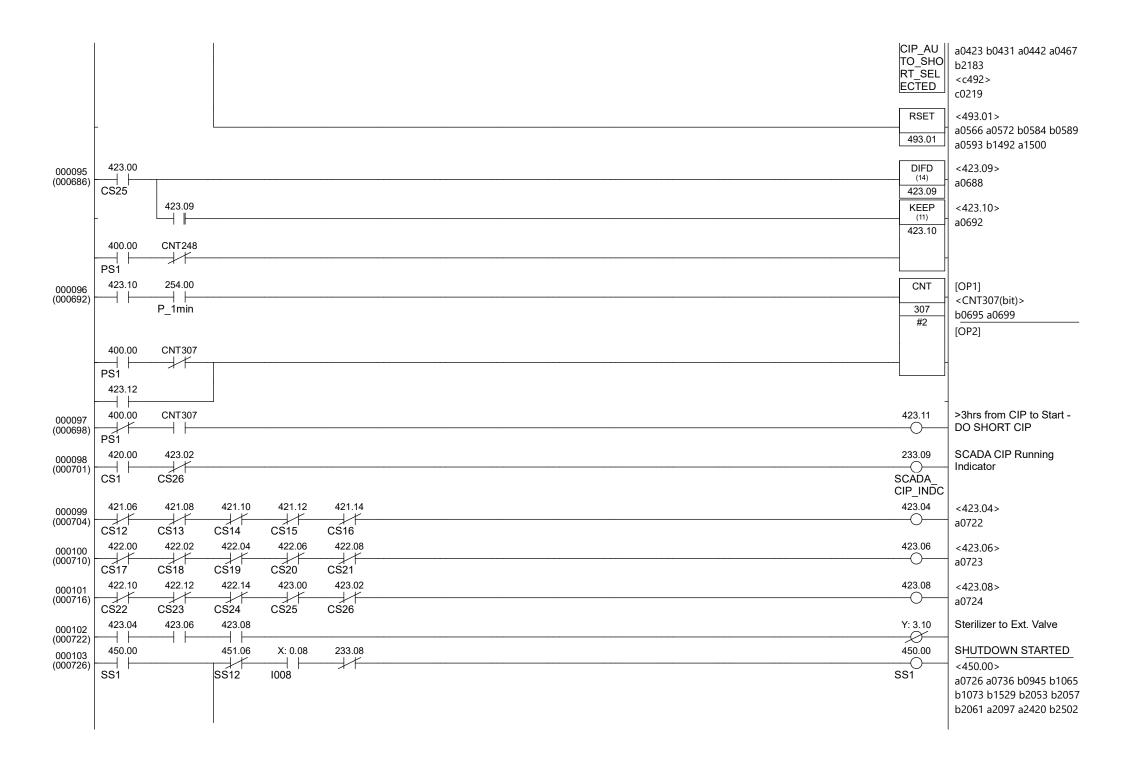


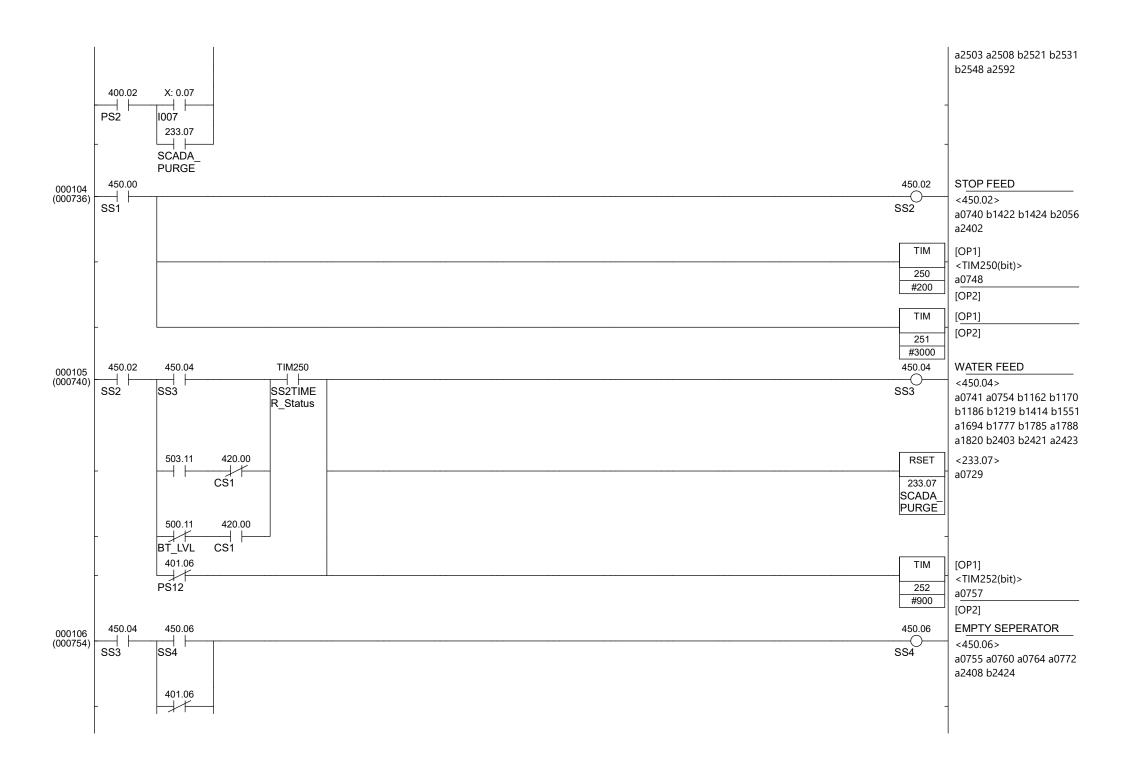


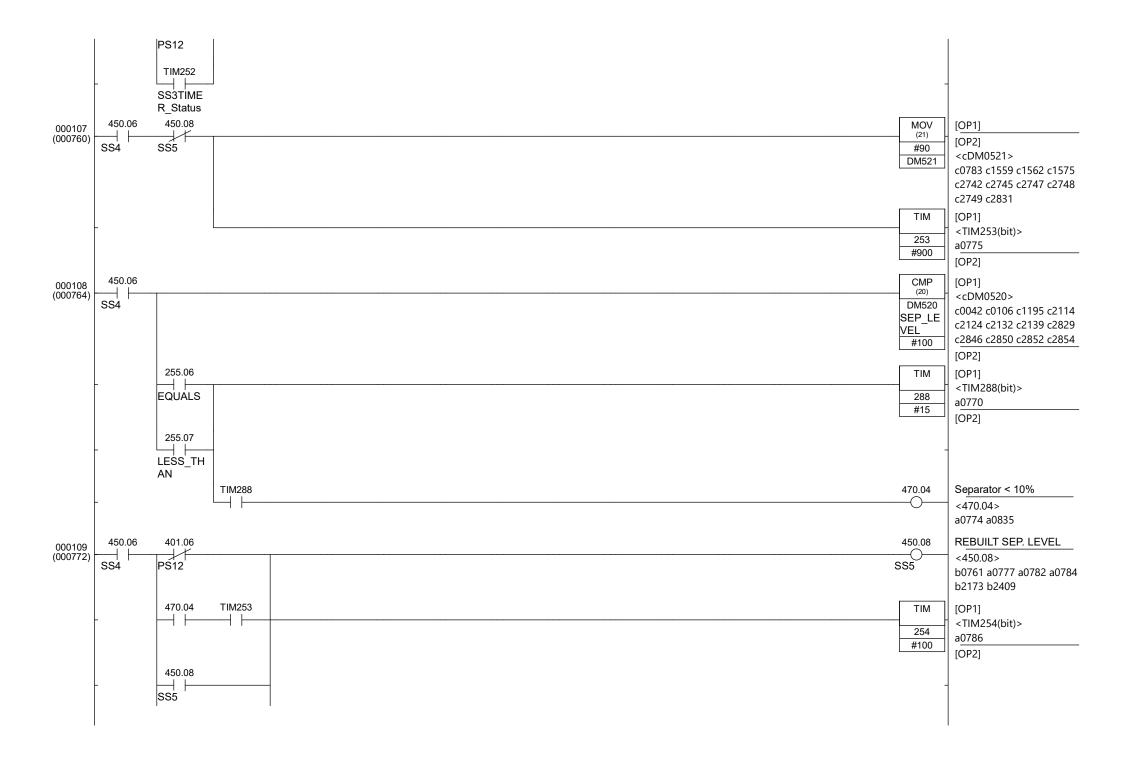


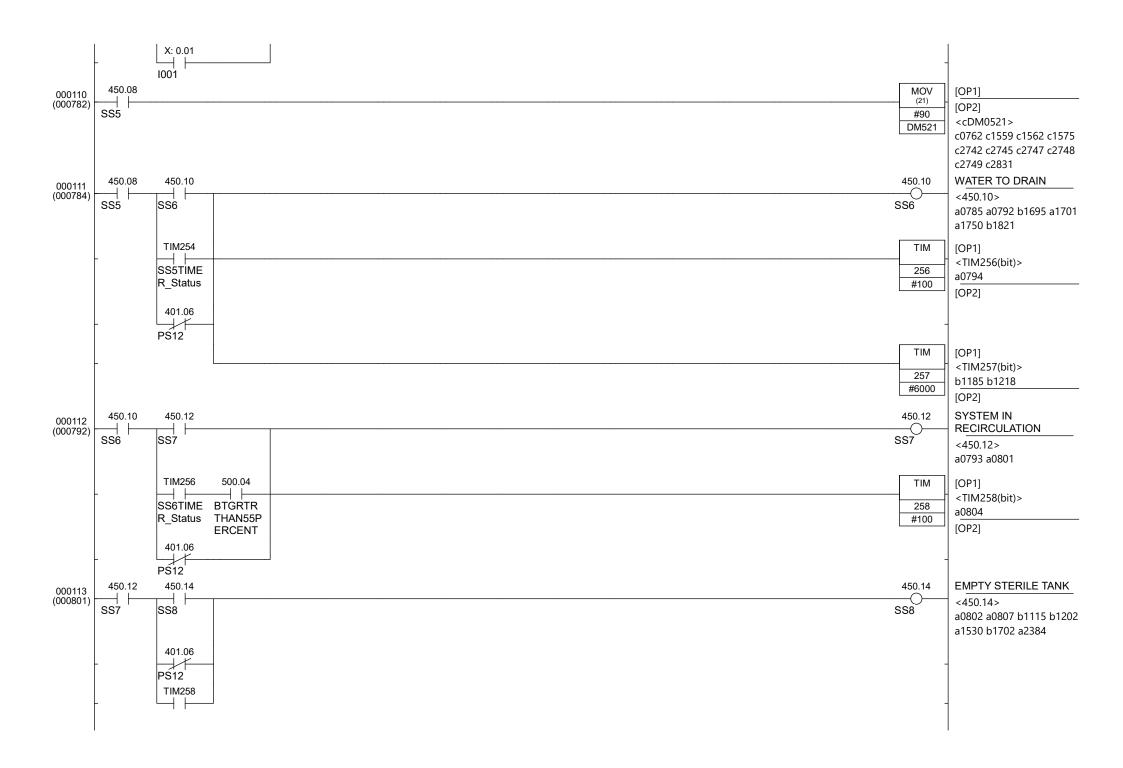


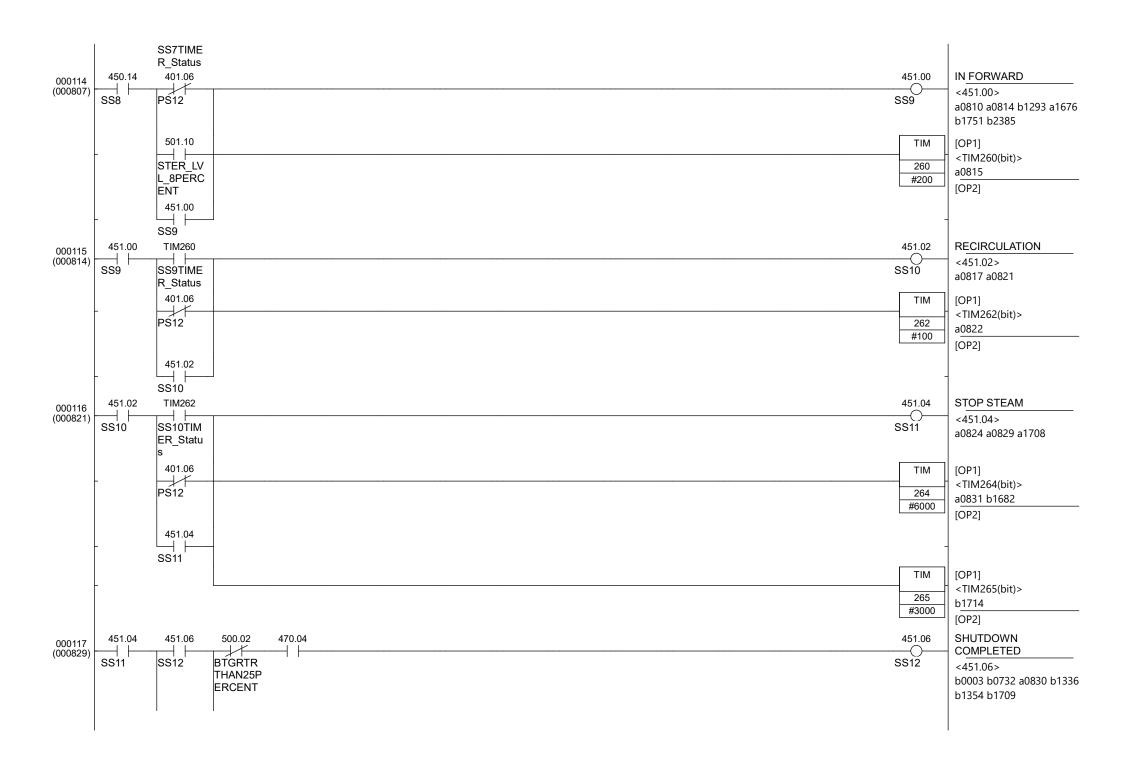


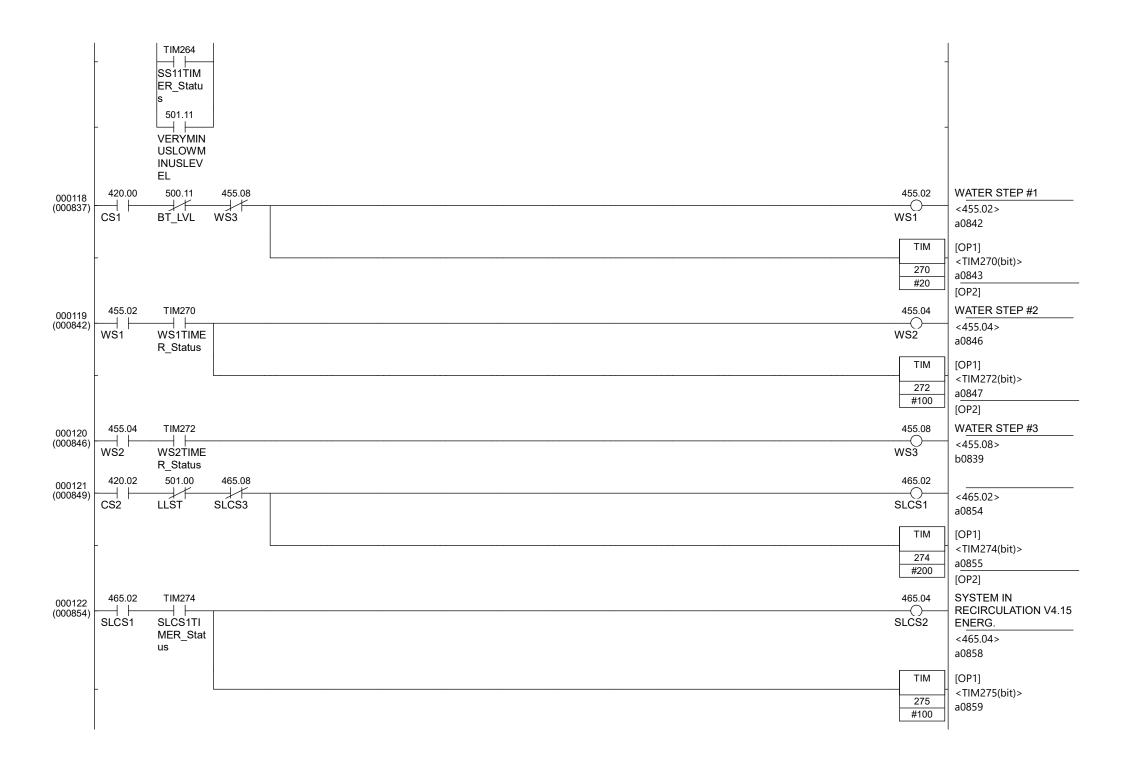


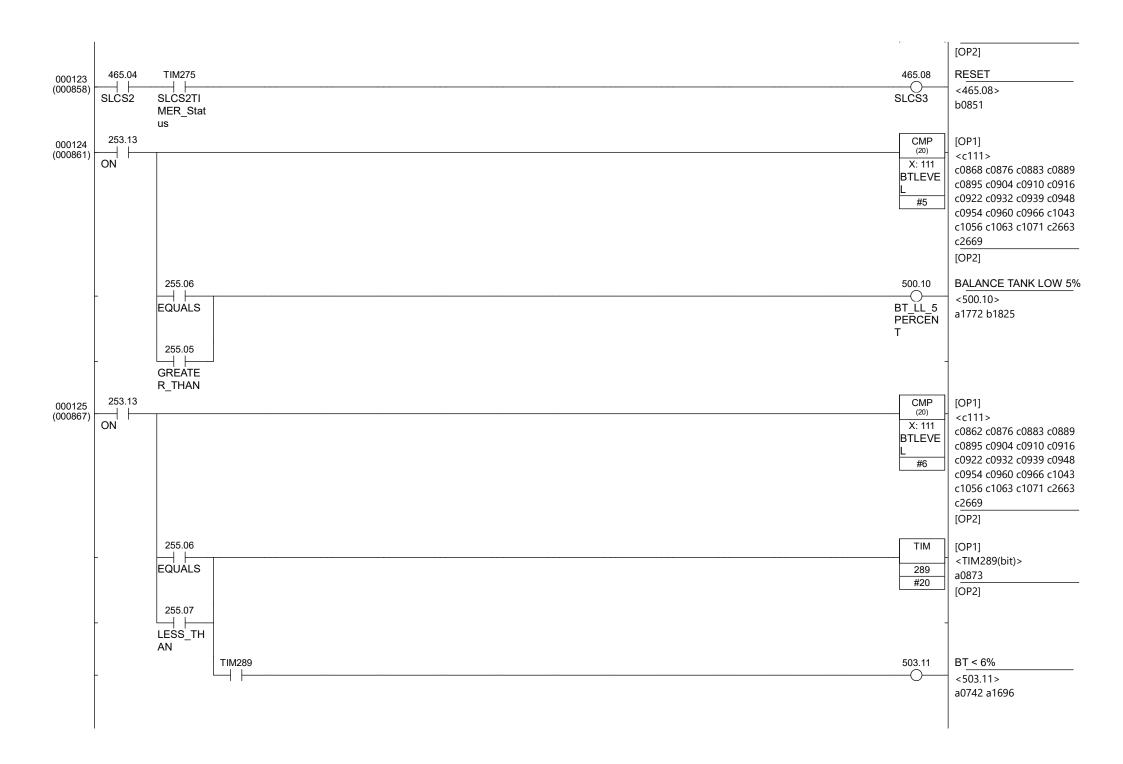


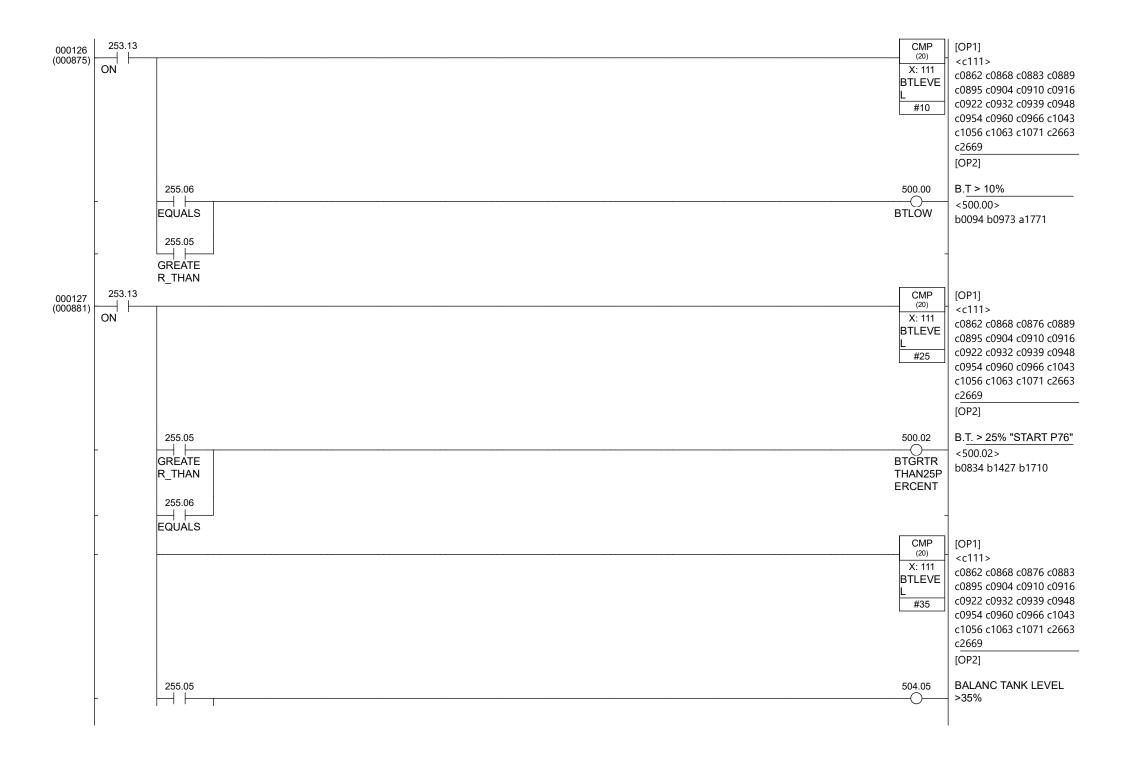


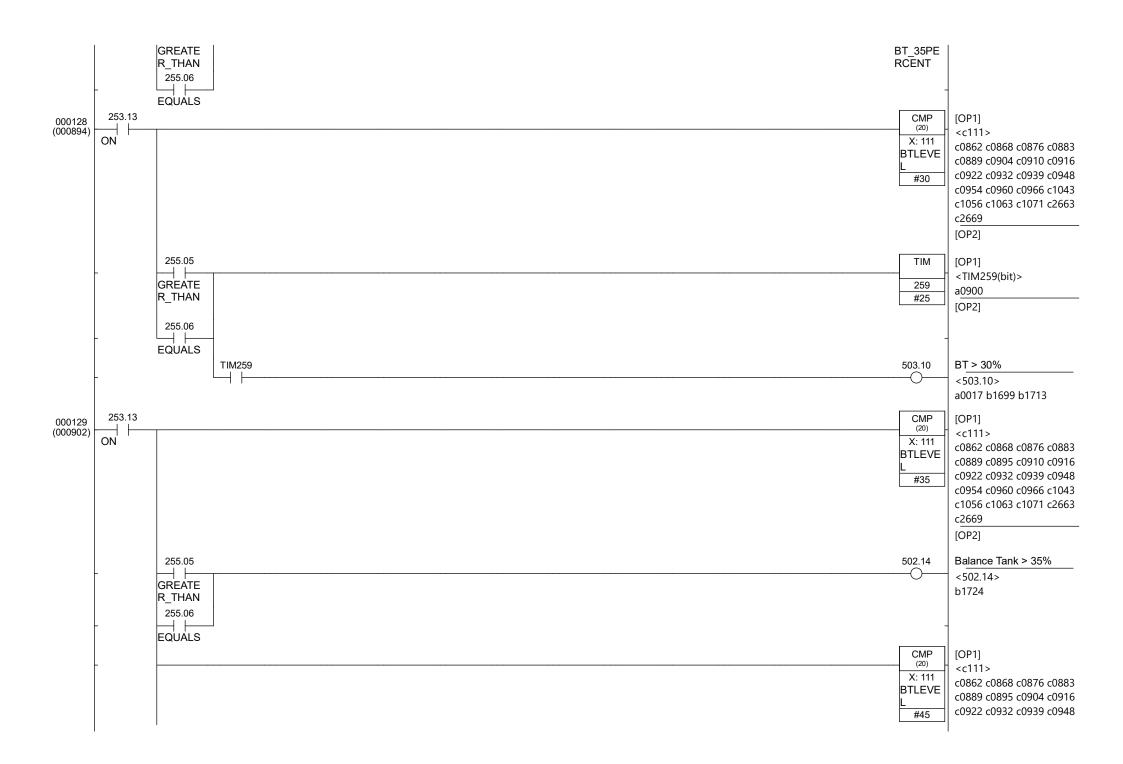


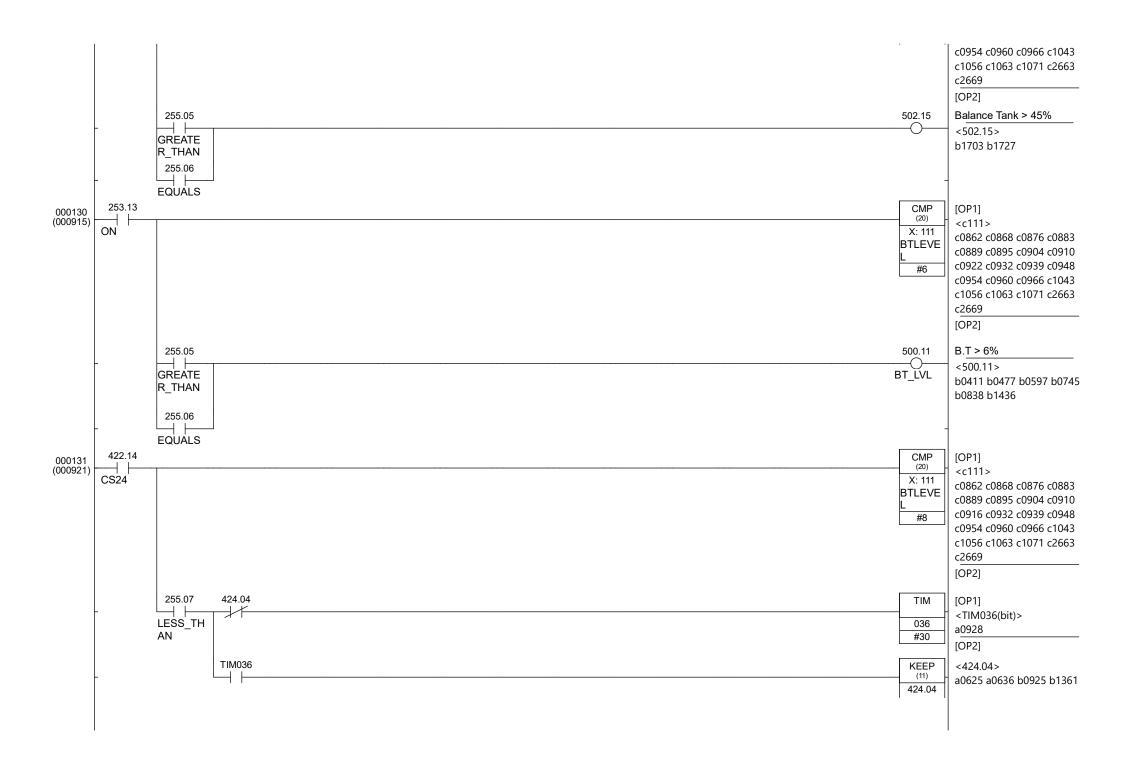


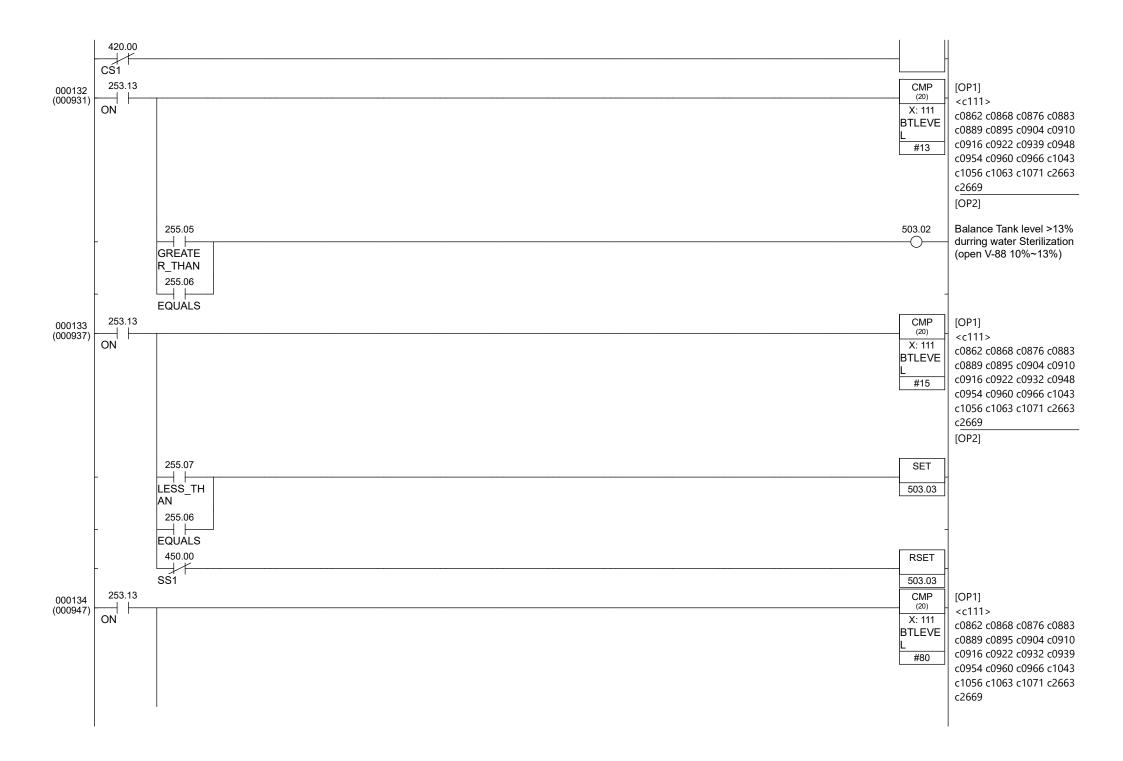


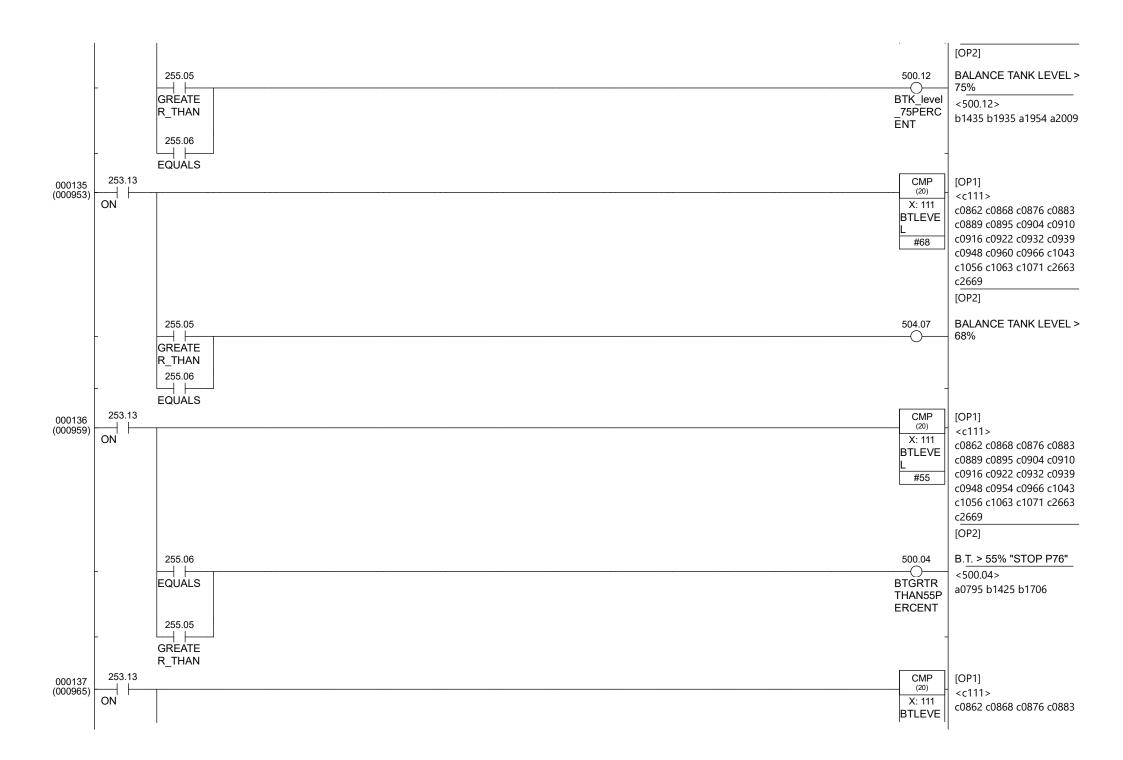


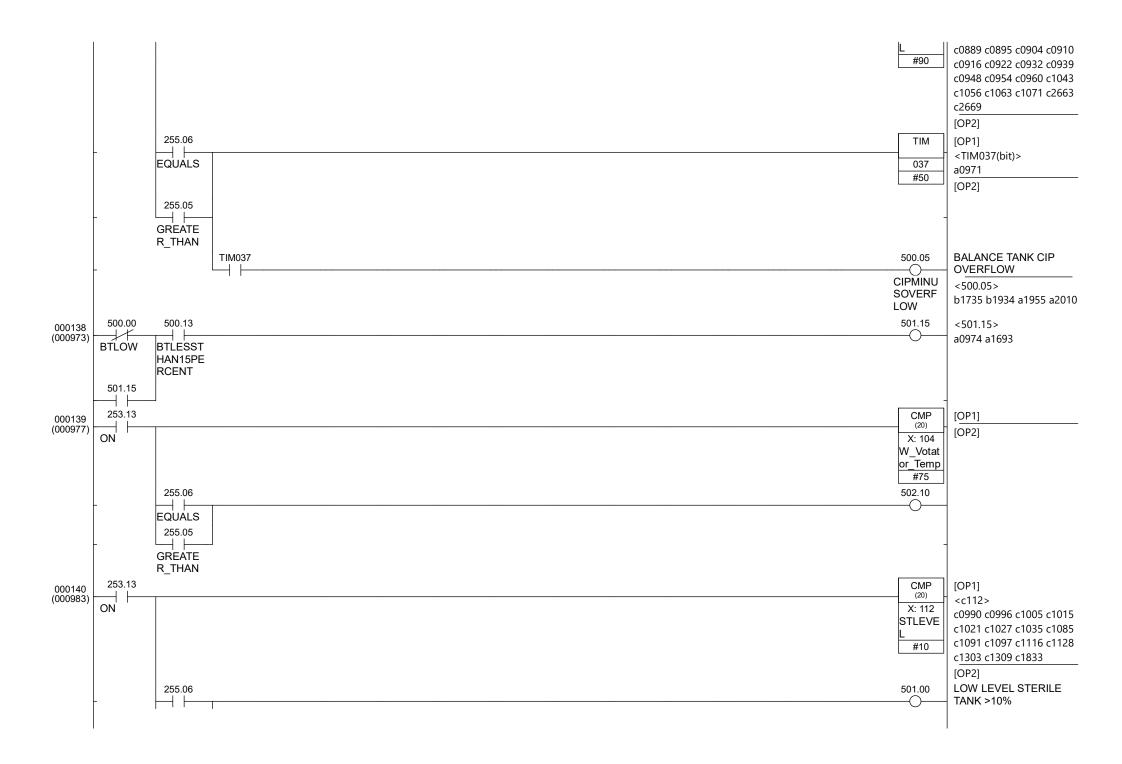


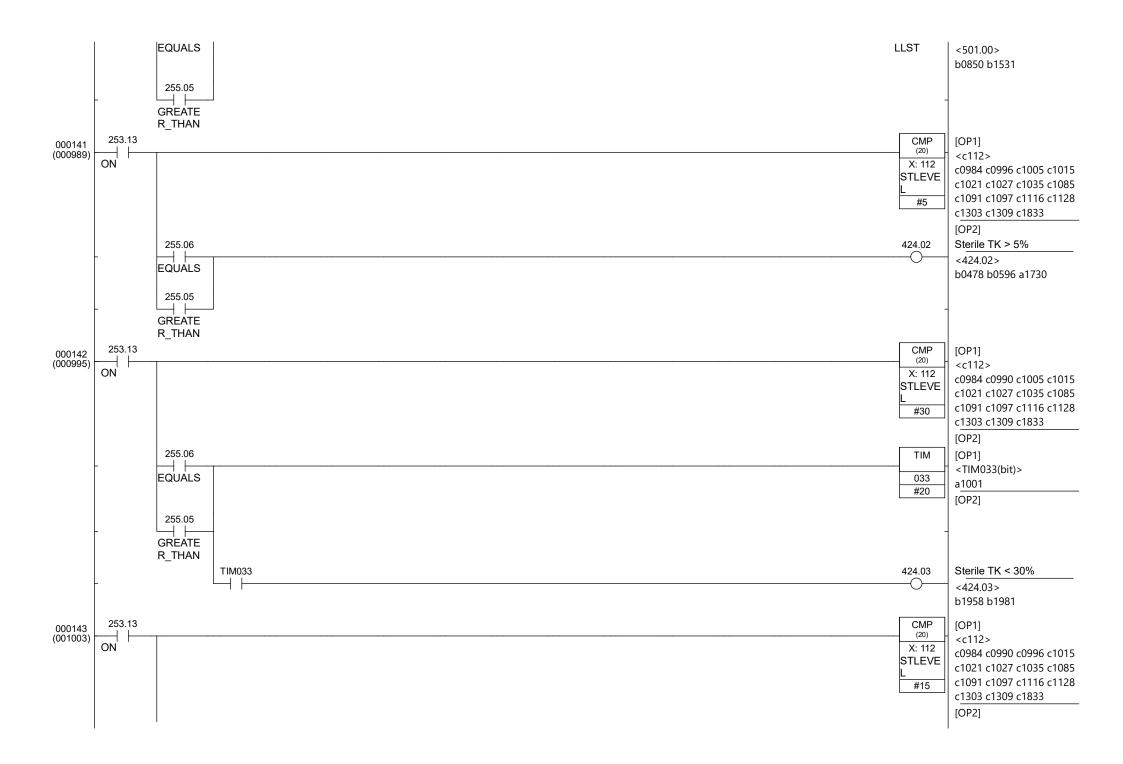


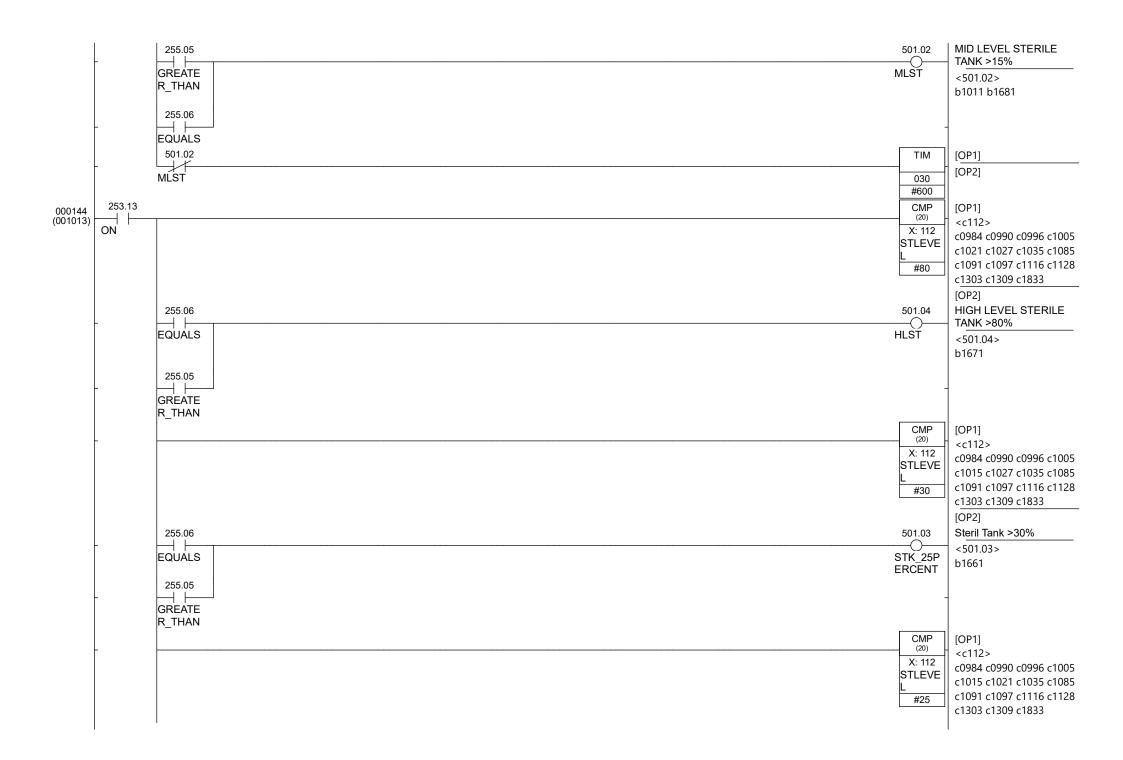


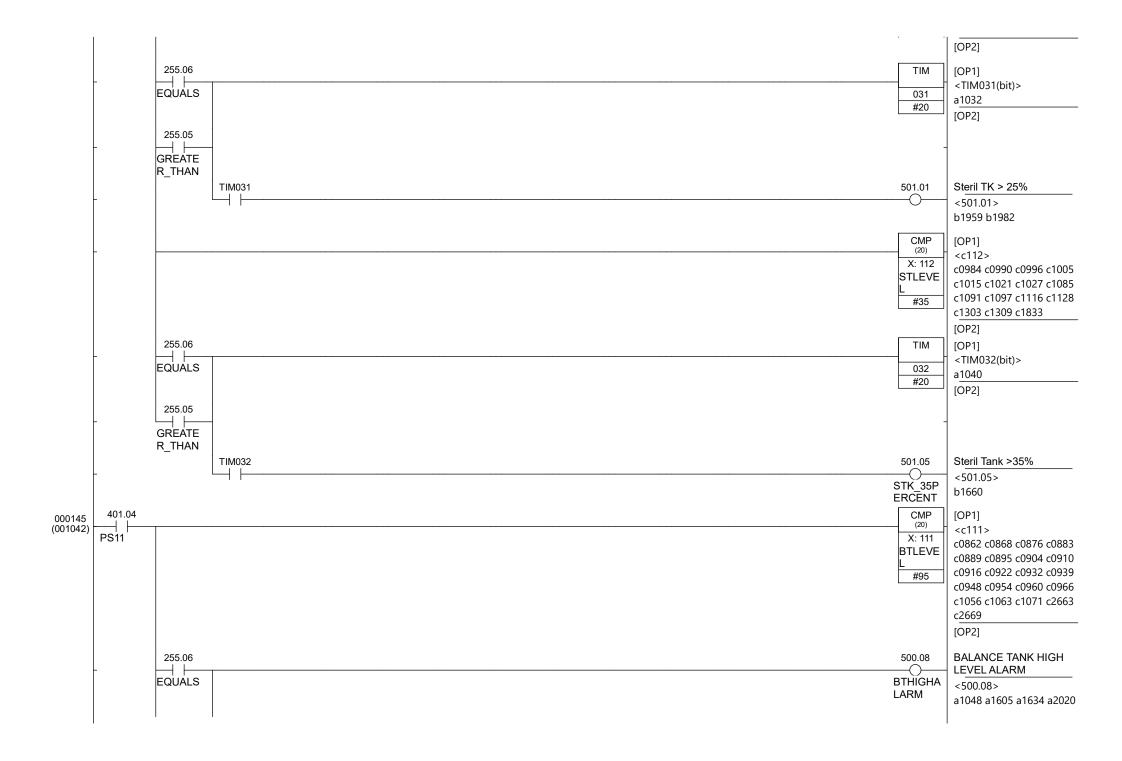


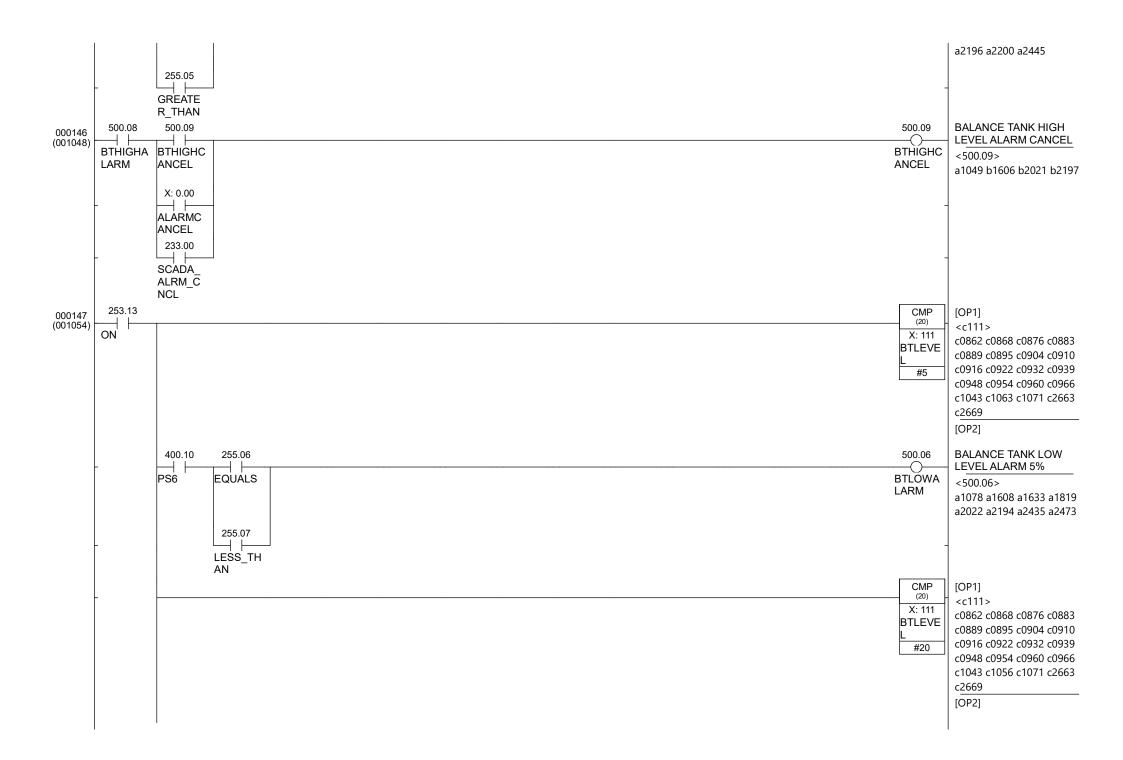


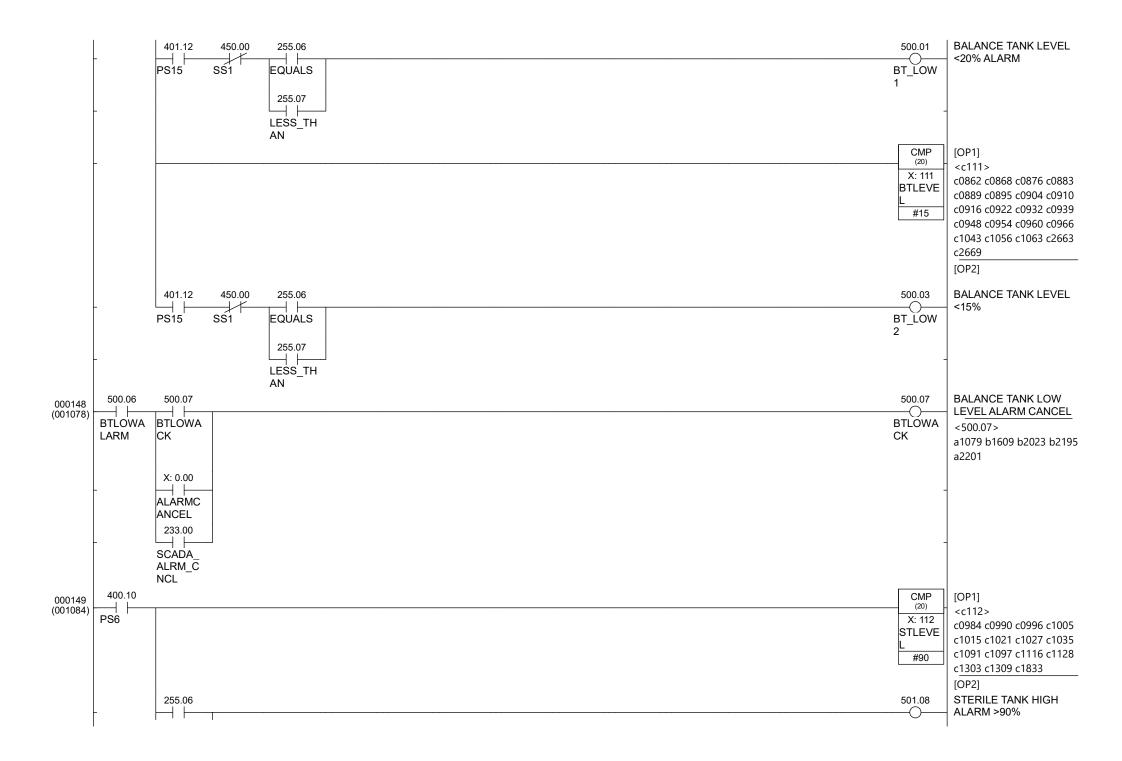


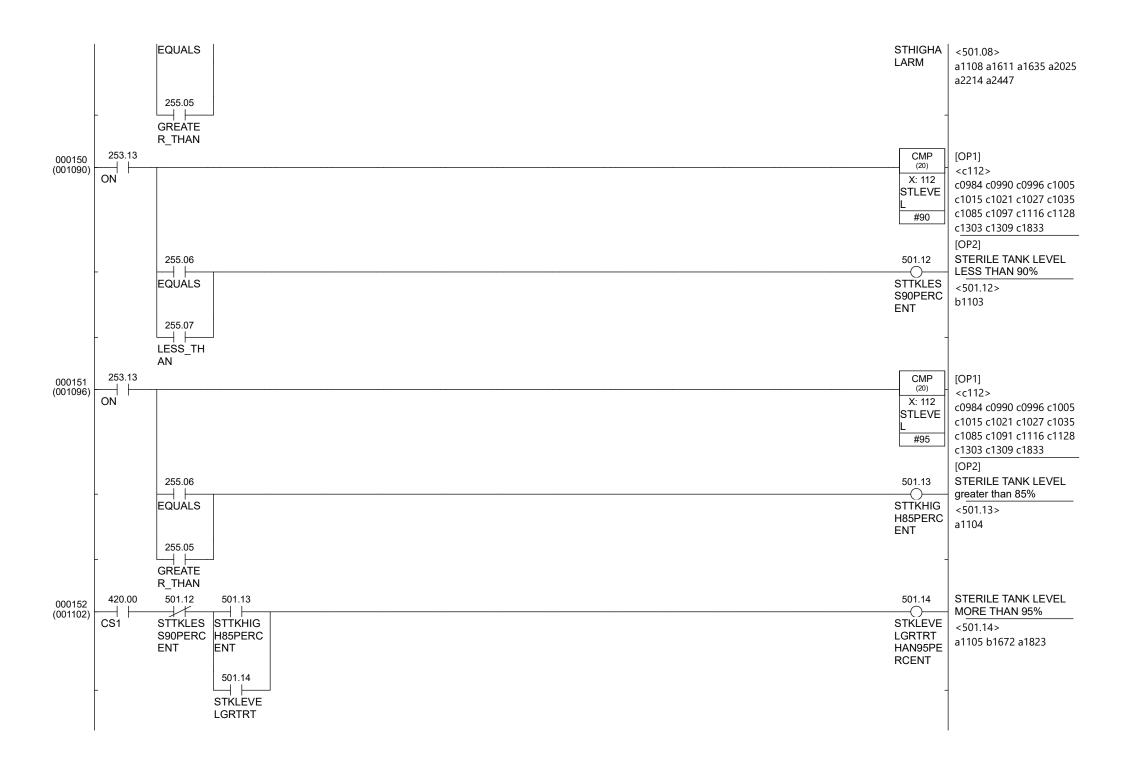


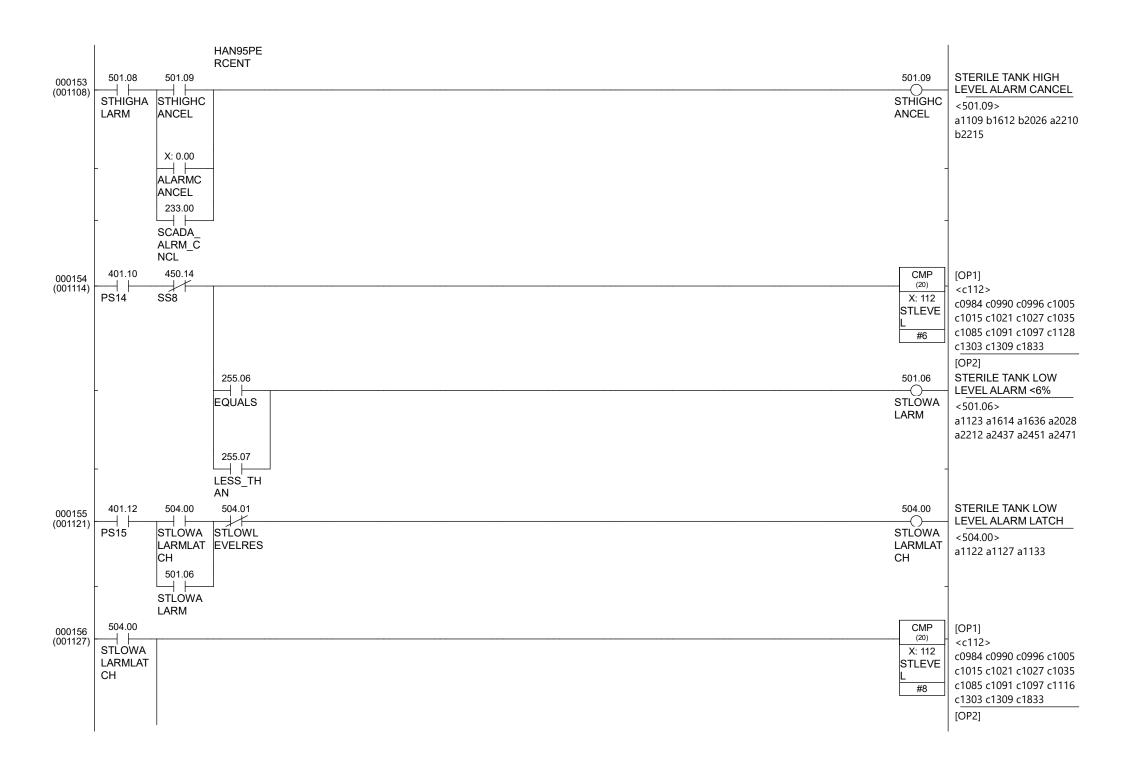


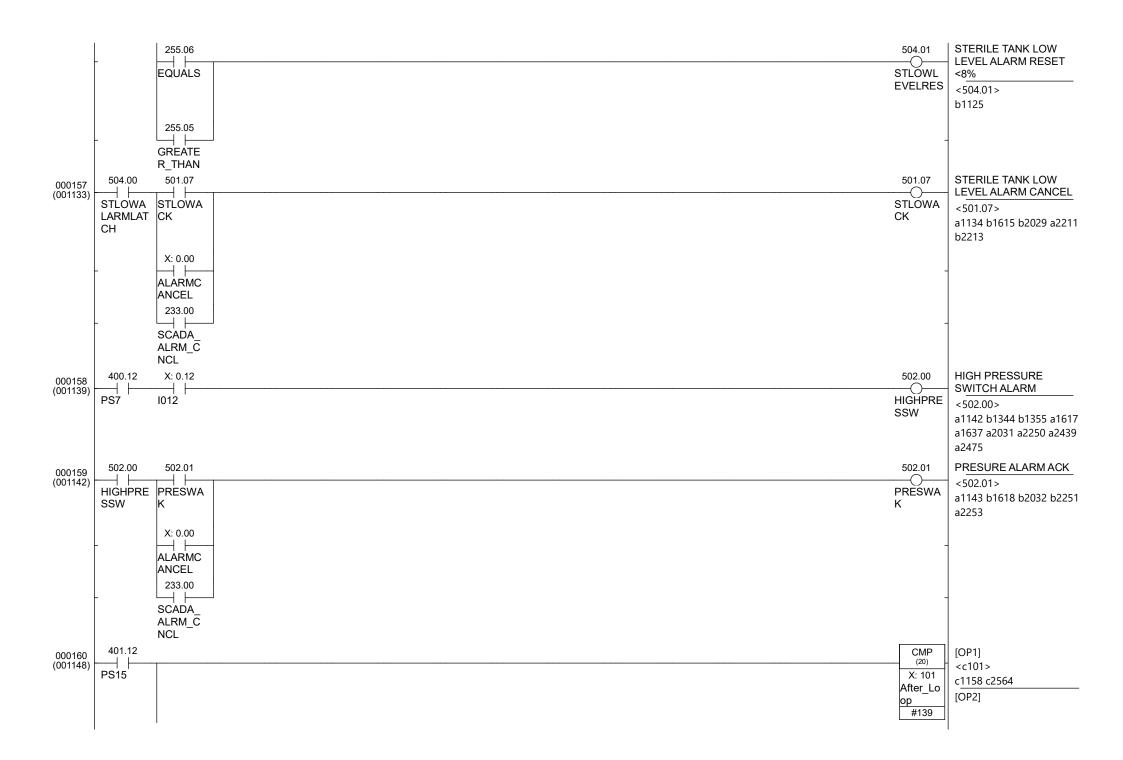


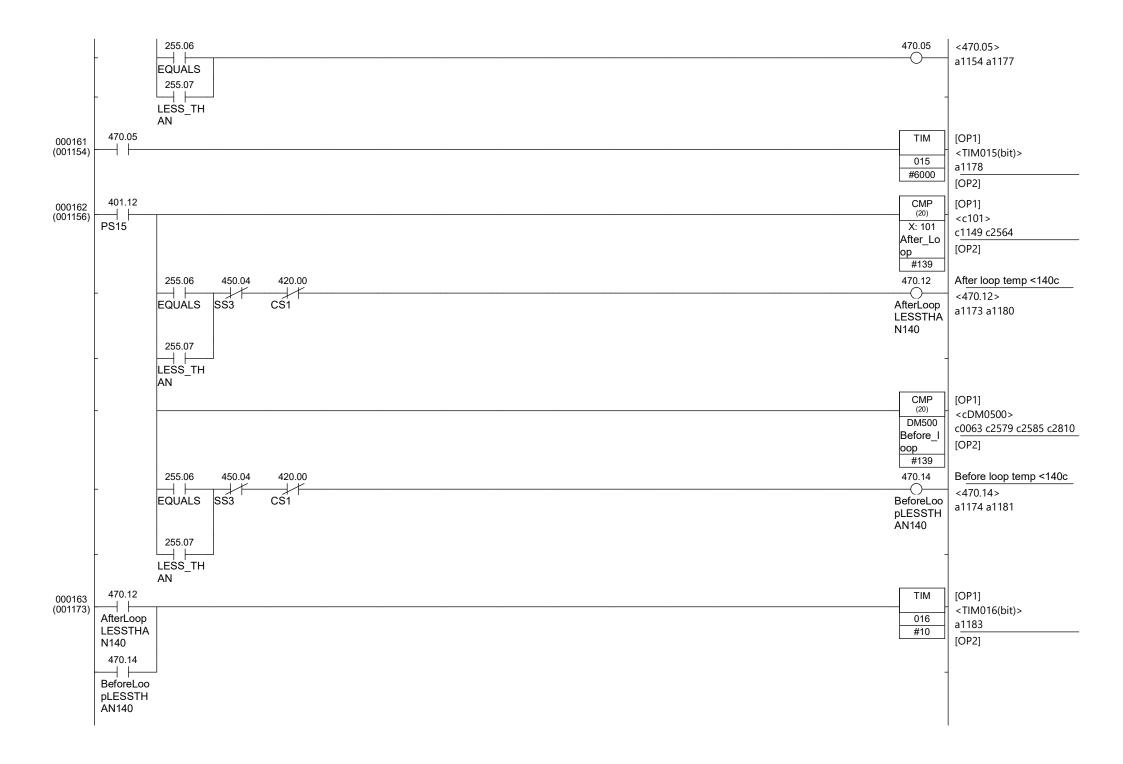


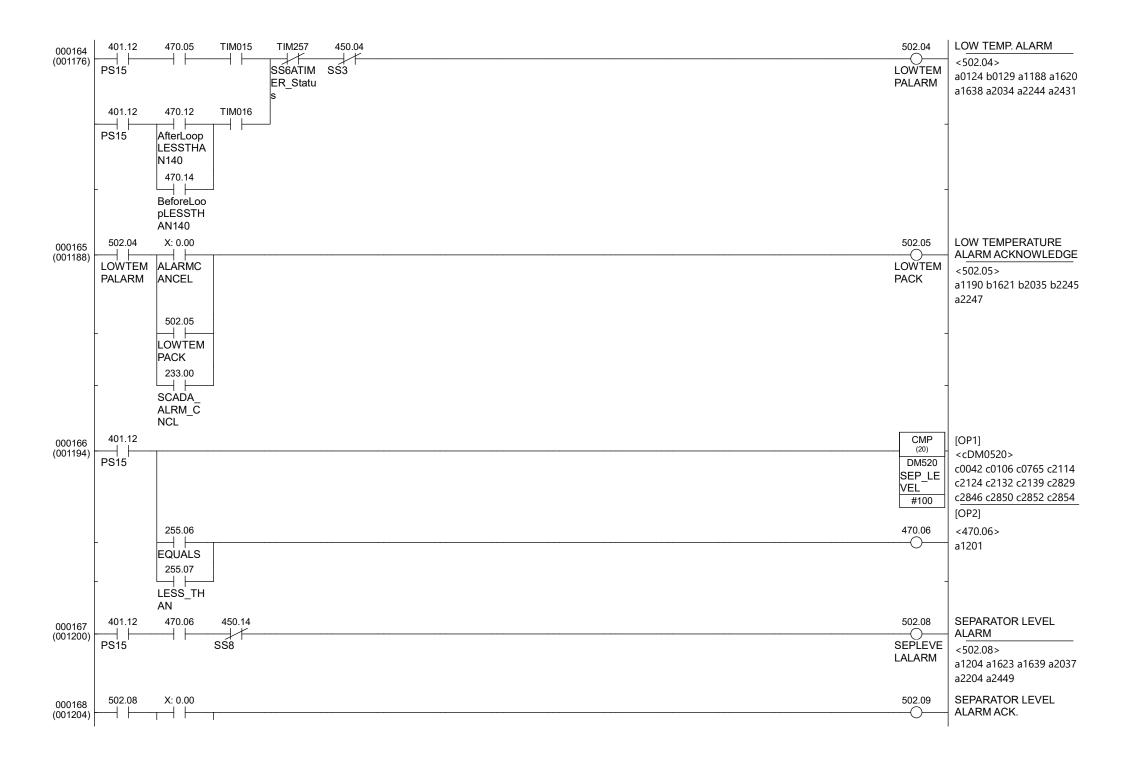


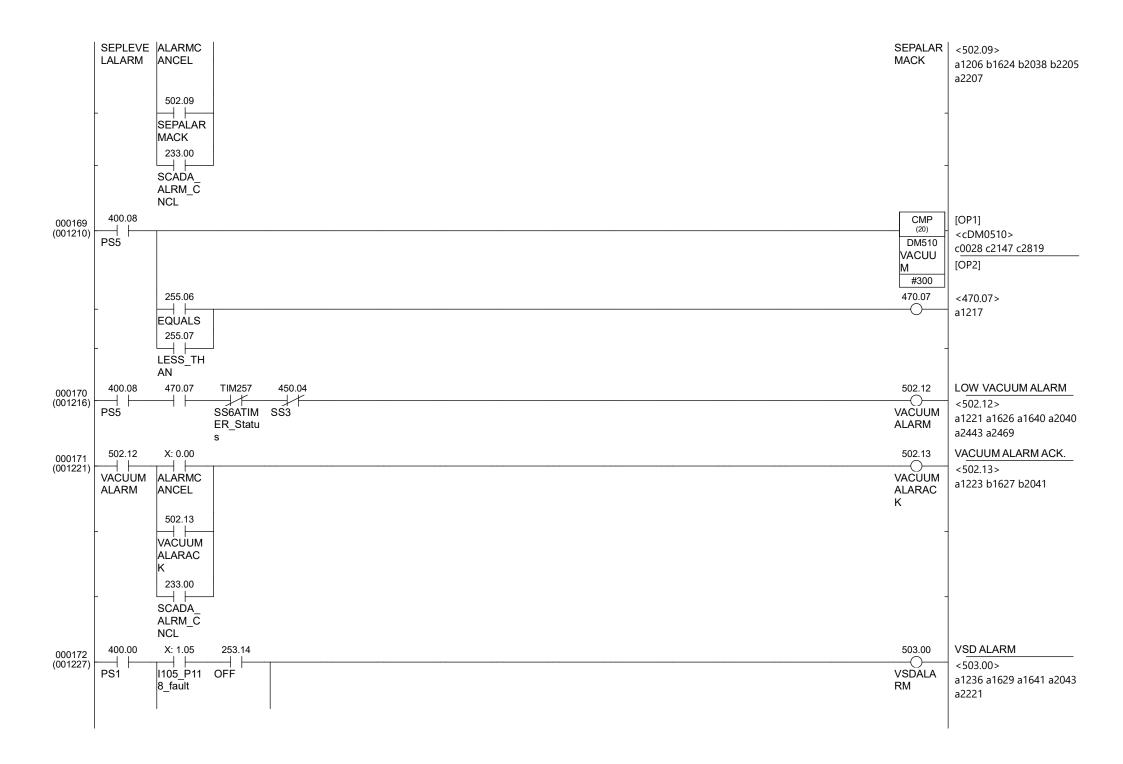


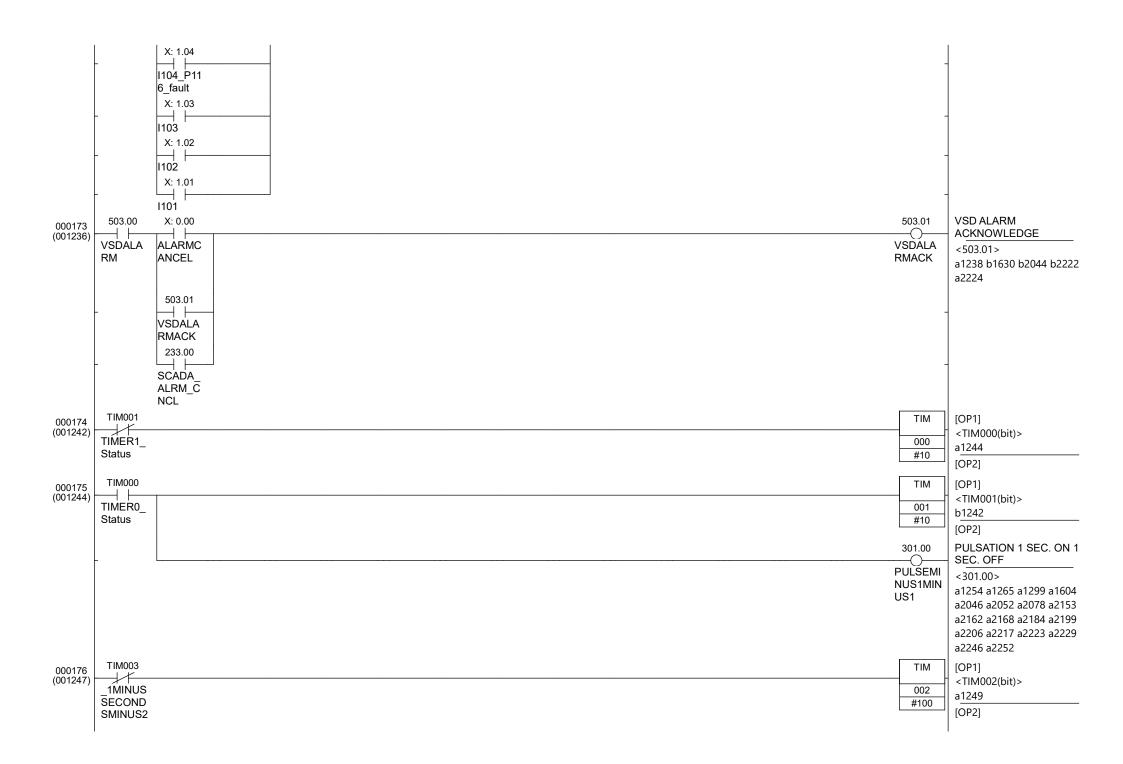


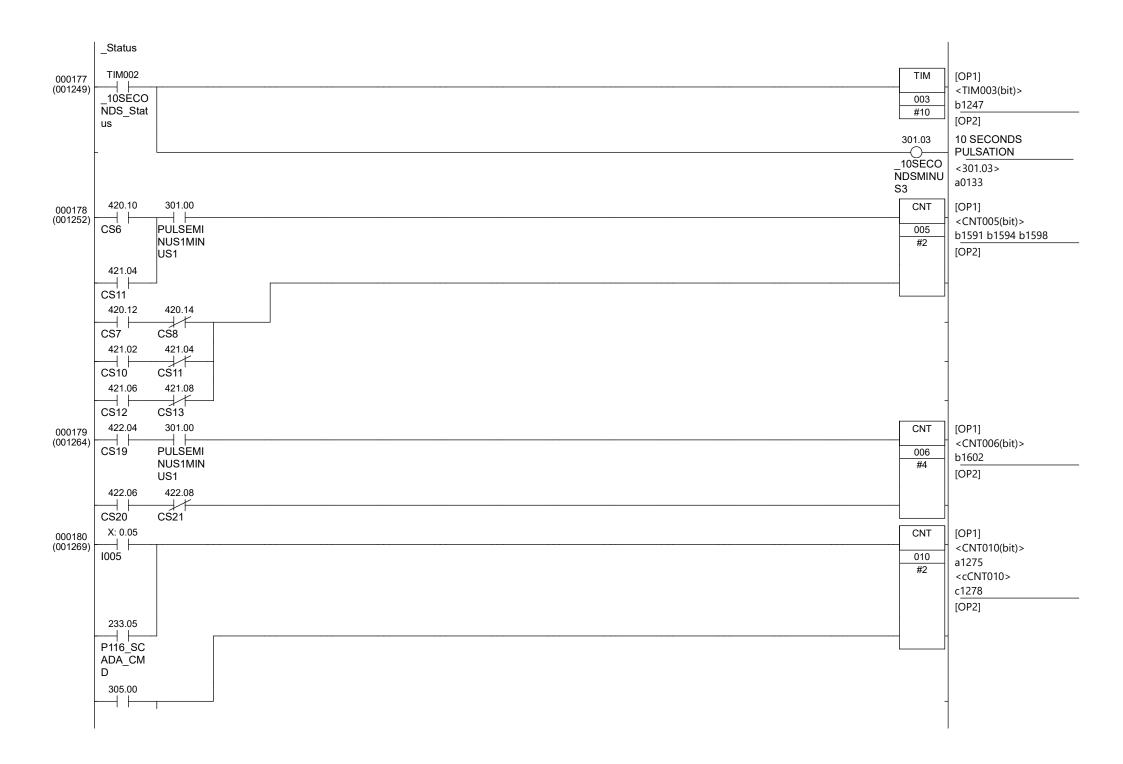


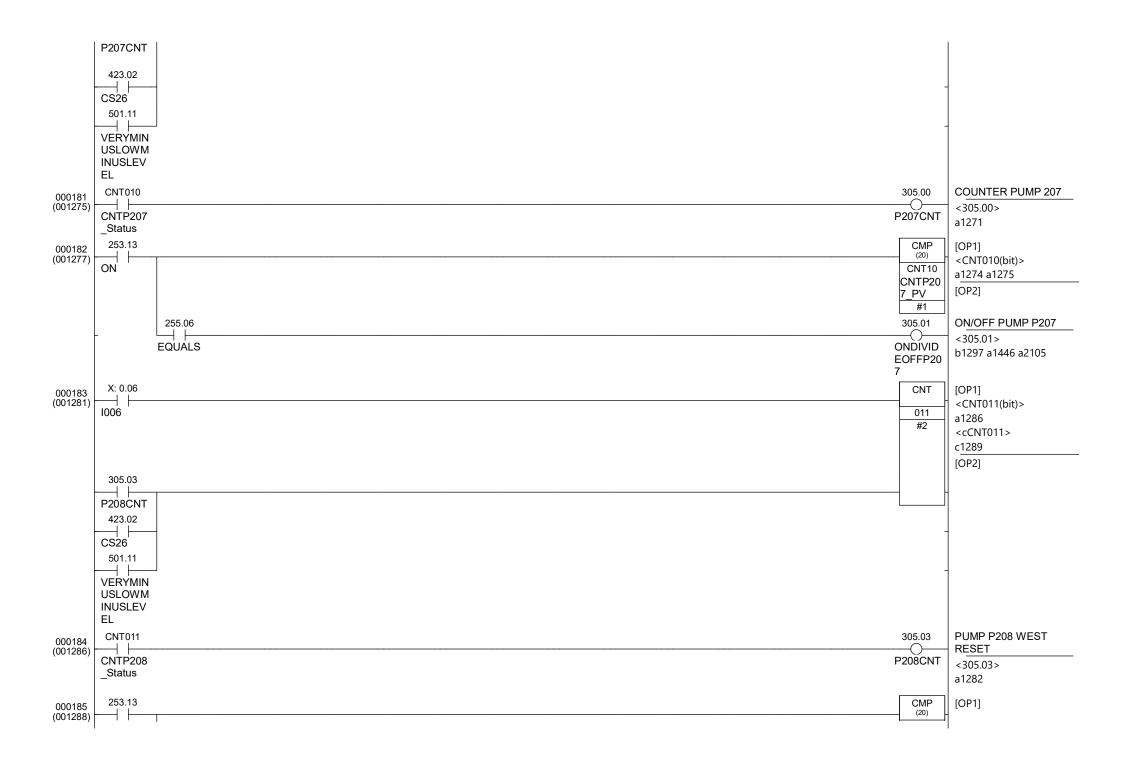


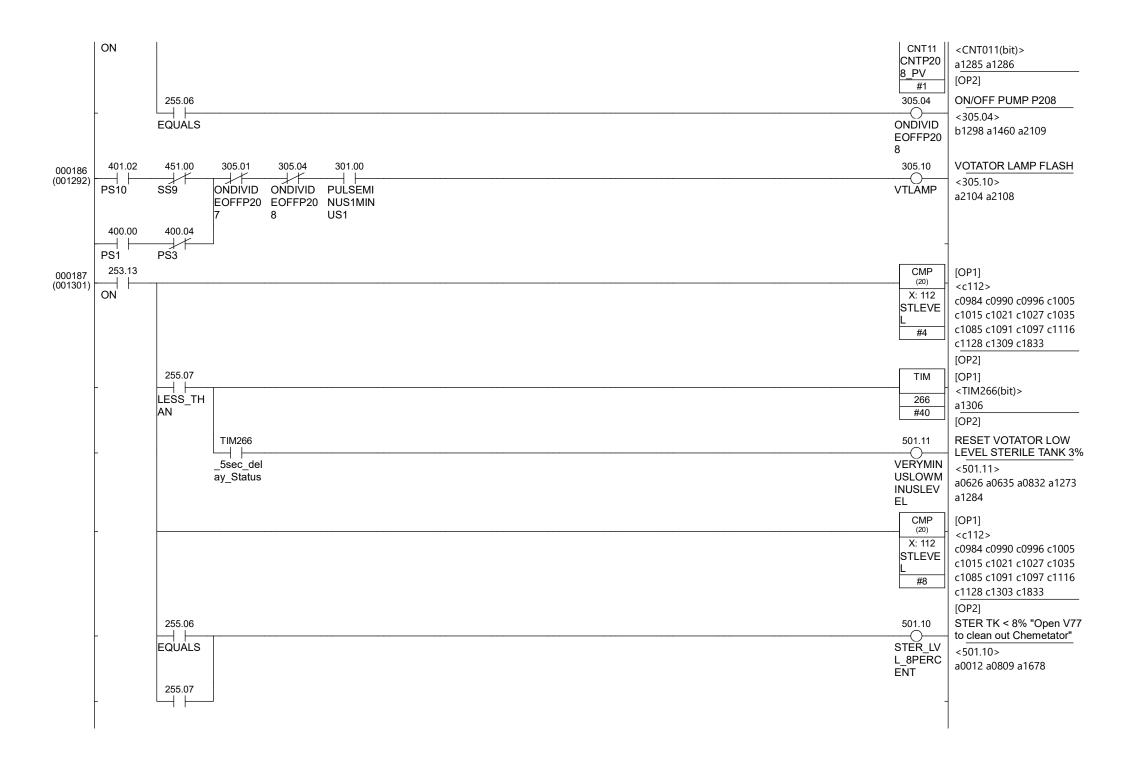


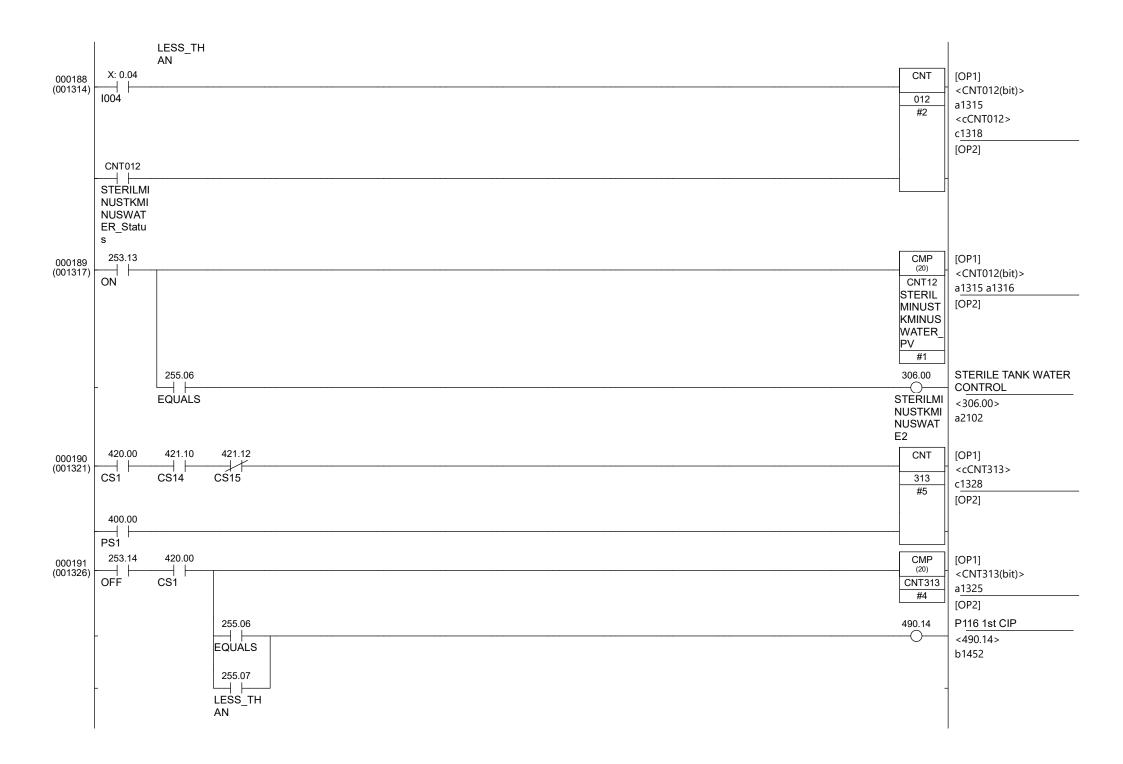


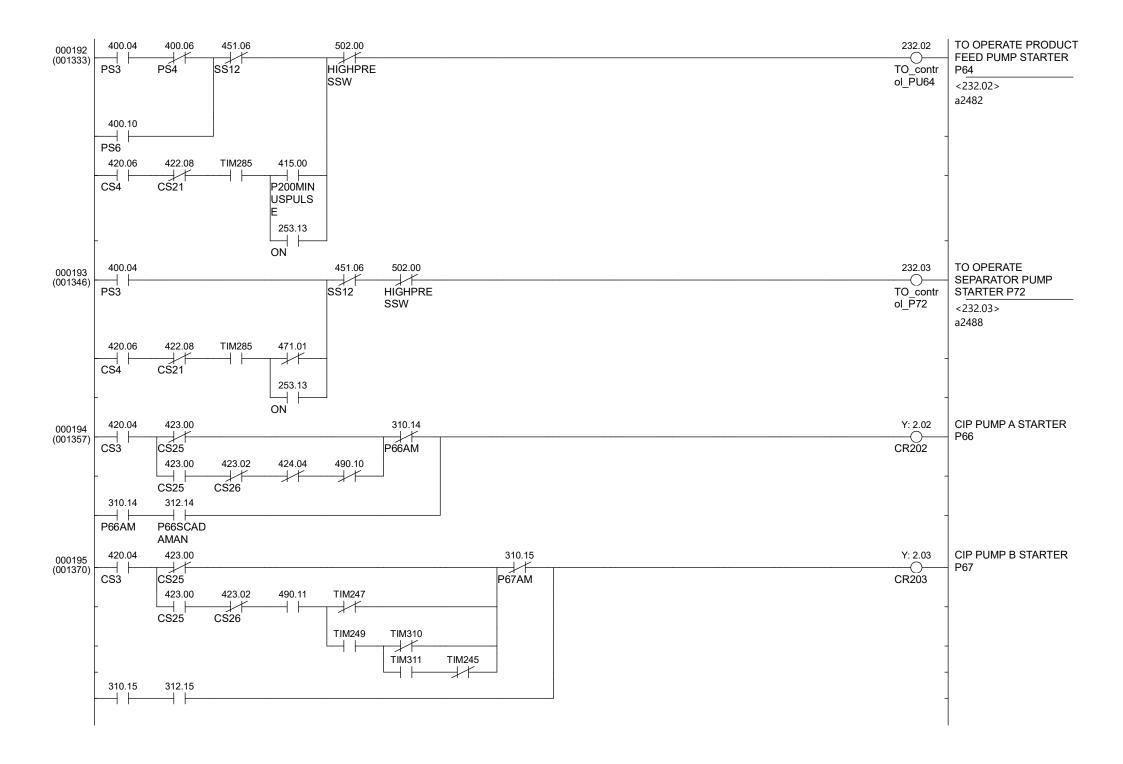


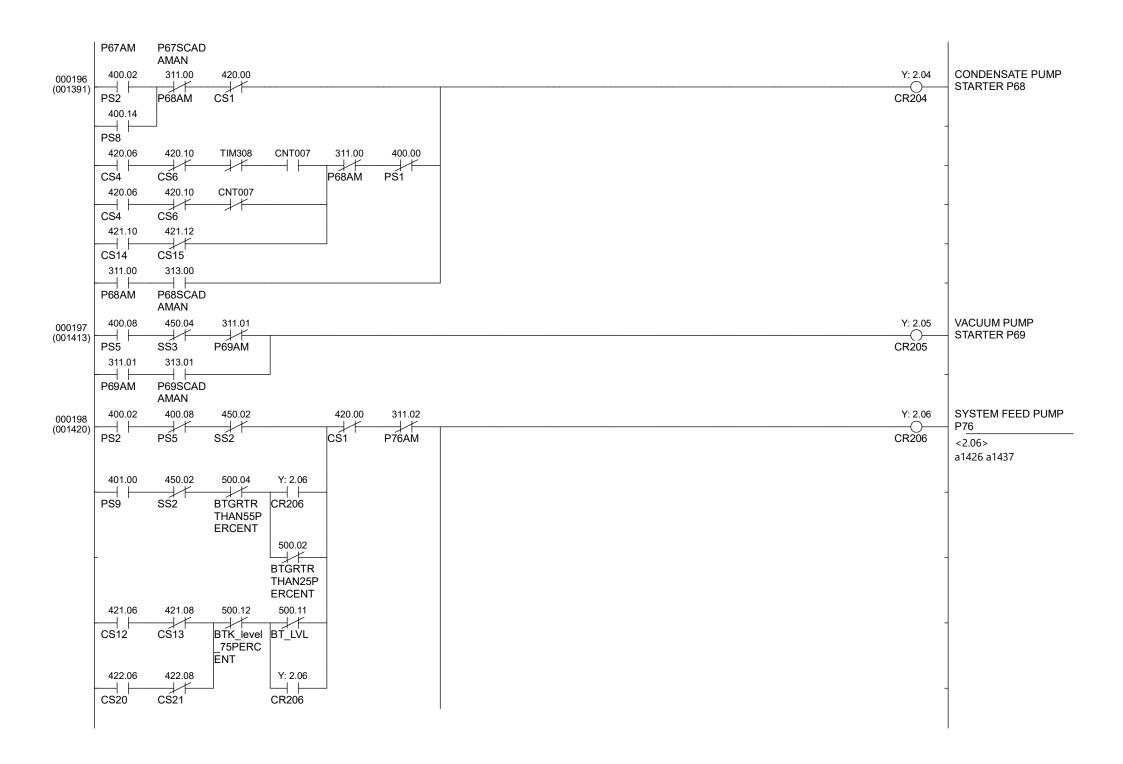


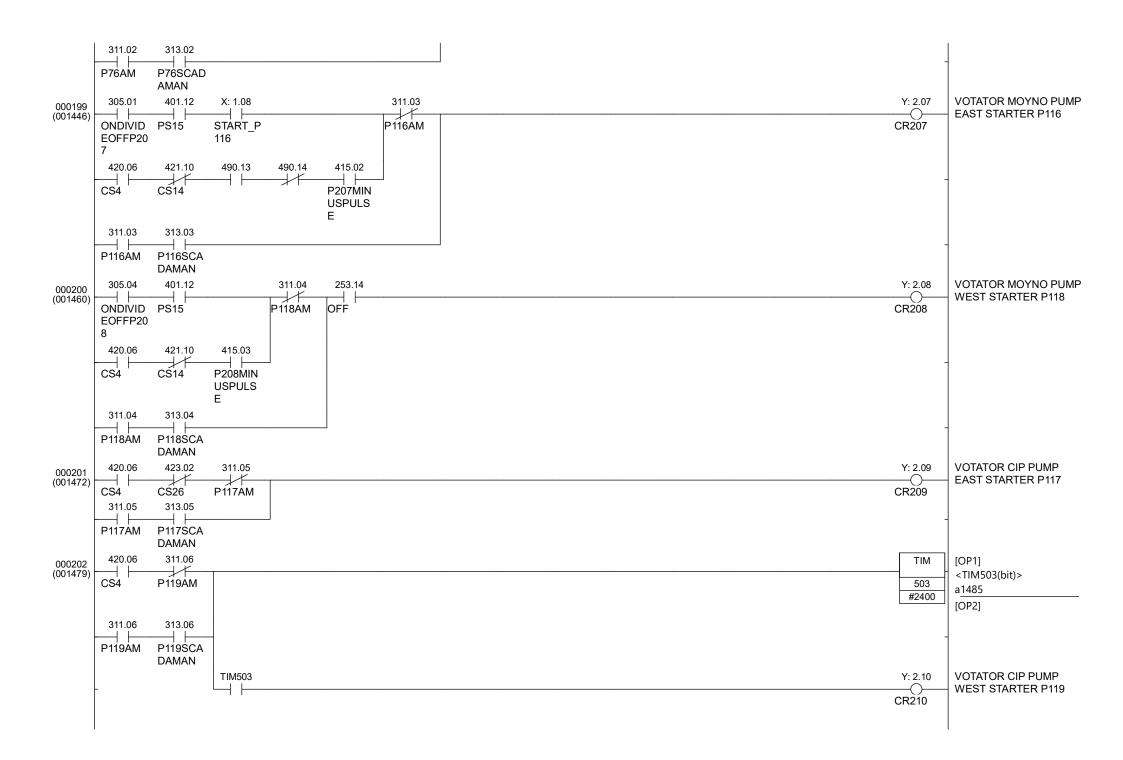


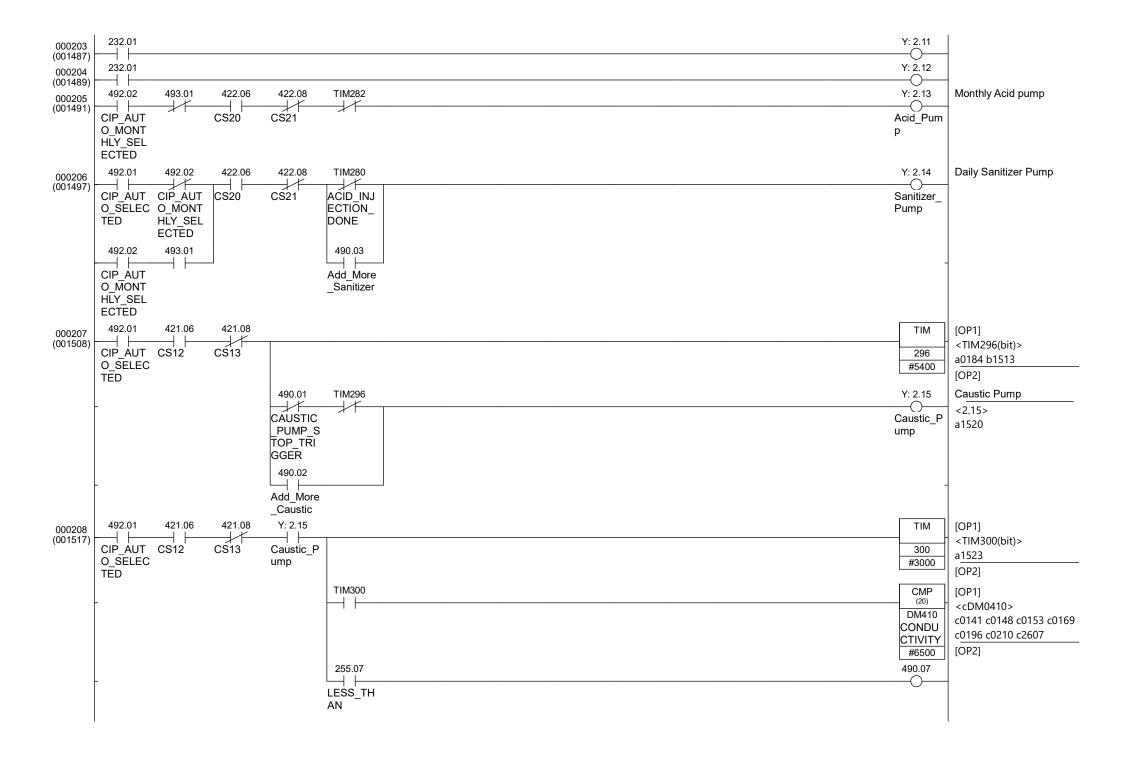


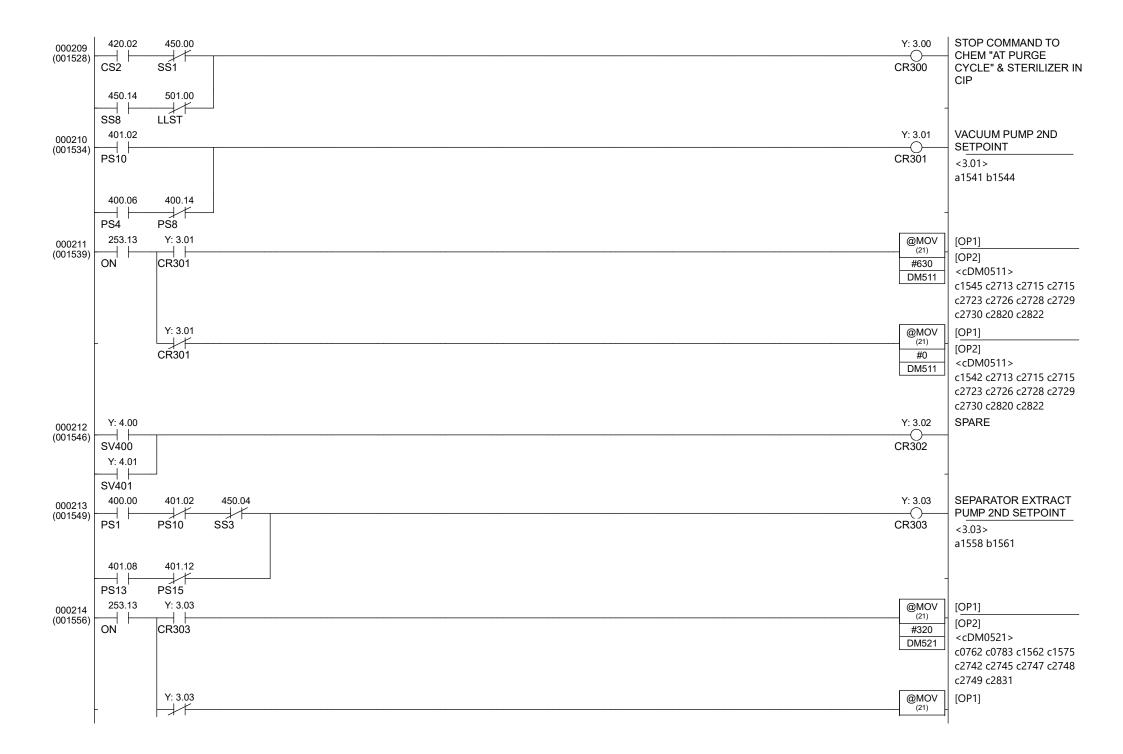


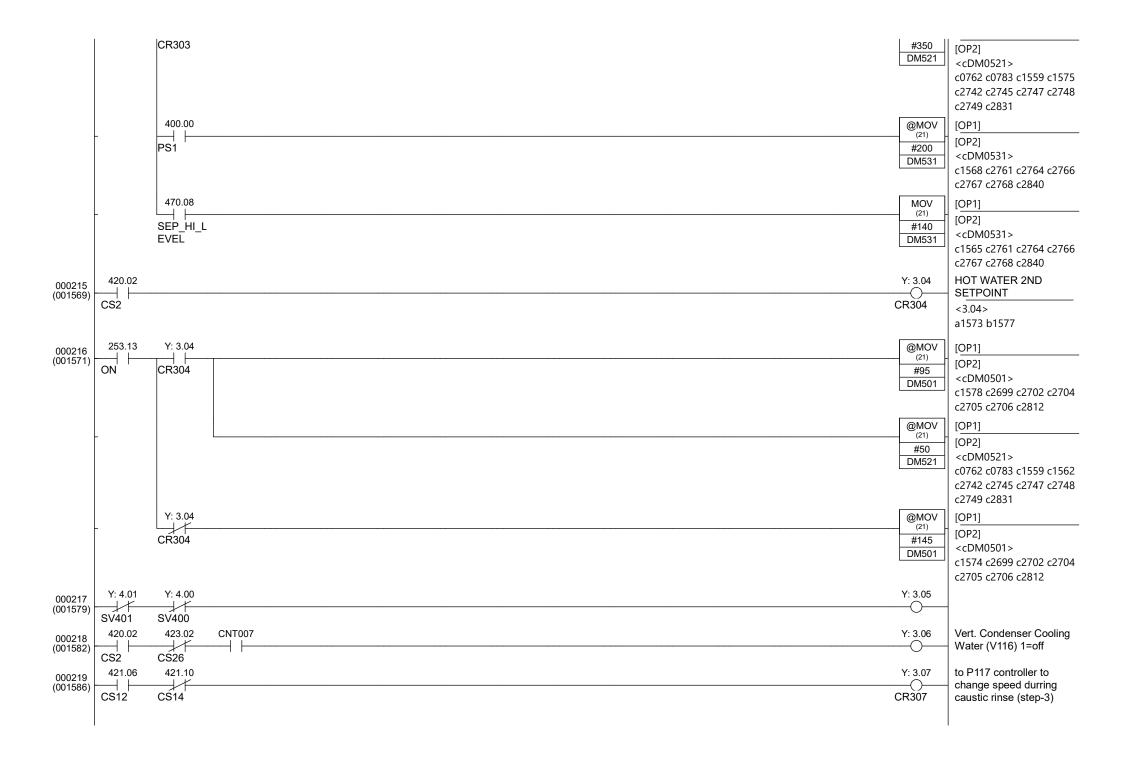


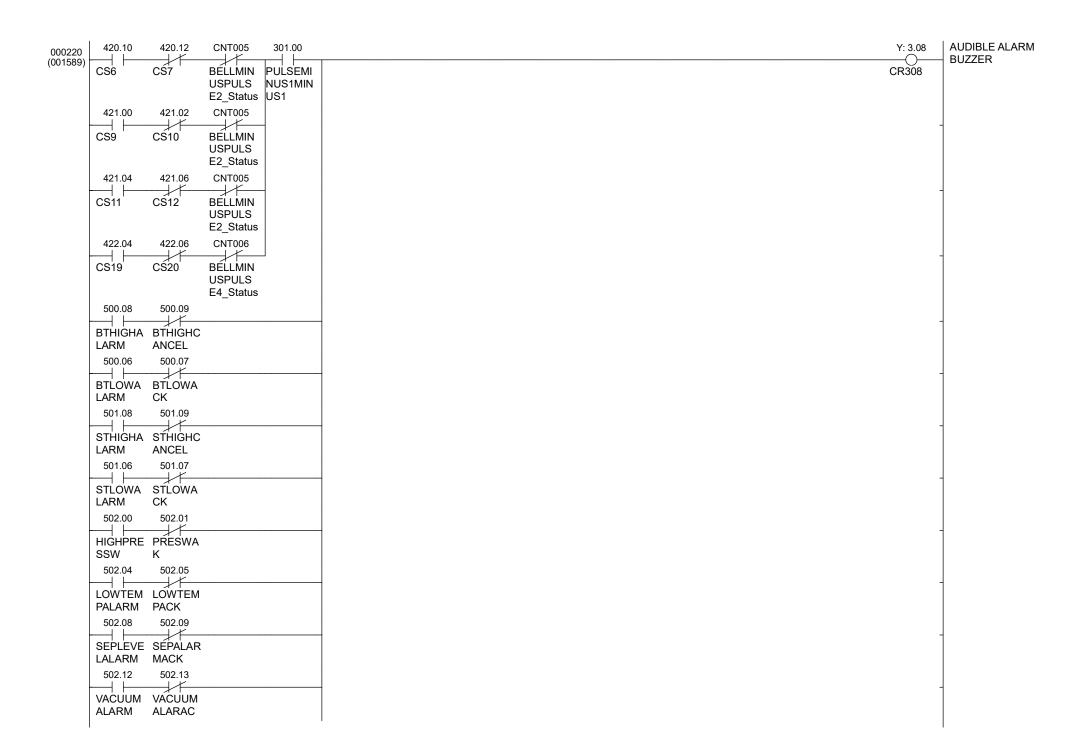


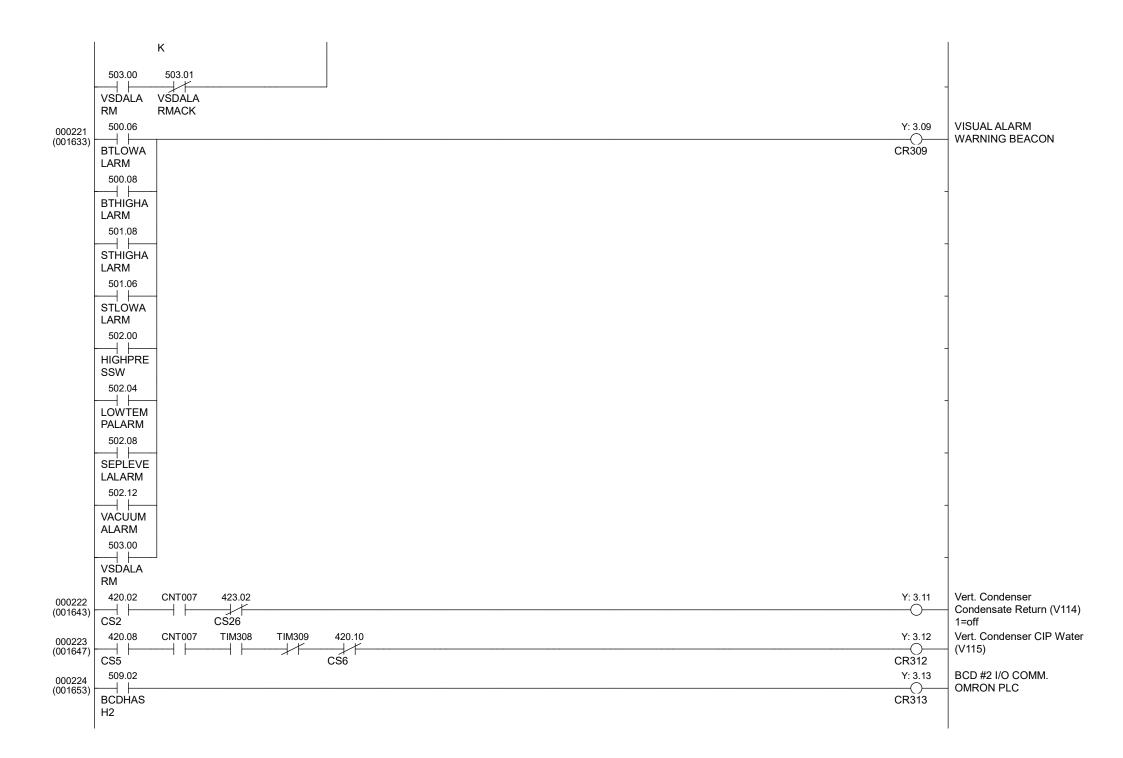


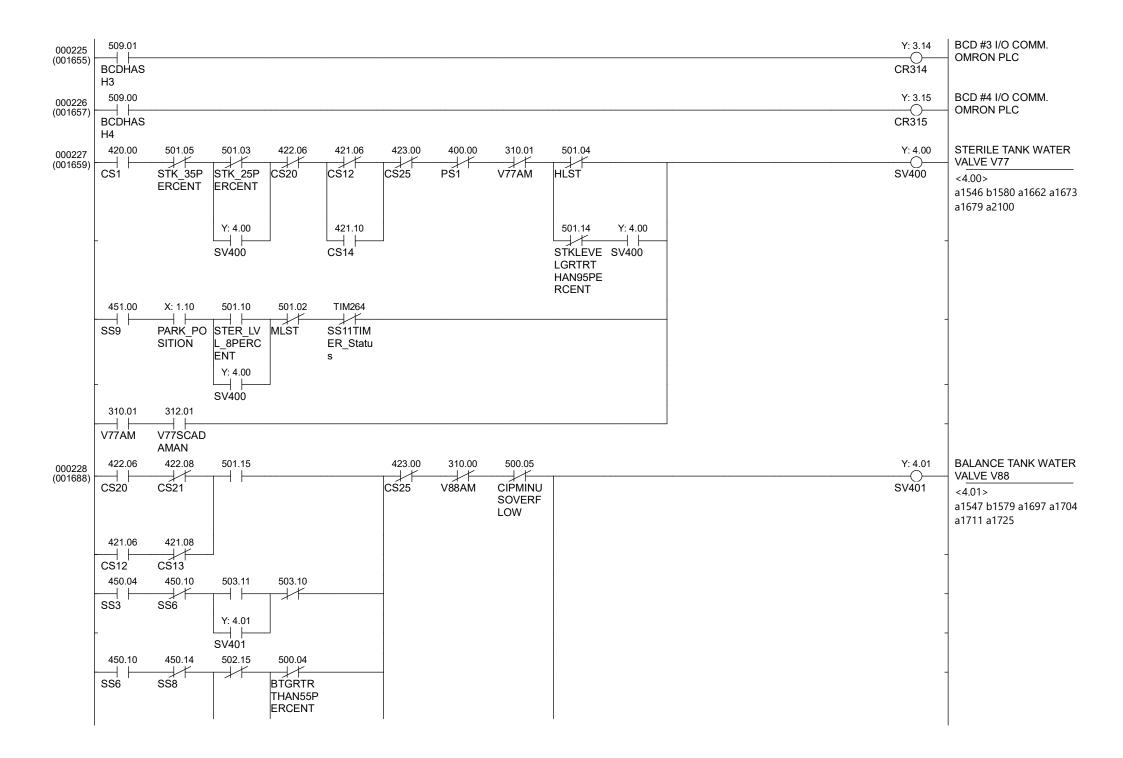


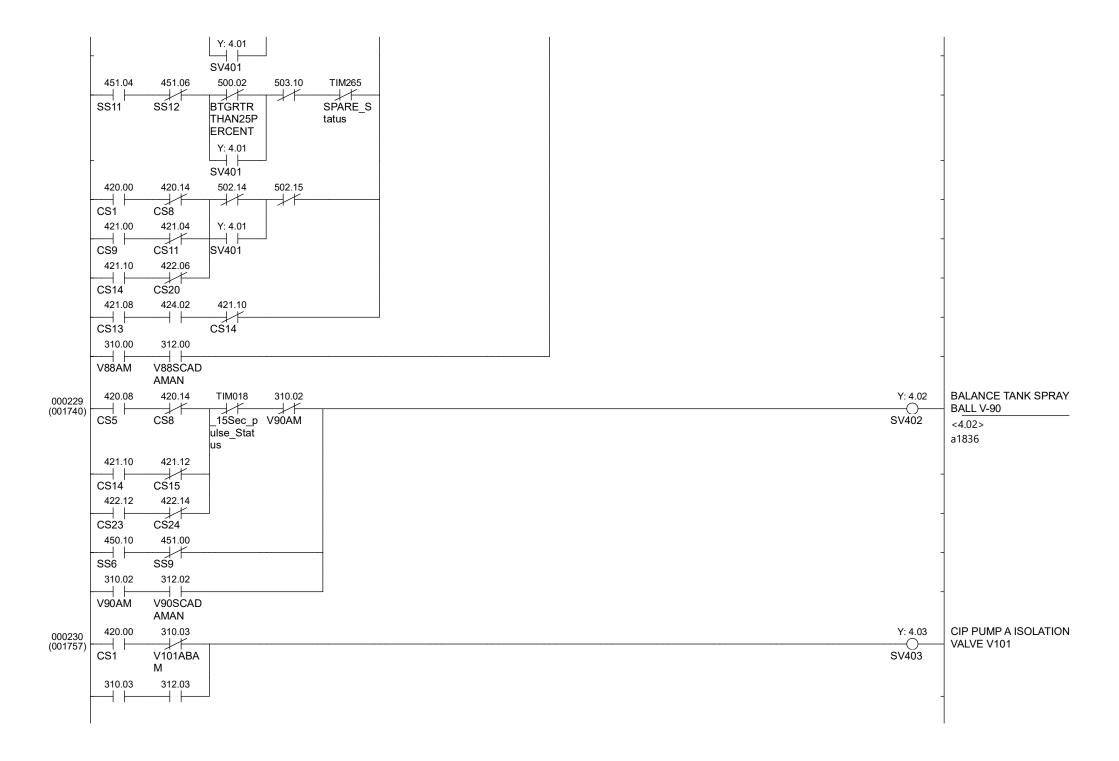


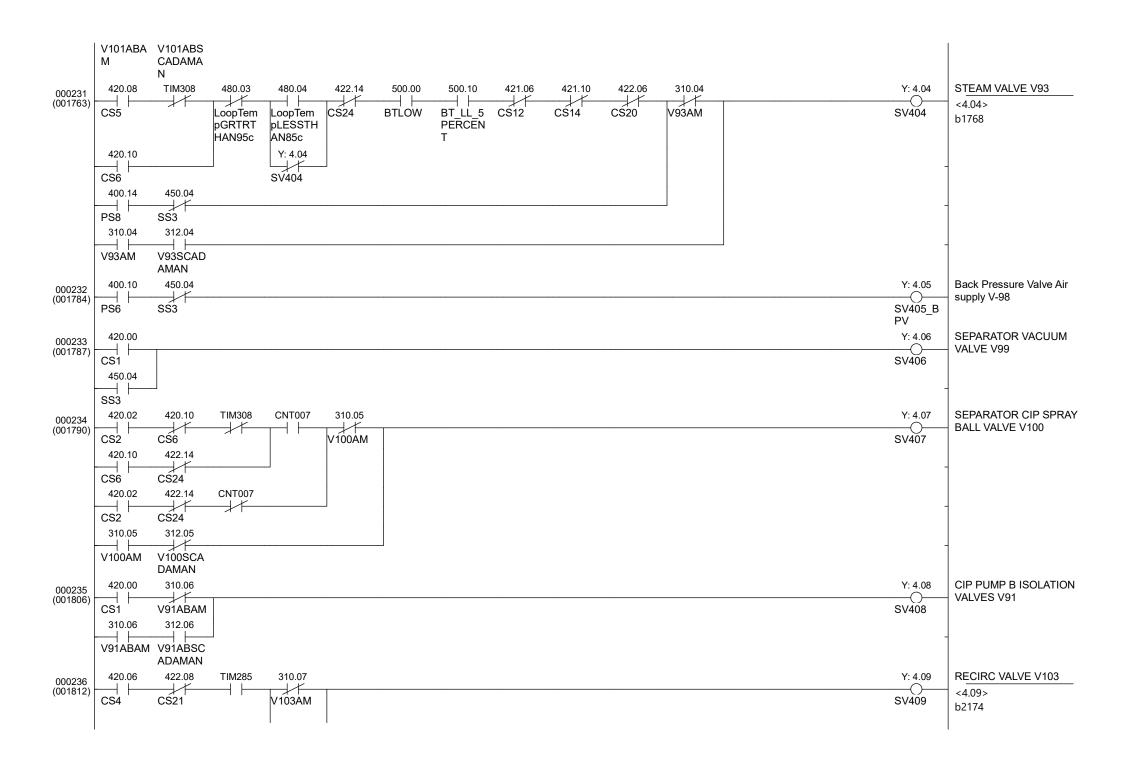


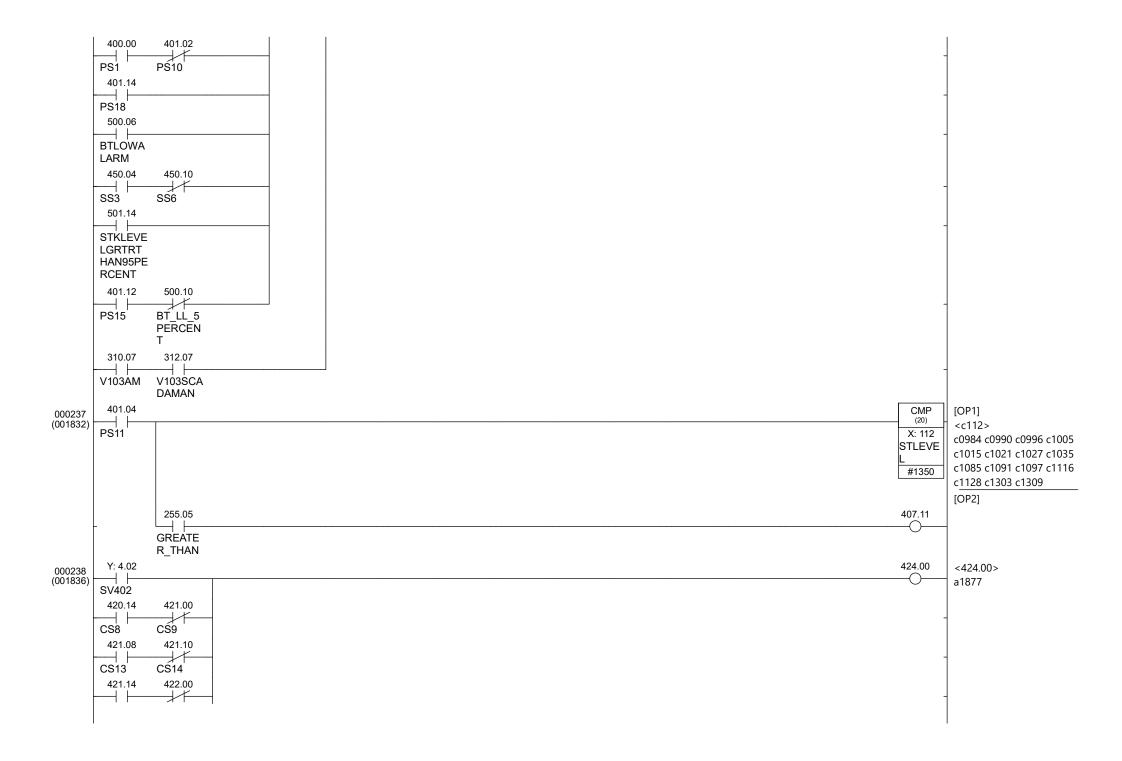


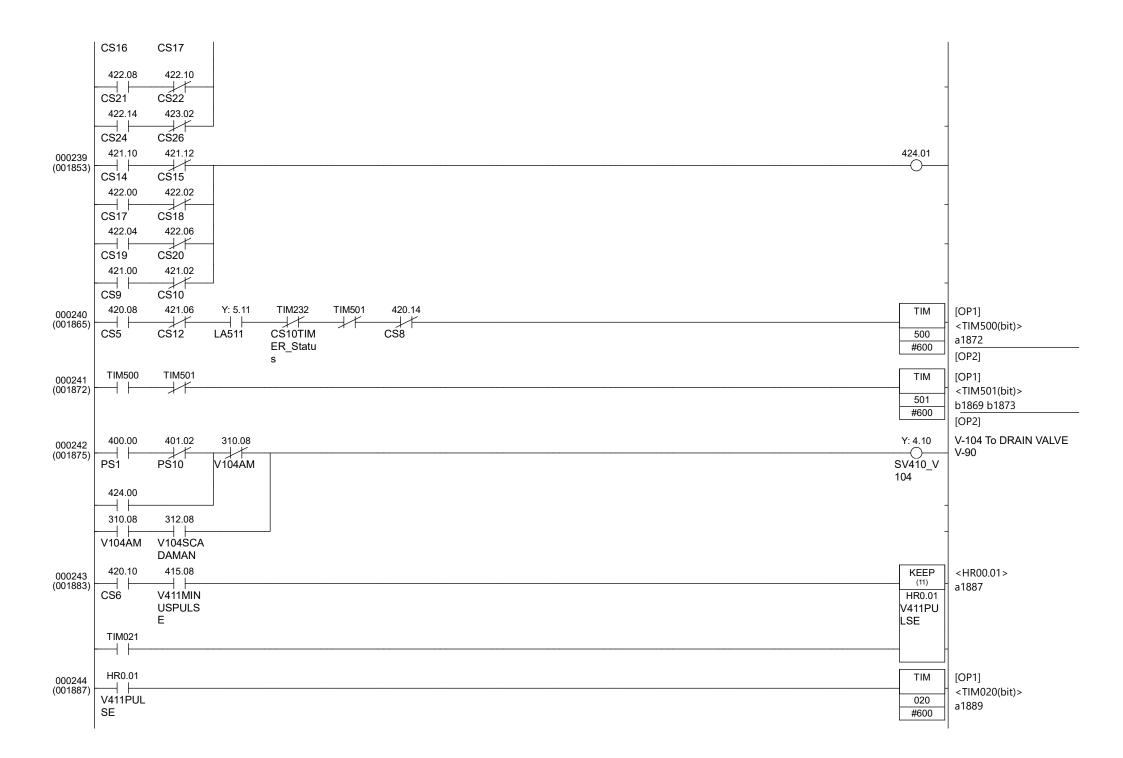


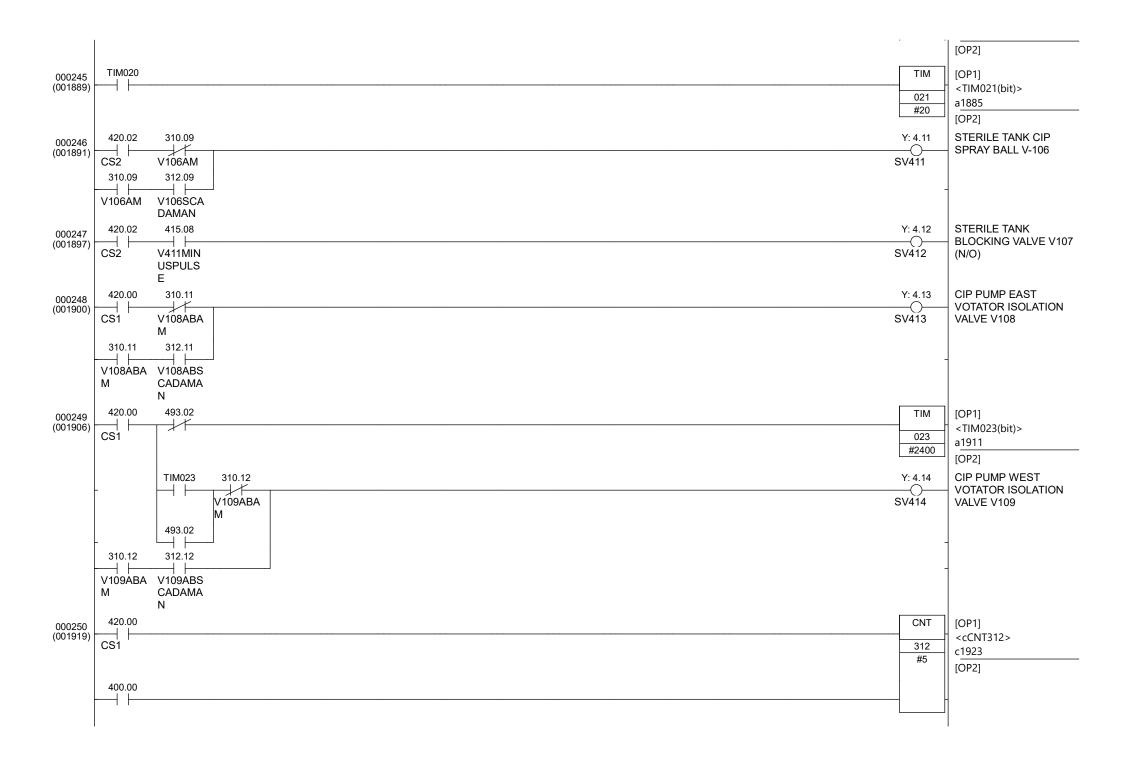


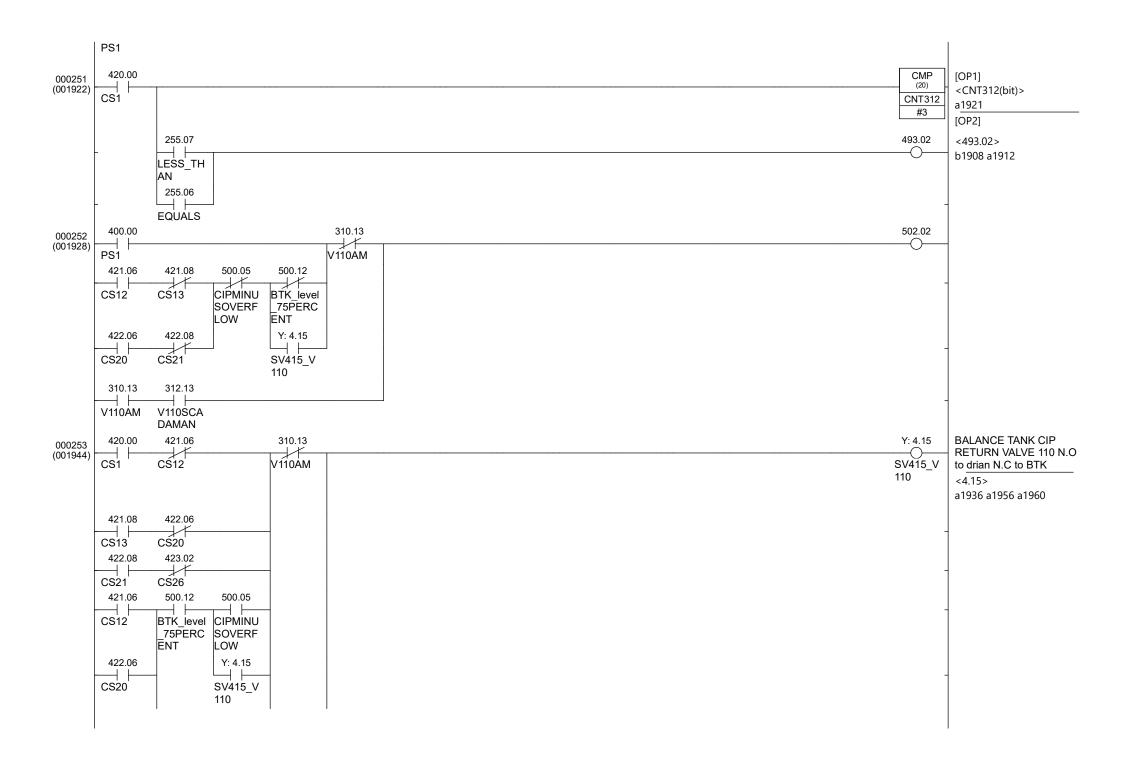


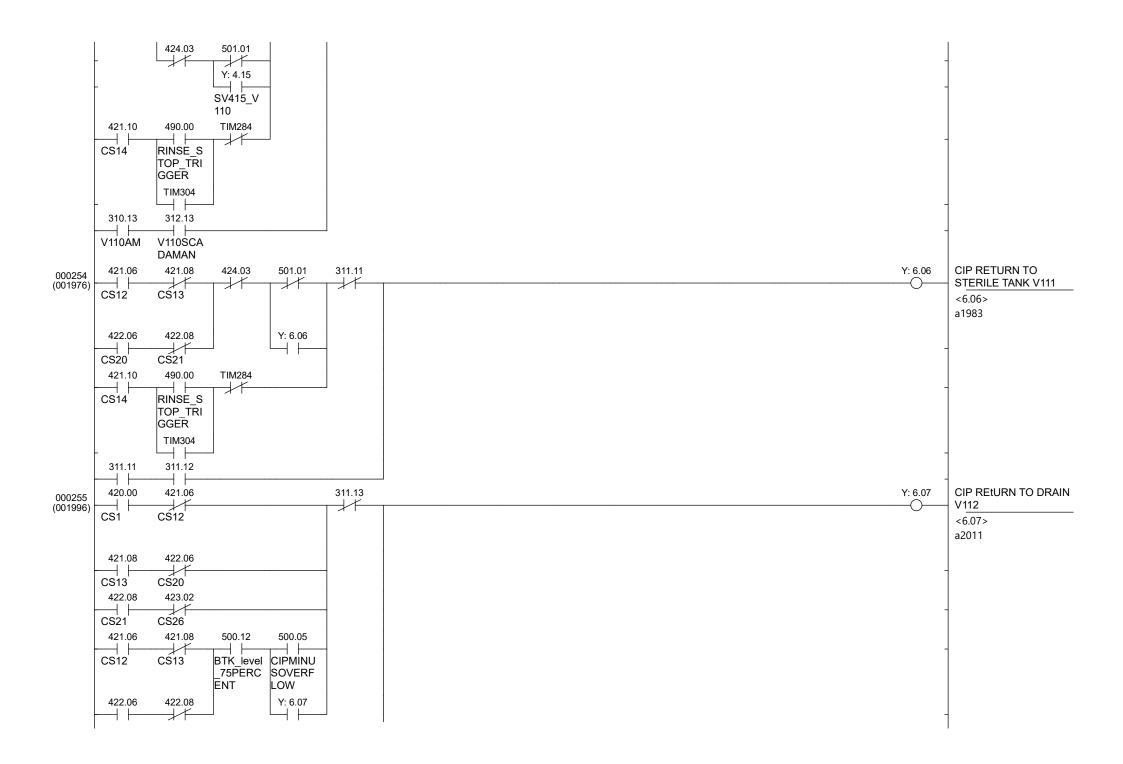


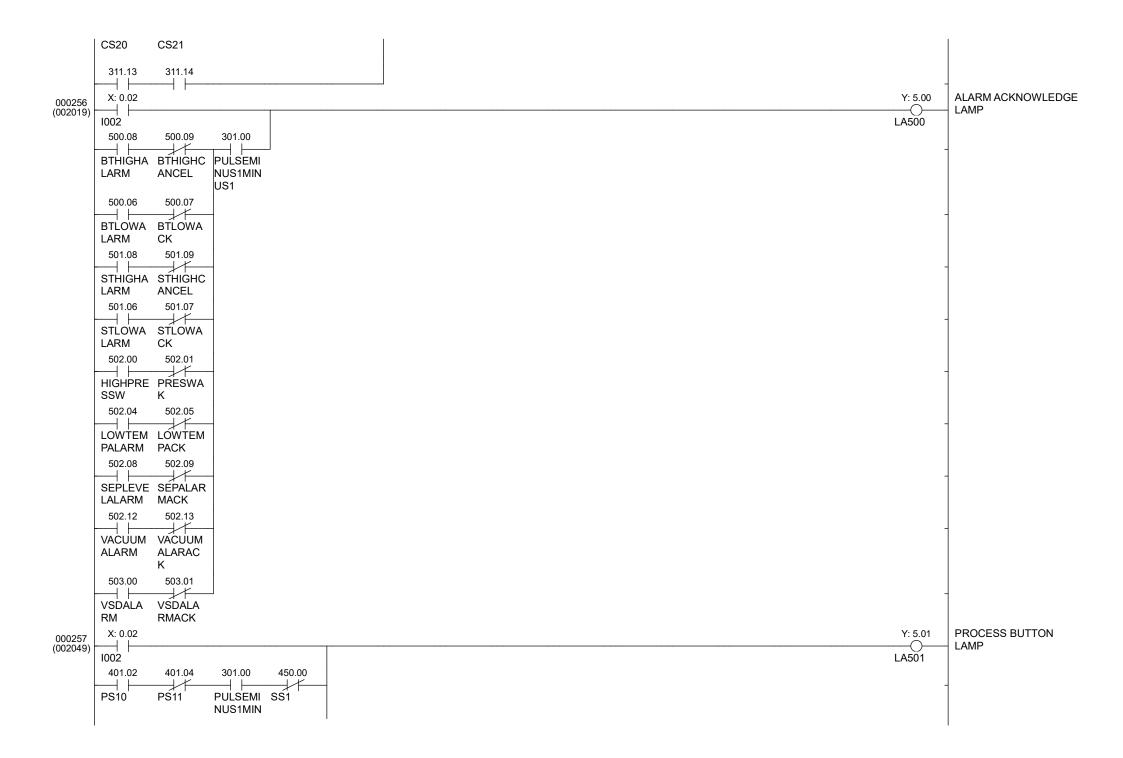


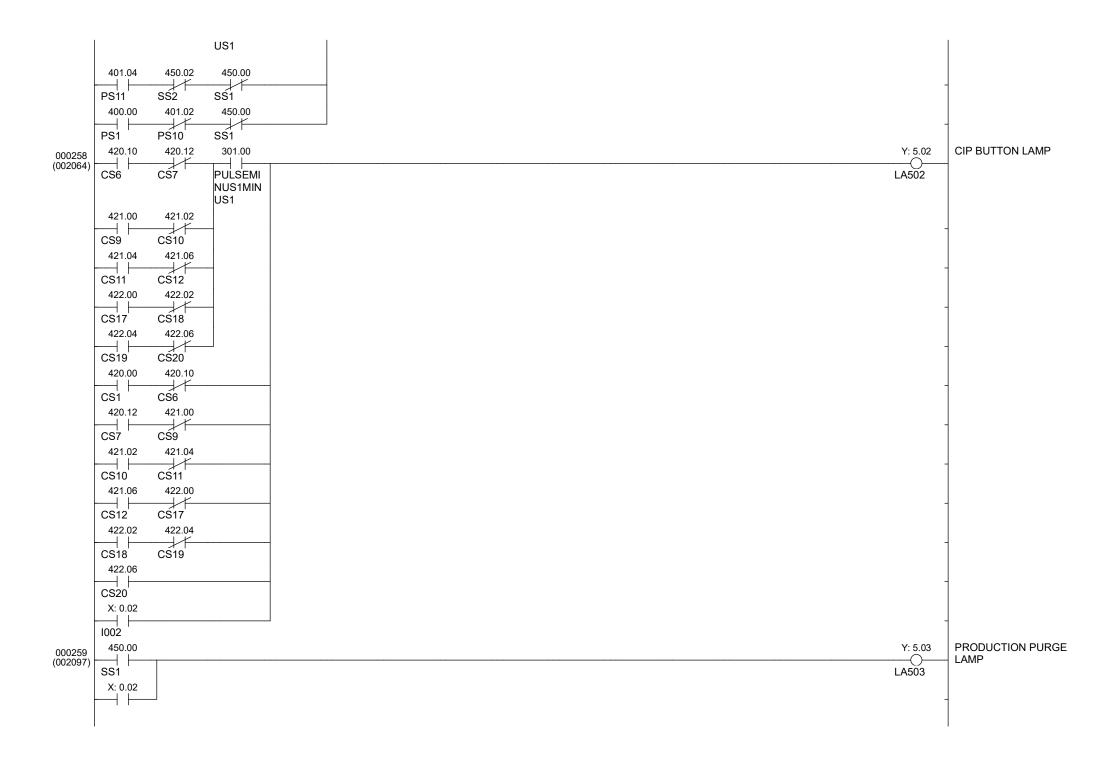


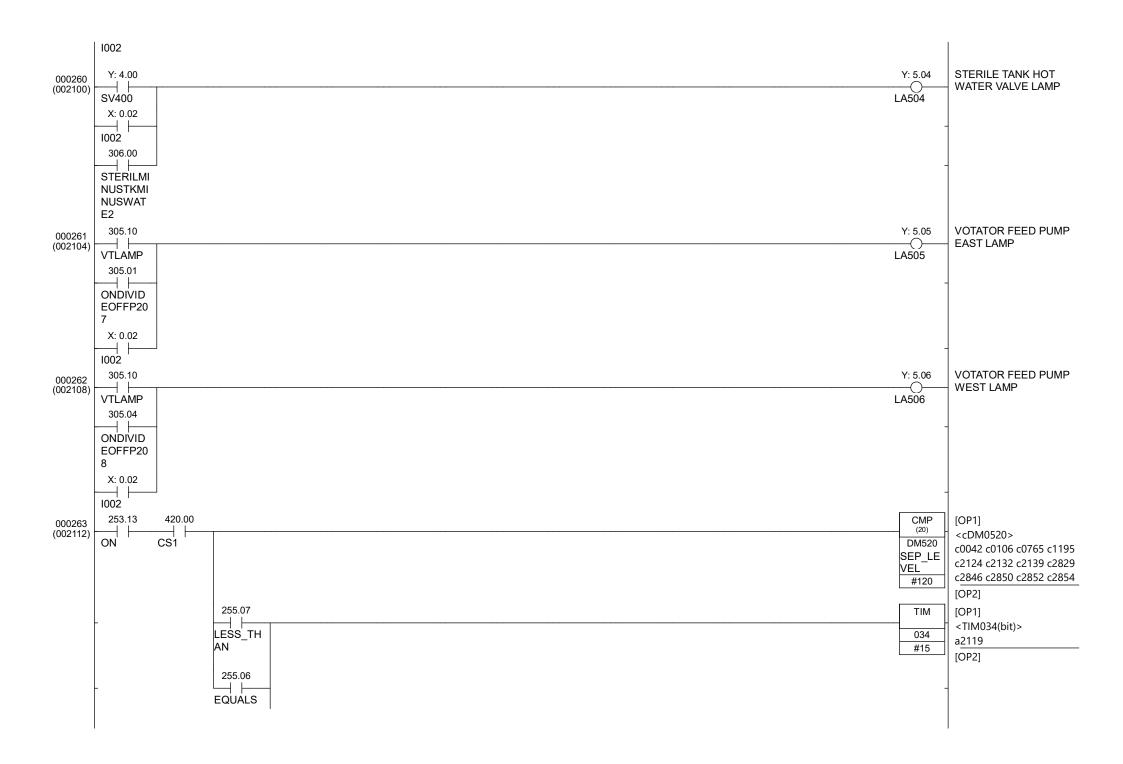


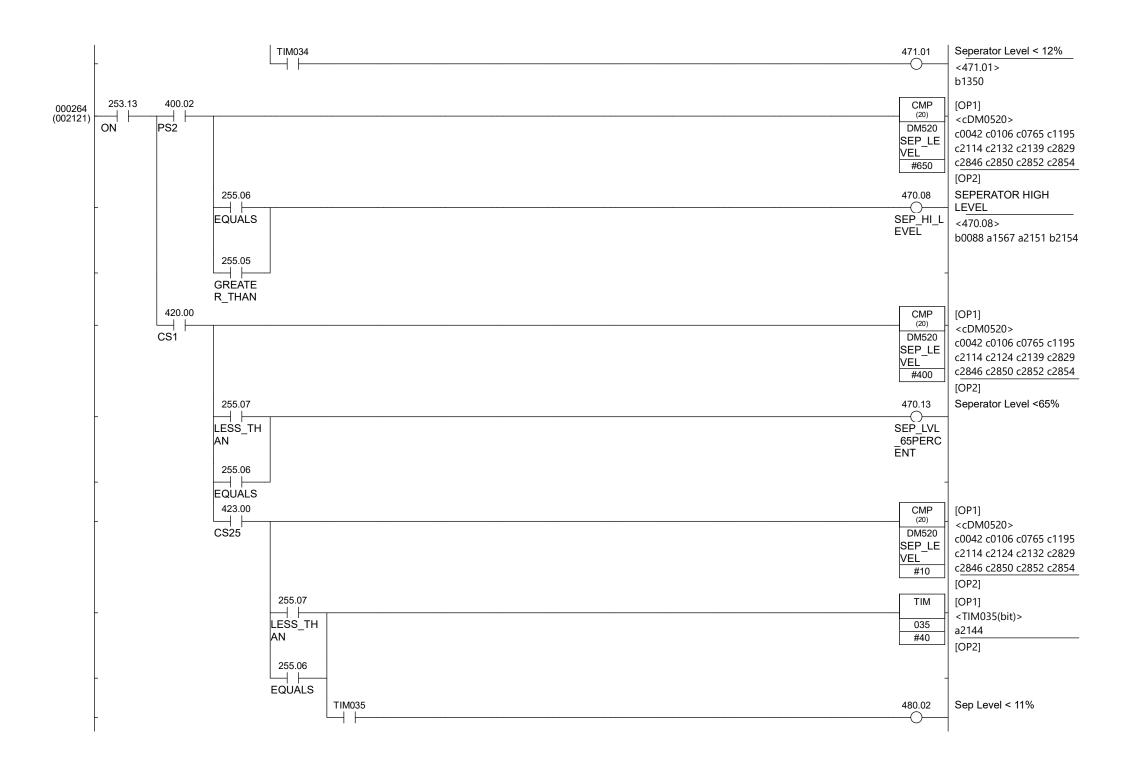


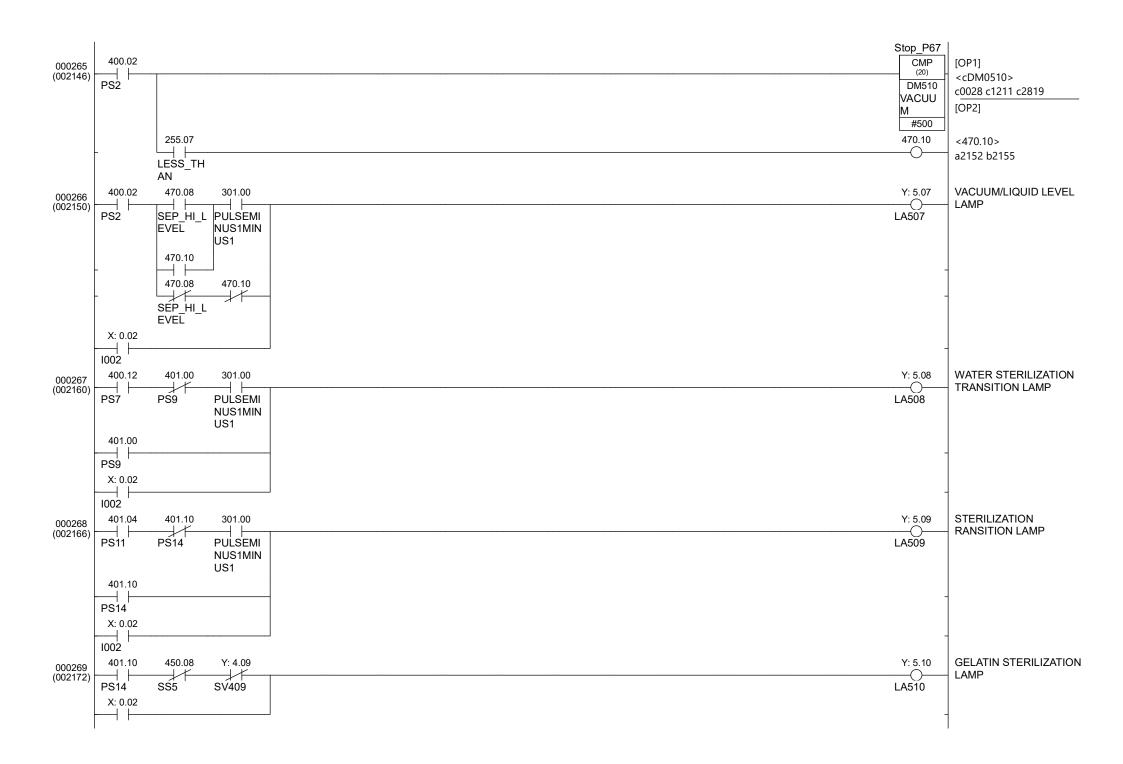


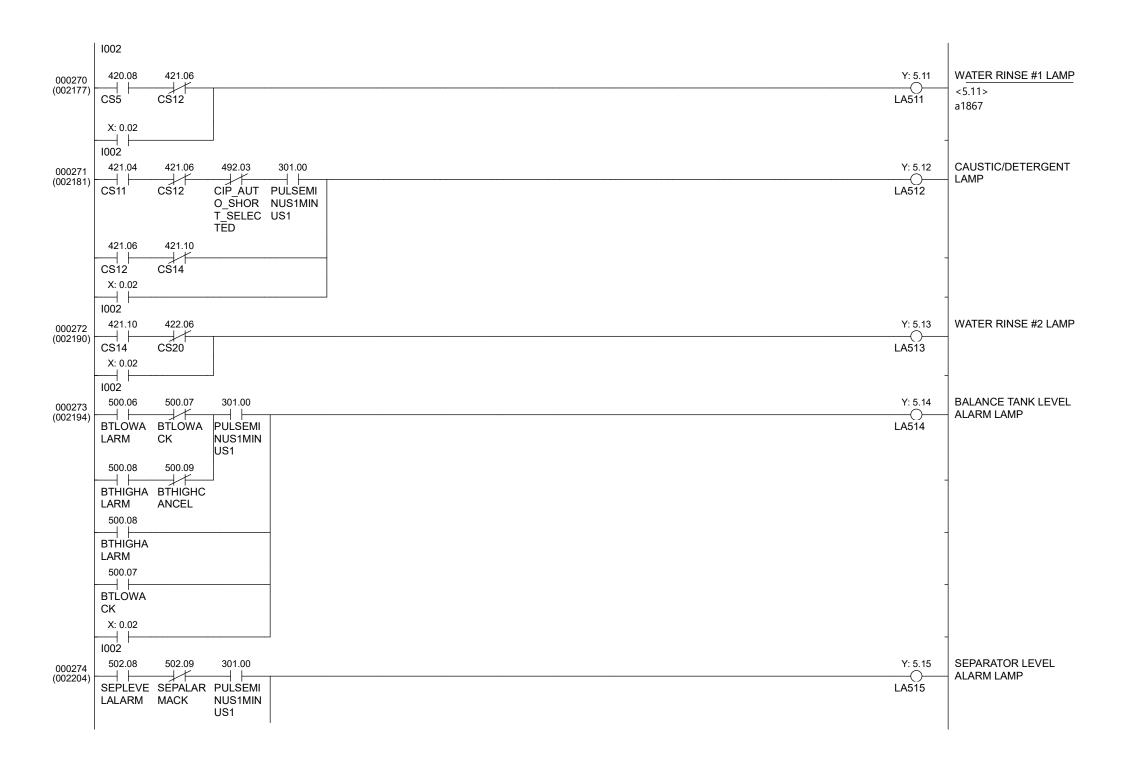


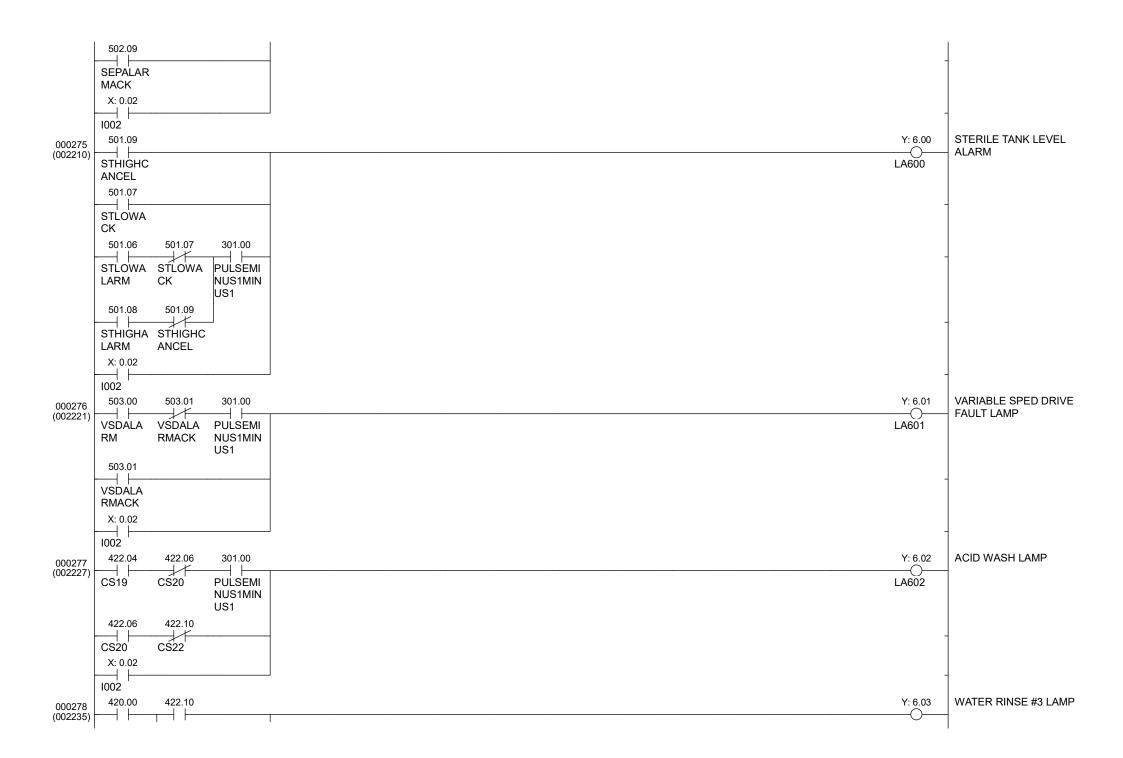


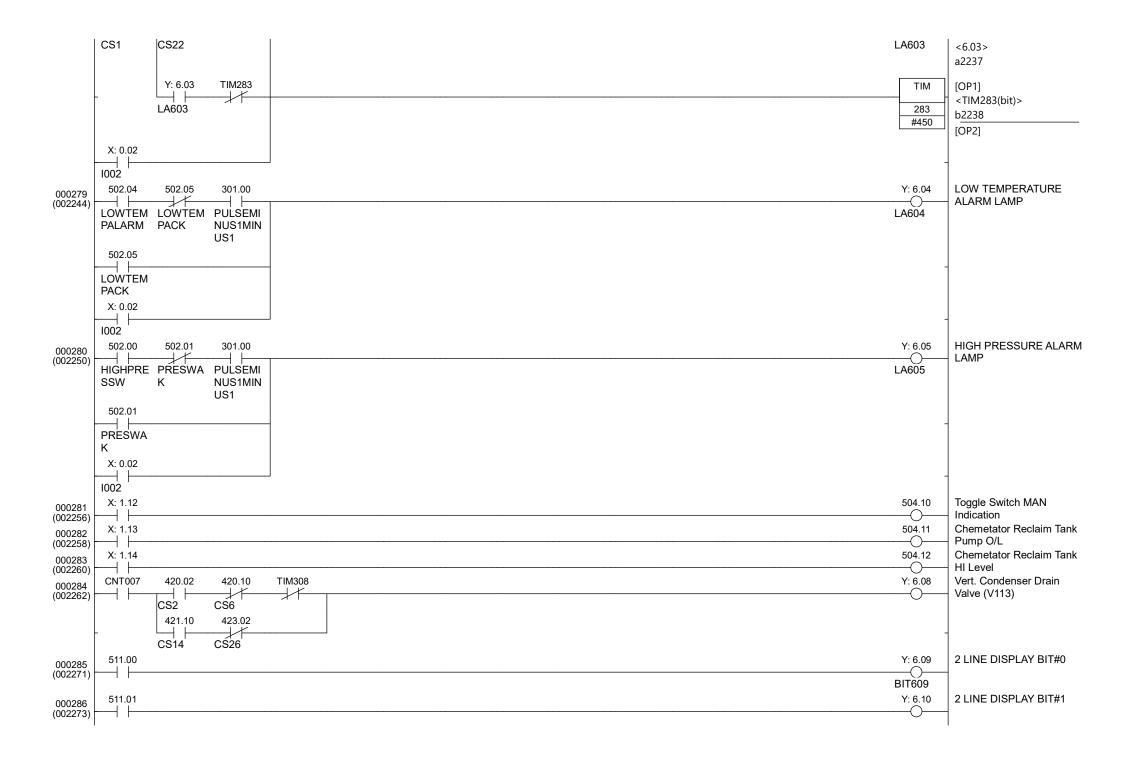


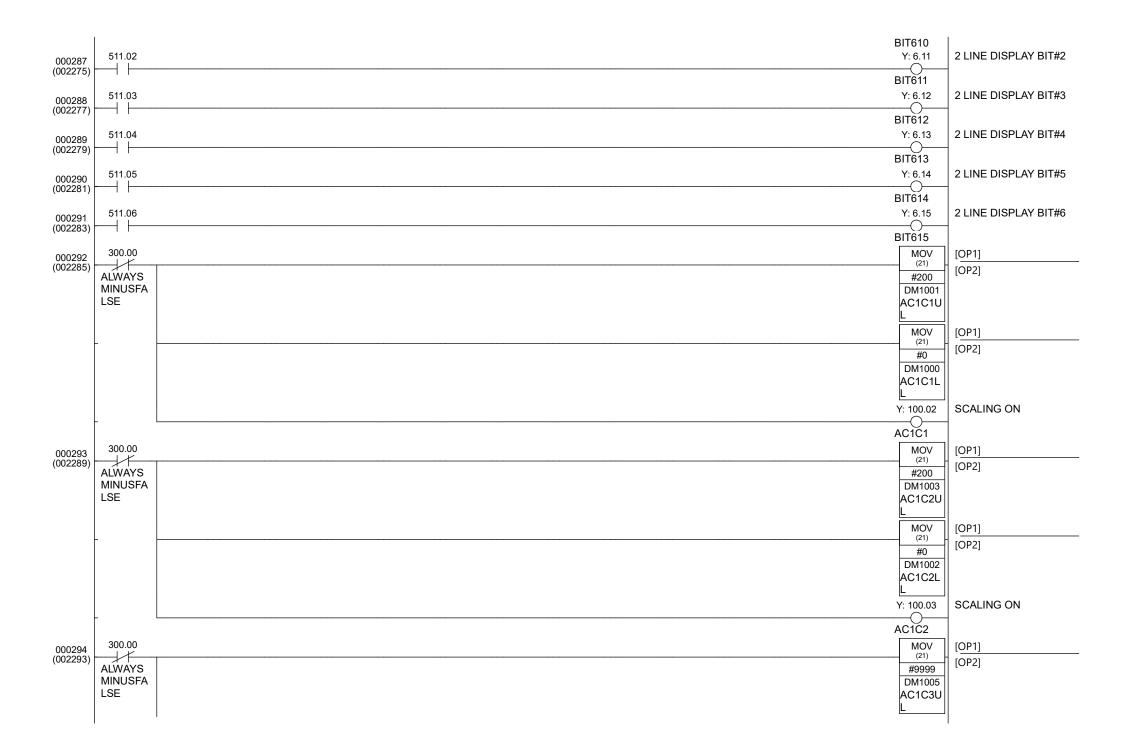




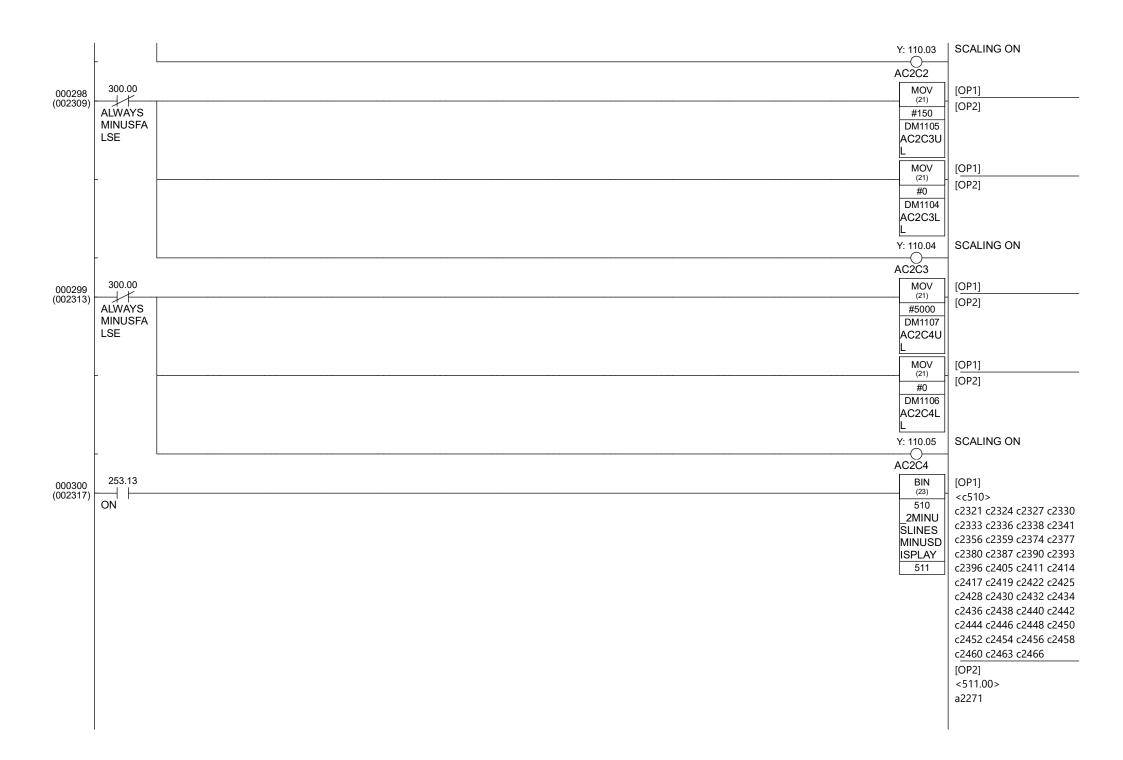


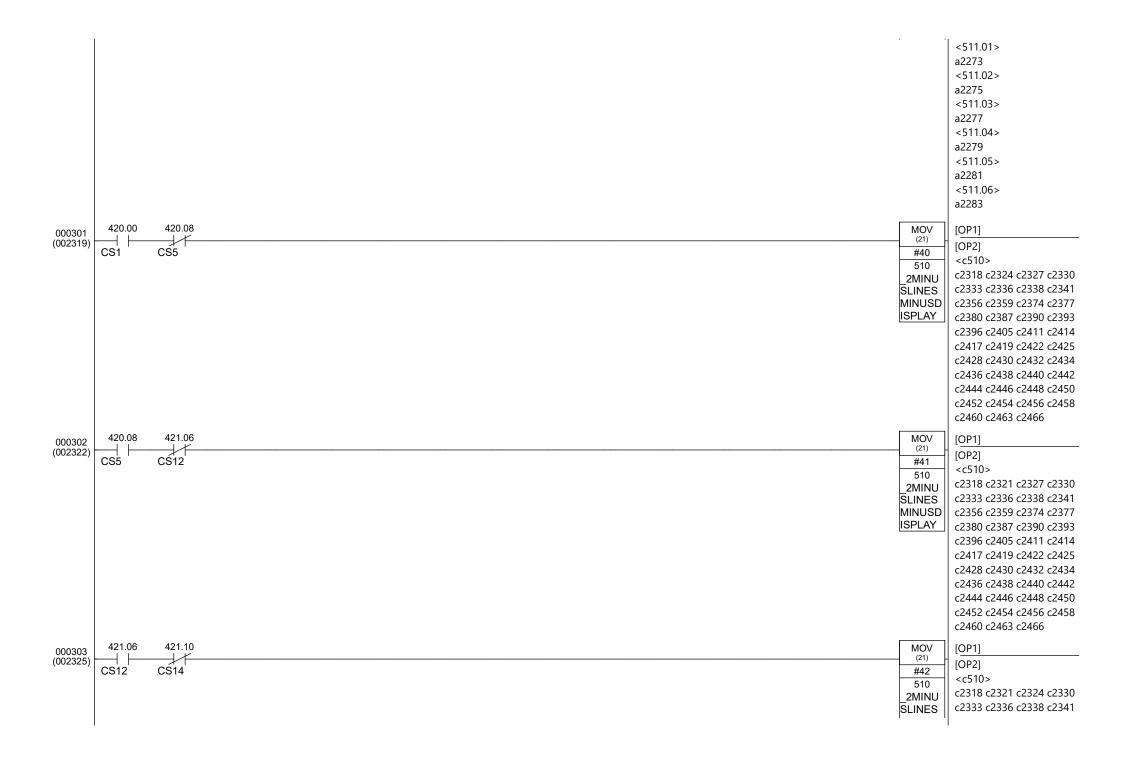


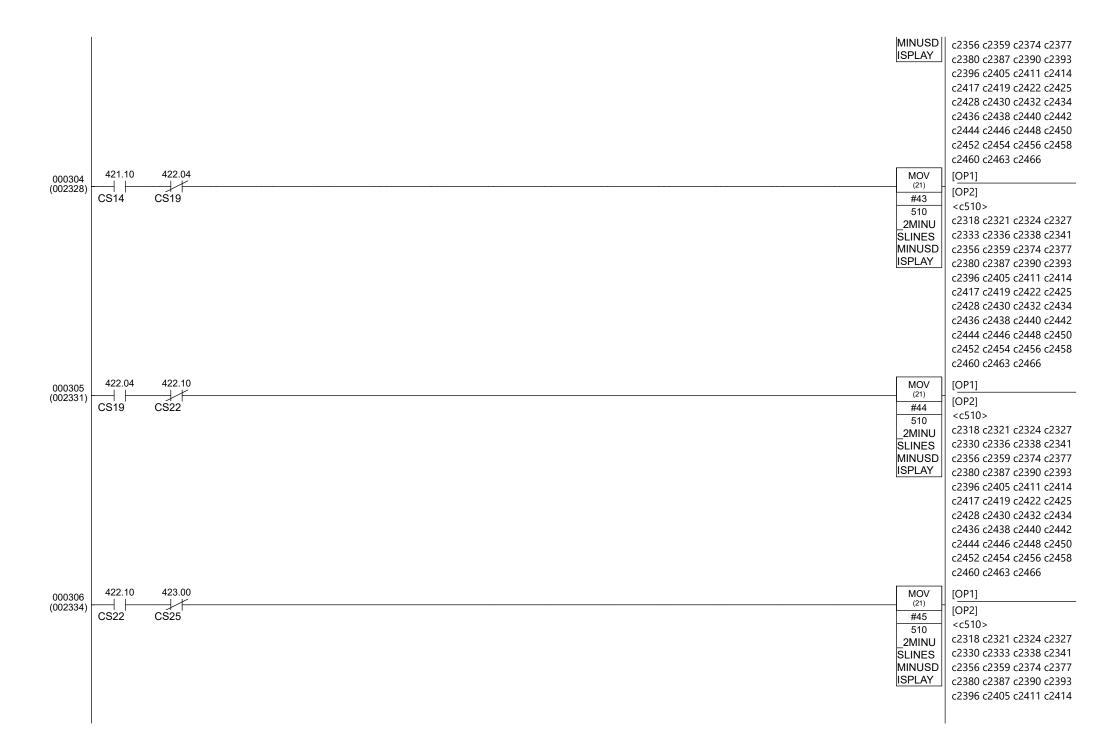


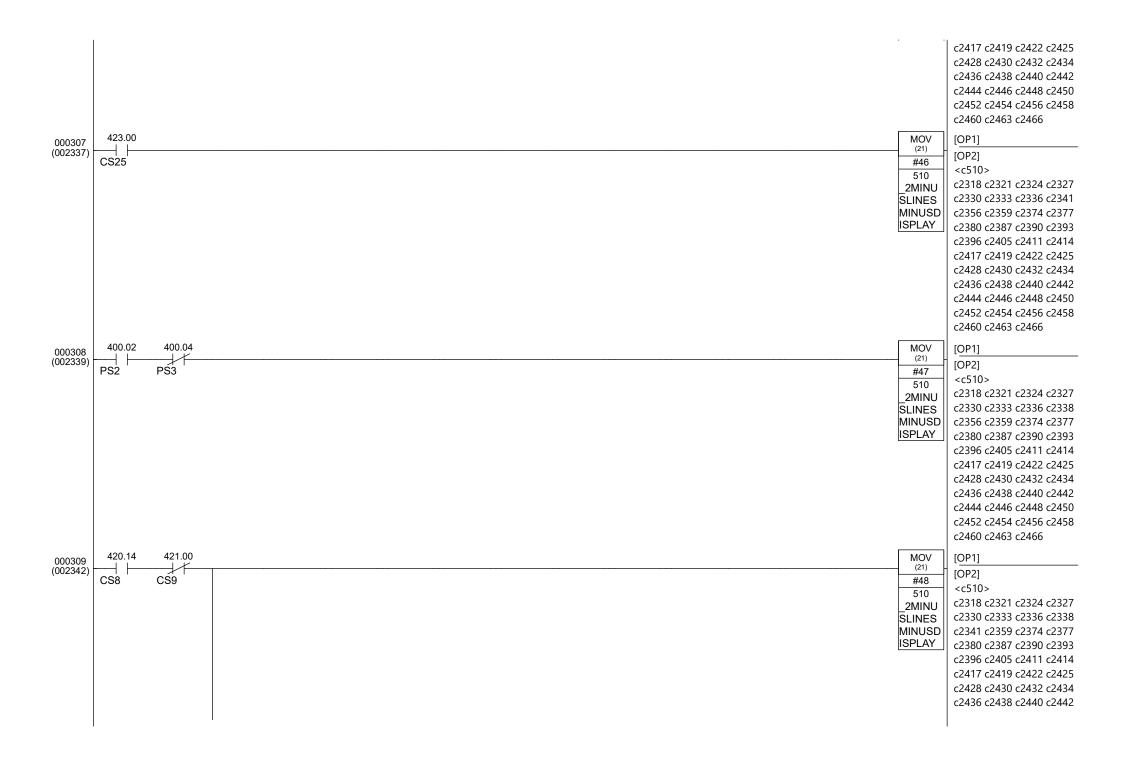


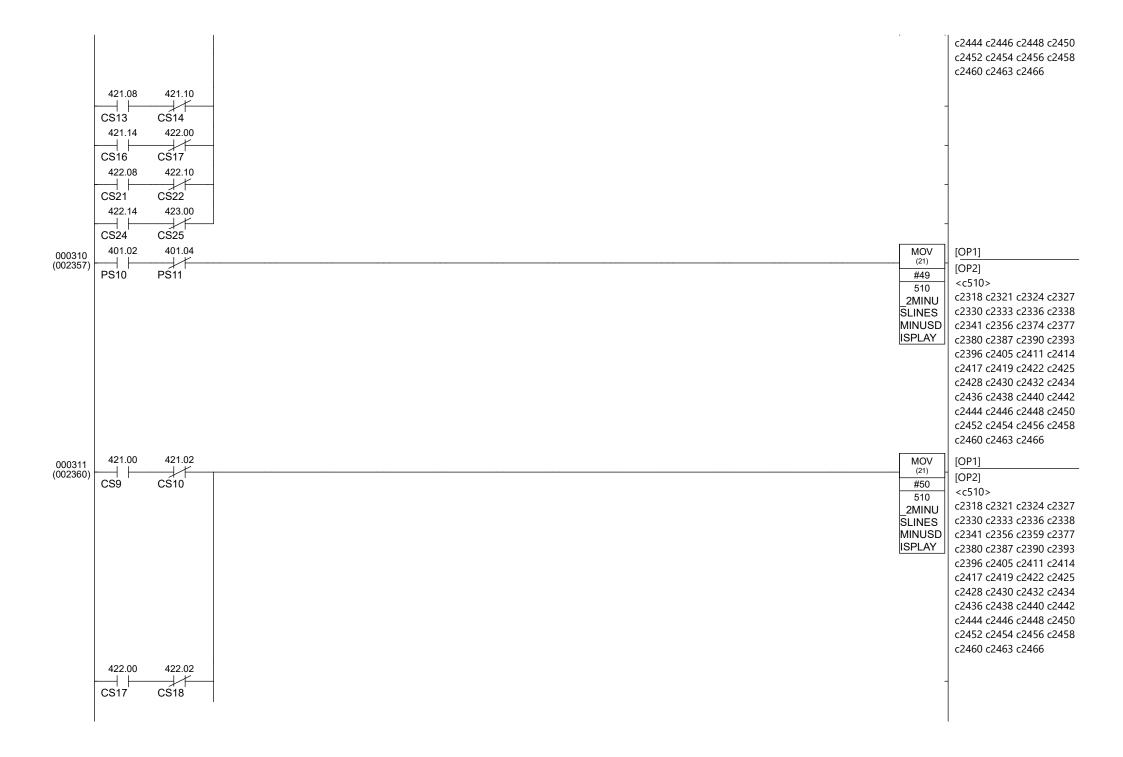
MOY   OP2		1		140)/	10041
O00295   O00297   O		-			[OP1]
AC1C3   Y:100.04   OC1C3   O					[ [0P2]
Control   Cont				DM1004	
MOV   CP1   CP2				L	
000295   000296   0			· ·	<i>(</i> : 100.04	SCALING ON
000295   000296   0		<u> </u>			
AUWAYS MINUSFA LSE  AUWAYS		300.00			[OD1]
ALWAYS  MINUSFA  LSE  MOV (21) (10) (22) (10) (21) (21) (21) (21) (21) (21) (21) (21		1-1/-		(21)	
SE		ALWAYS			[012]
L   MOV (21) #0   DM1006   AC1C4   Y: 100.05   AC1C4   CP1]   (OP2)   (OV2301)   MOV (21)   #100   M		LSE		AC1C4U	
000296 (002301) AC1C4  WOV (21) ALWAYS MINUSFA LSE  MOV (21) AC2C1U  MOV (				L	
100296   1002301)   100296   1002301)   100296   1002301)   100296   1002301   100296   1002301   100296   1002301   100296   1				MOV	[OP1]
000296 (002301) AC1C4 (002301) AC1C4 (002301) ALWAYS MINUSFA LSE (002301) AC2C1U (21) (002301					[OP2]
000296 300.00				DM1006	
000296 (002301)				AC1C4L	
000296 (002301)				└ /· 100 05	SCALING ON
300.00   MOV (21)   (OP2)		-		$-\!$	30/12/11/3
(002301) ALWAYS MINUSFA LSE    C21)   H100     DM1101     AC2C1U     WOV (21)     H00     DM1100     COP2]		200.00	A		
#100 DM1101 AC2C1U L [OP2]	000296	300.00		MOV (21)	
LSE AC2C1U L [OP1] [OP2]	000297	ALWAYS		#100	[UP2]
L		ISF		DM1101	
(21) #0 DM1100				L	
#0 DM1100				MOV	[OP1]
DM1100				#0	[OP2]
AC2C1L L				DM1100	
				AC2C1L	
Y: 110.02 SCALING ON				<u>-</u>	SCALING ON
AC2C1		-			30/12/11/3
		000.00	A		
000297 300.00 [OP1] (002305) (21) (OD23)		1 1 1		MOV (21)	
ALWAYS		ALWAYS		#100	[UP2]
MINUSFA LSE AC2C2U		I SF		DM1103	
				L .52525	
MOV [OP1]				MOV	[OP1]
(21) #0 [OP2]		<u> </u>		(21) #0	[OP2]
DM1102				DM1102	
AC2C2L				AC2C2L	
				<u> </u>	

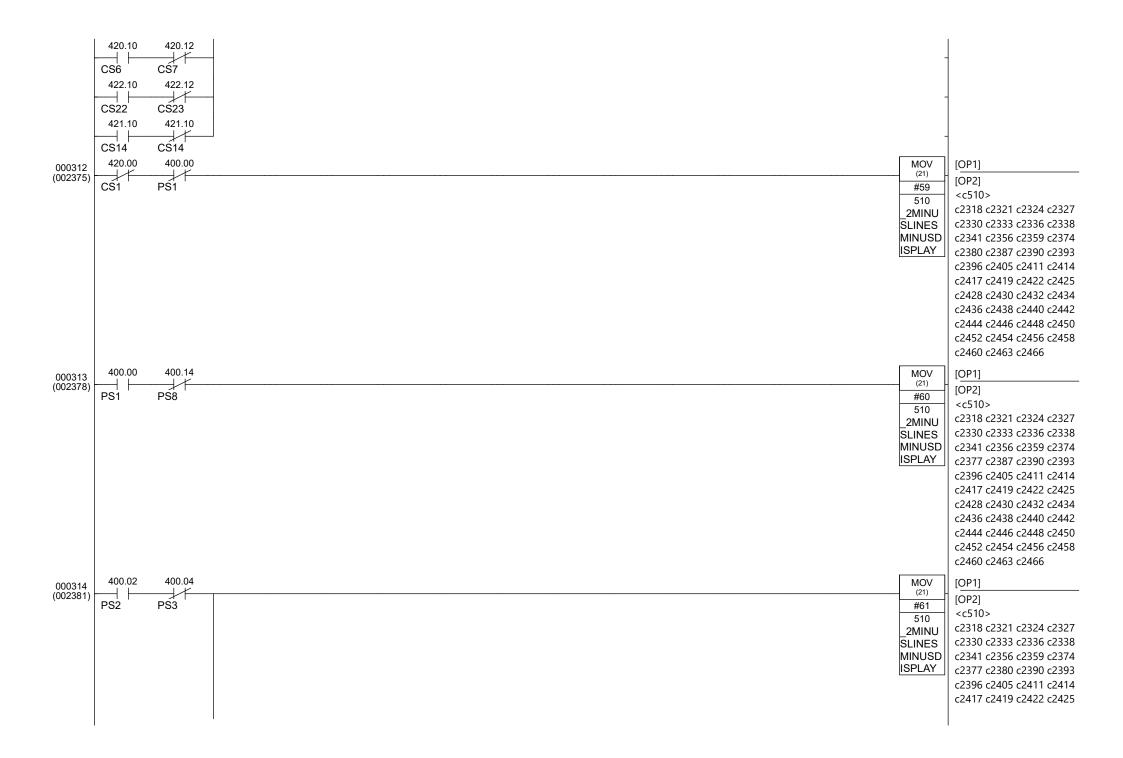


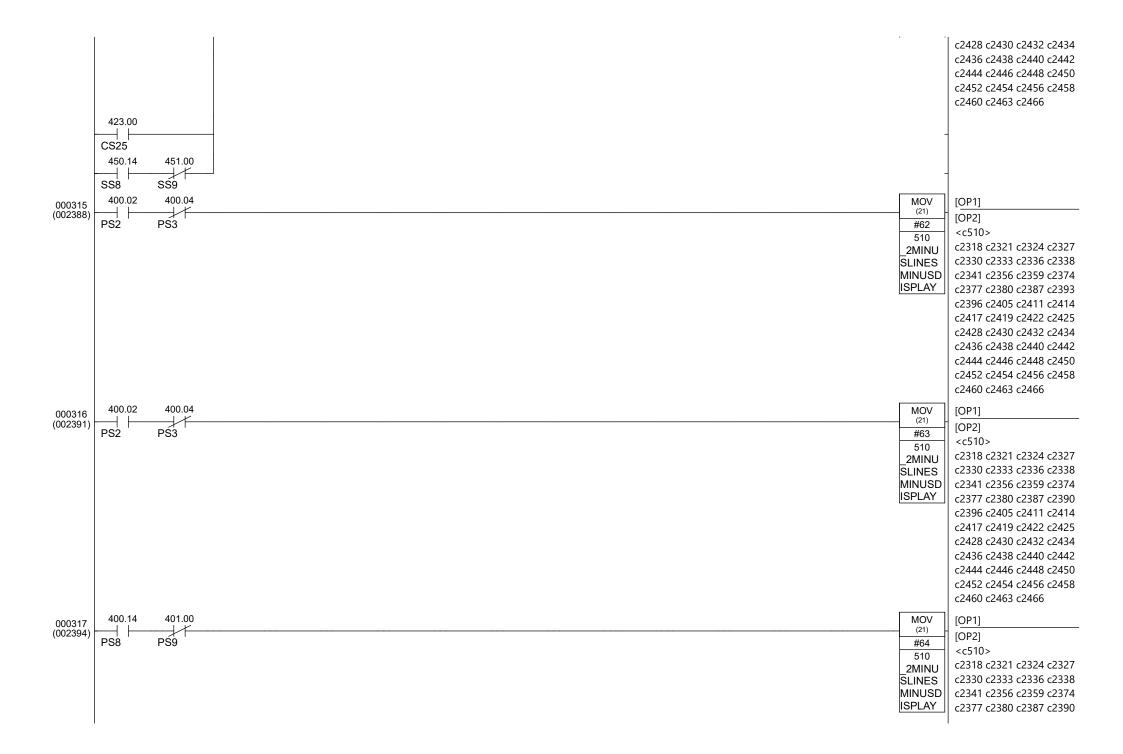


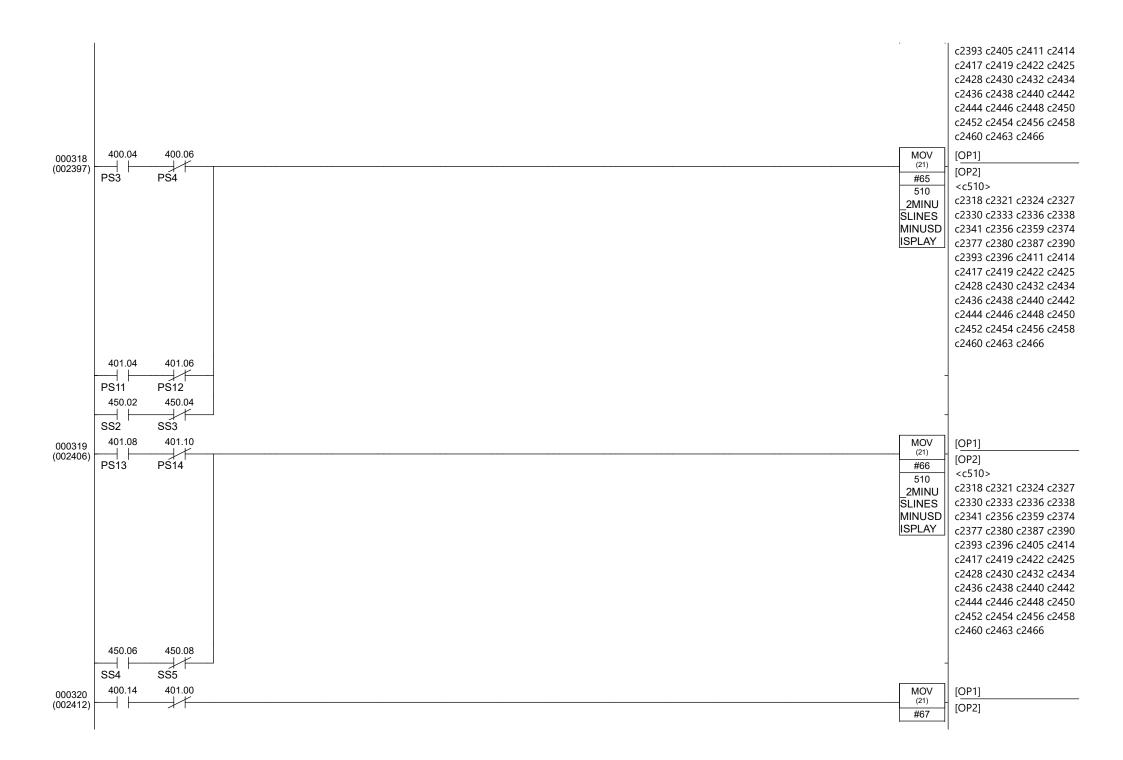


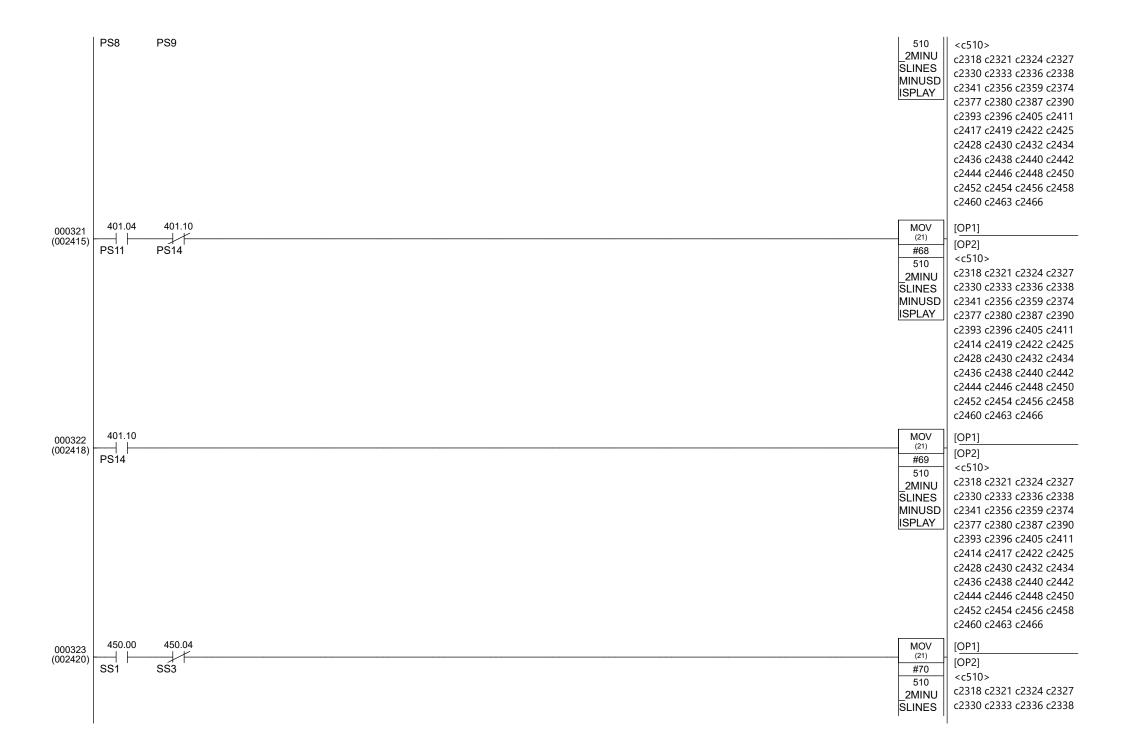


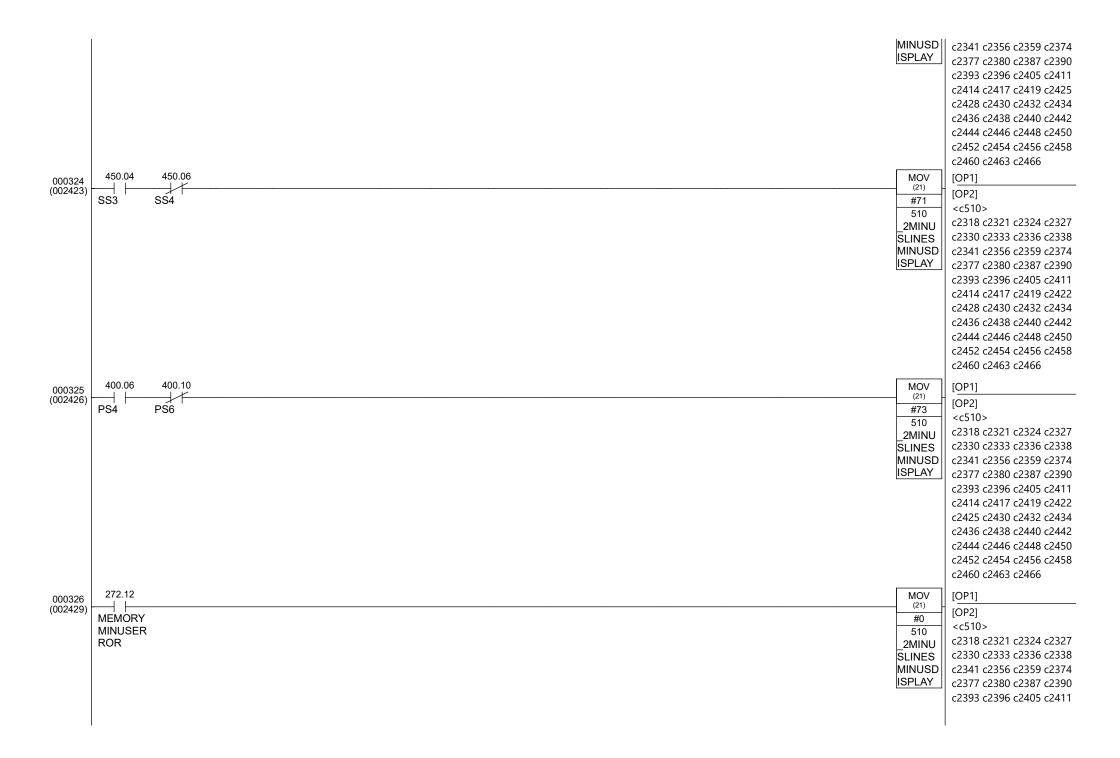




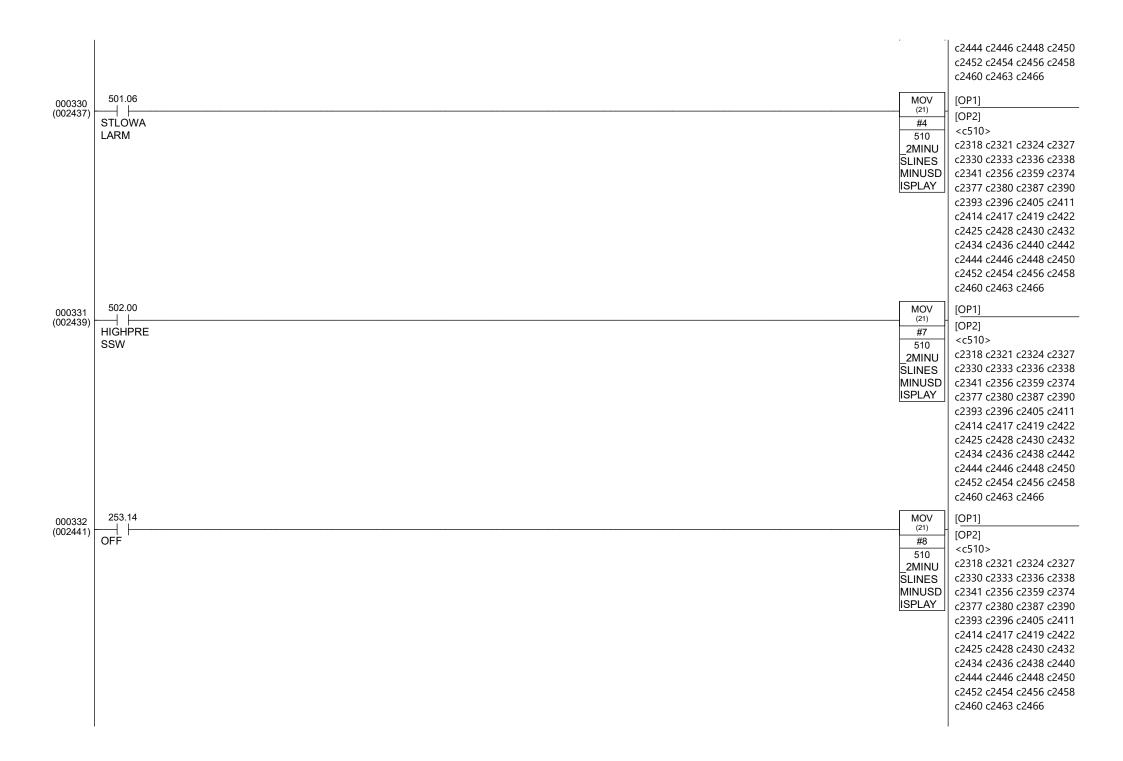






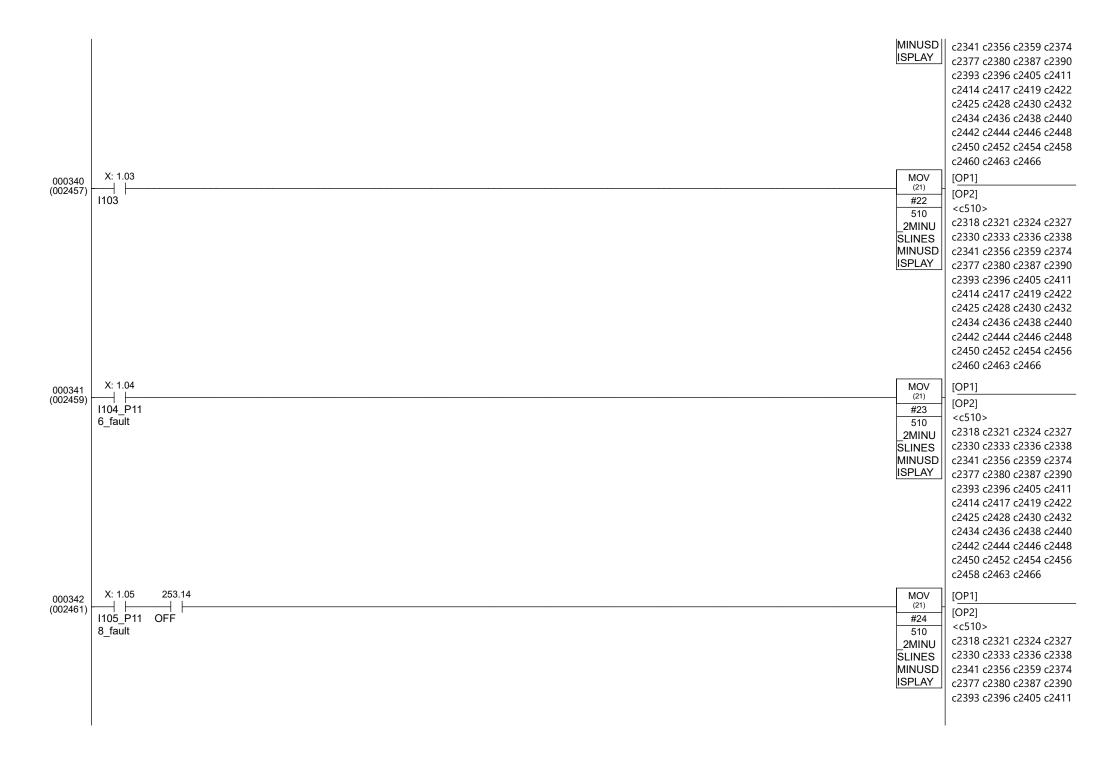


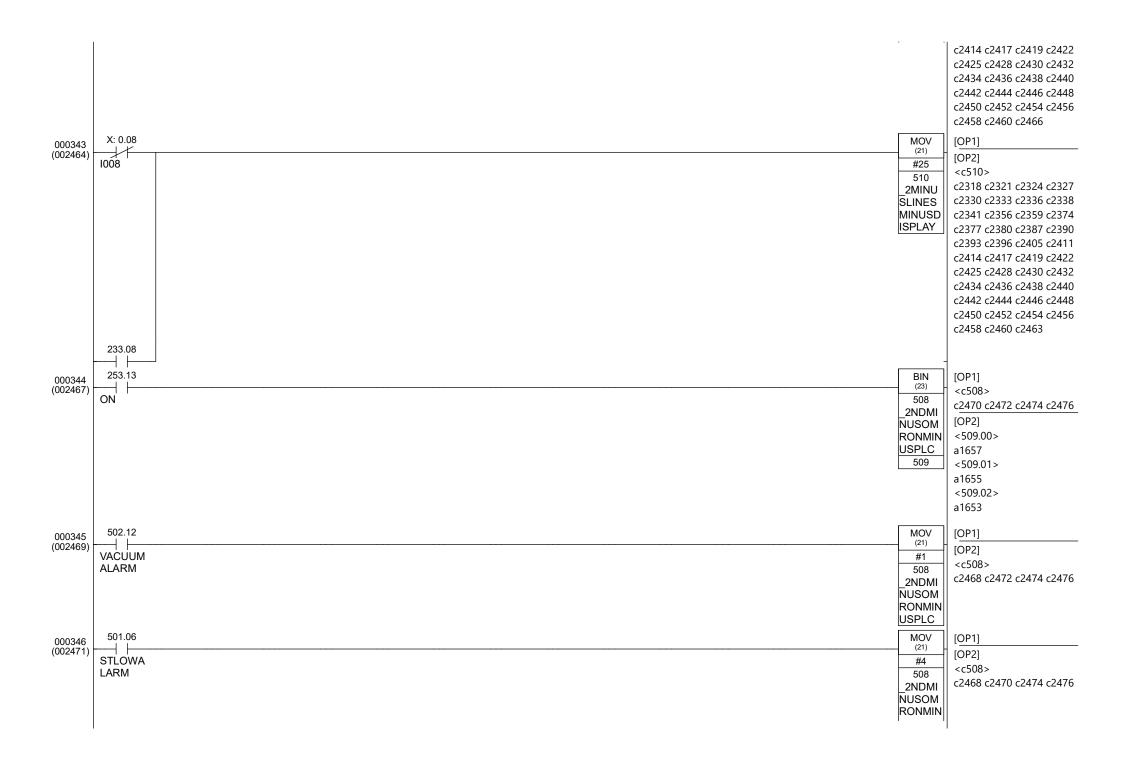


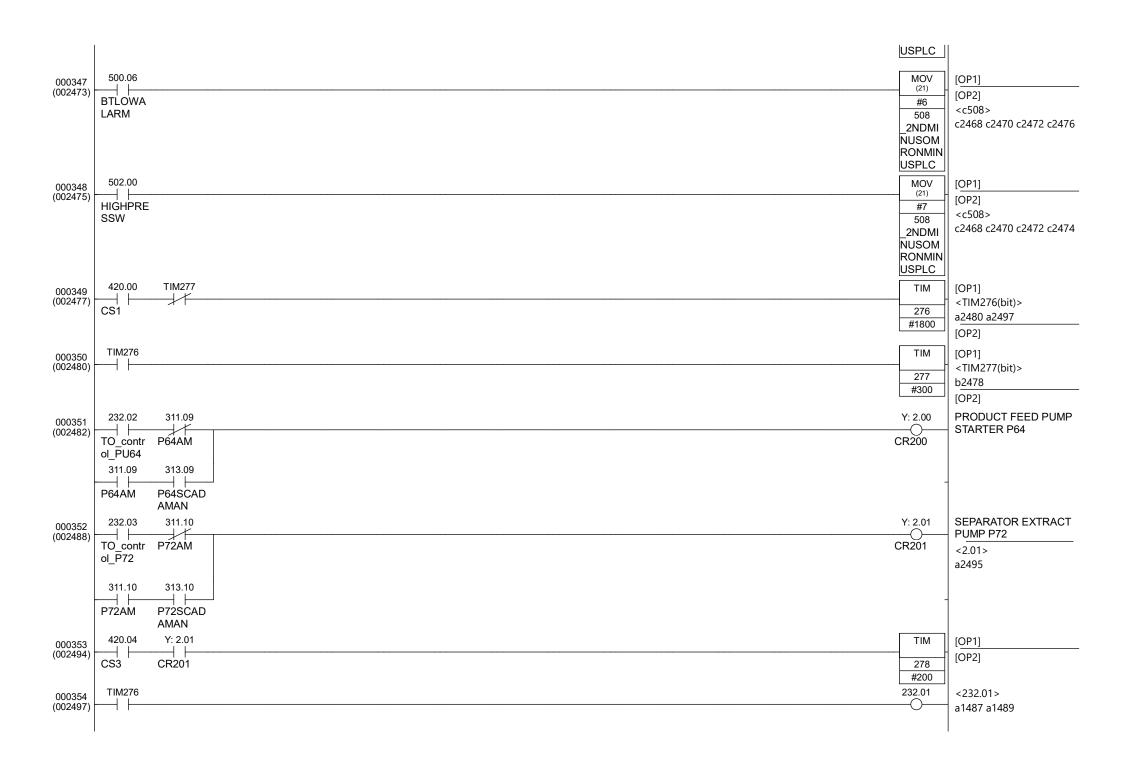


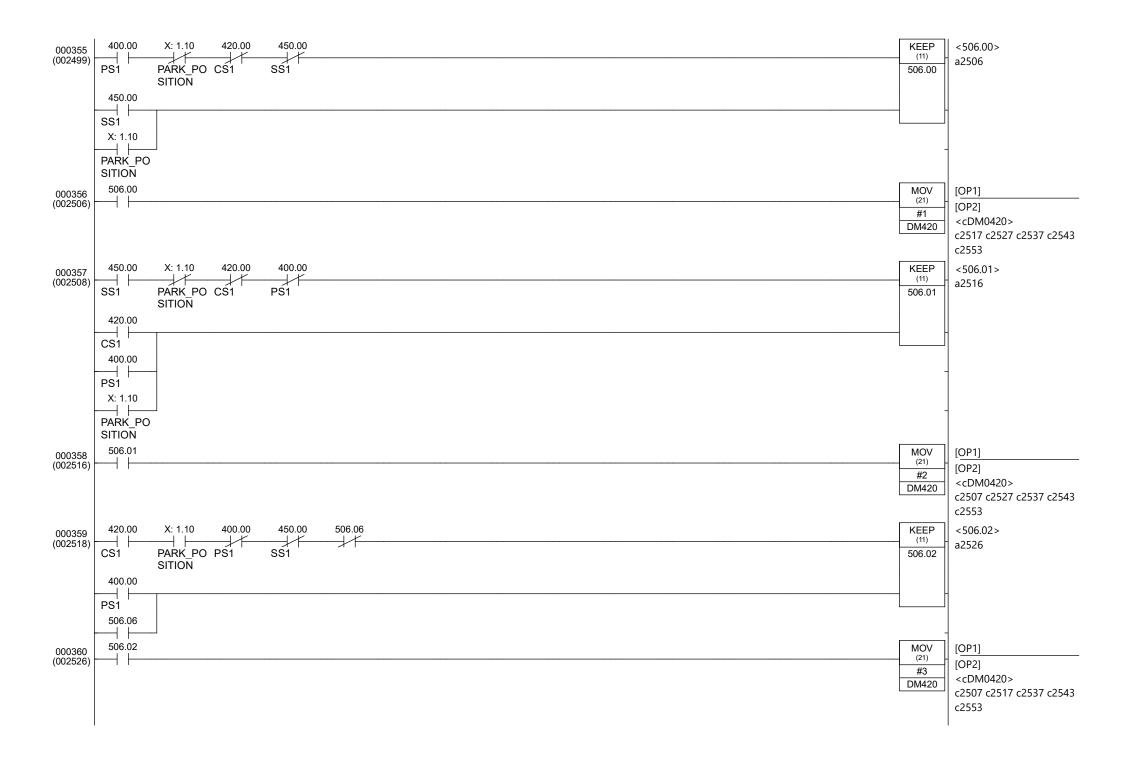


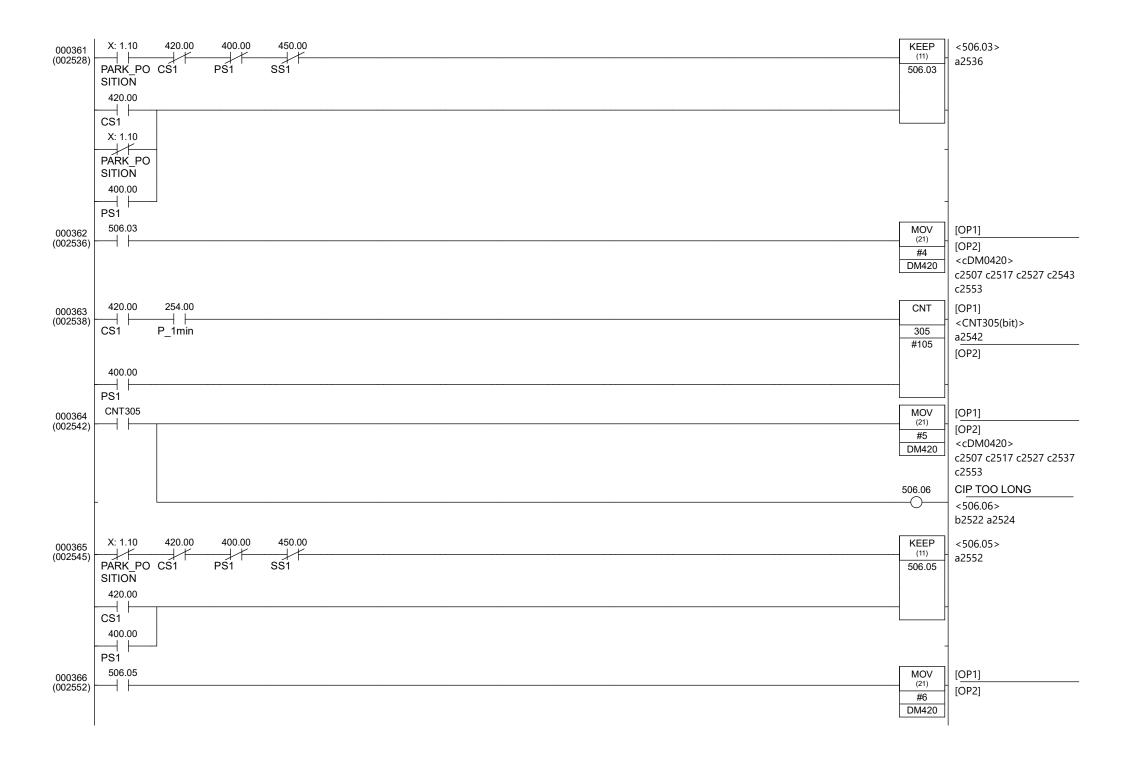


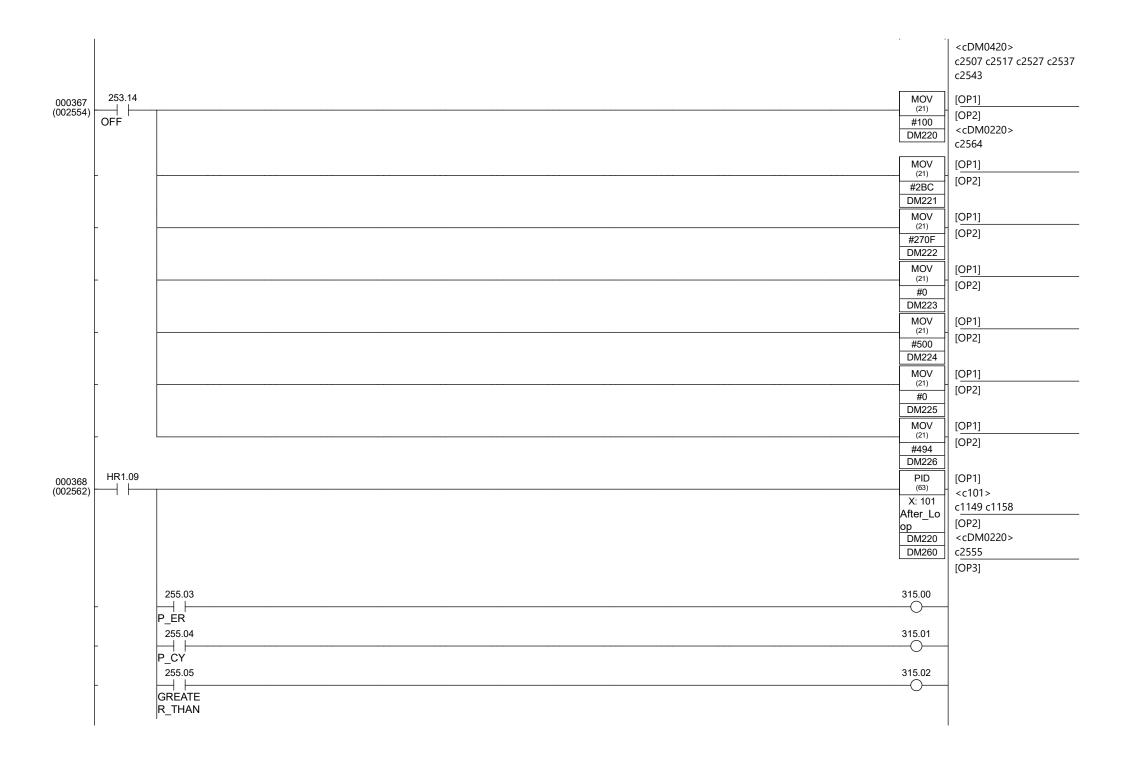




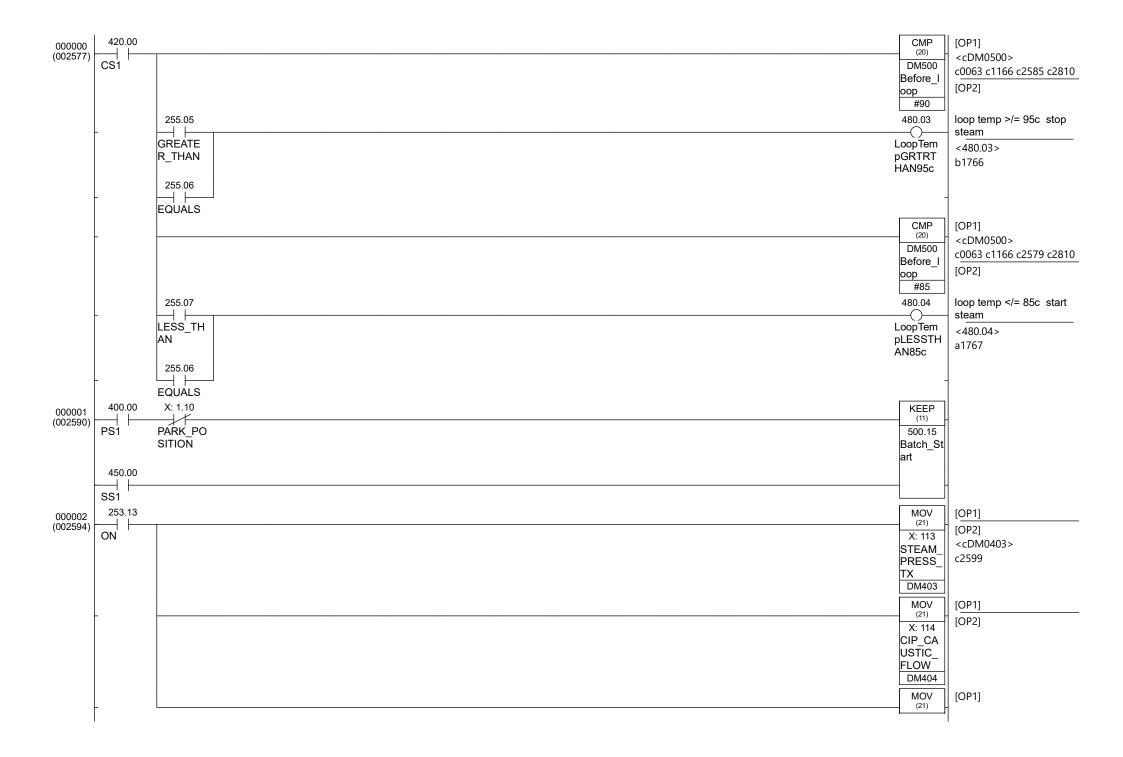


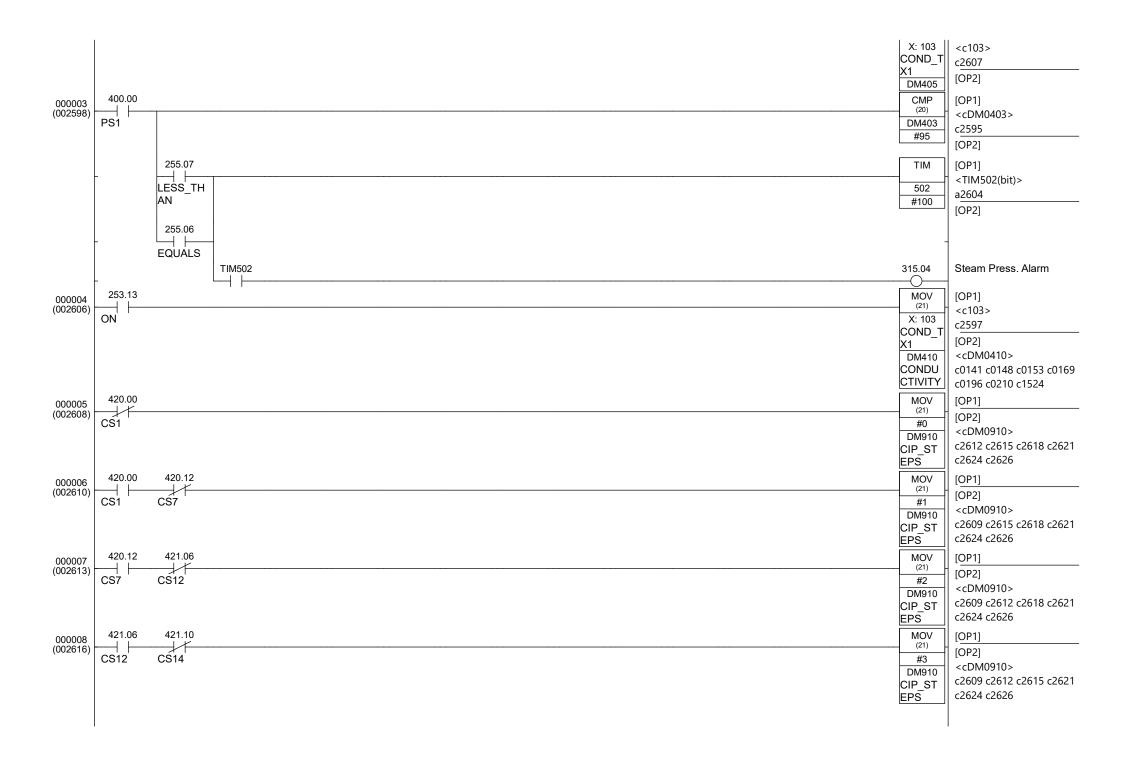


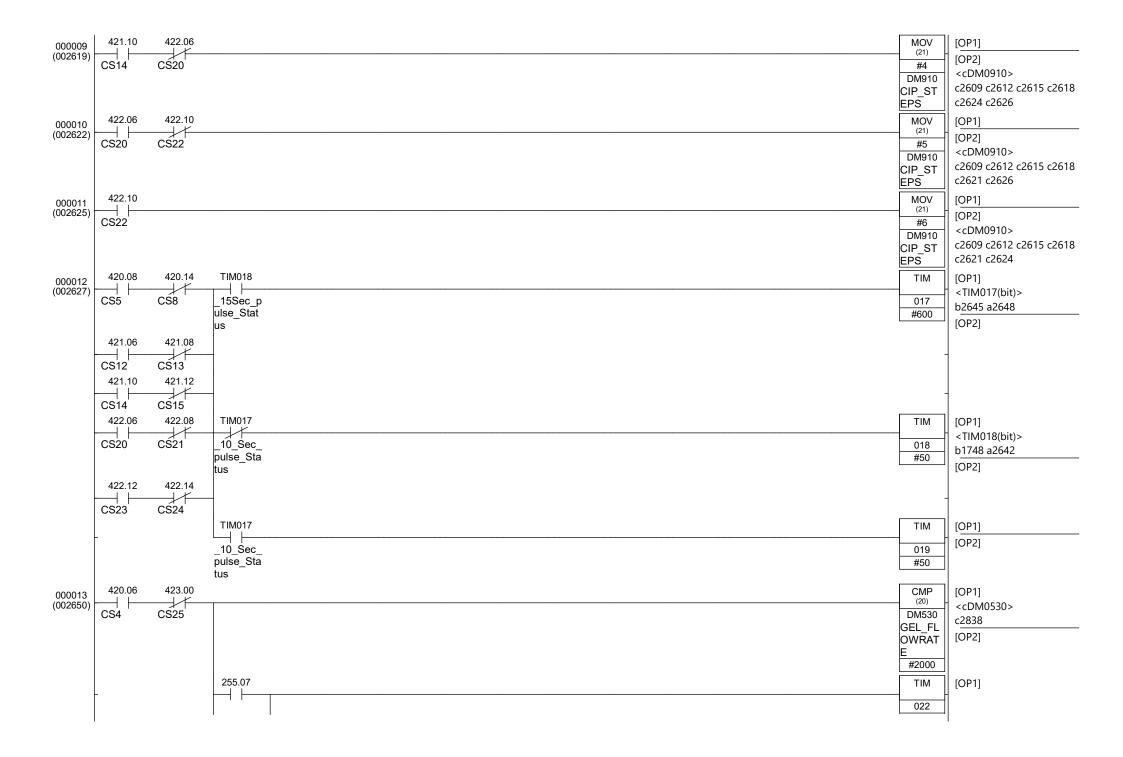


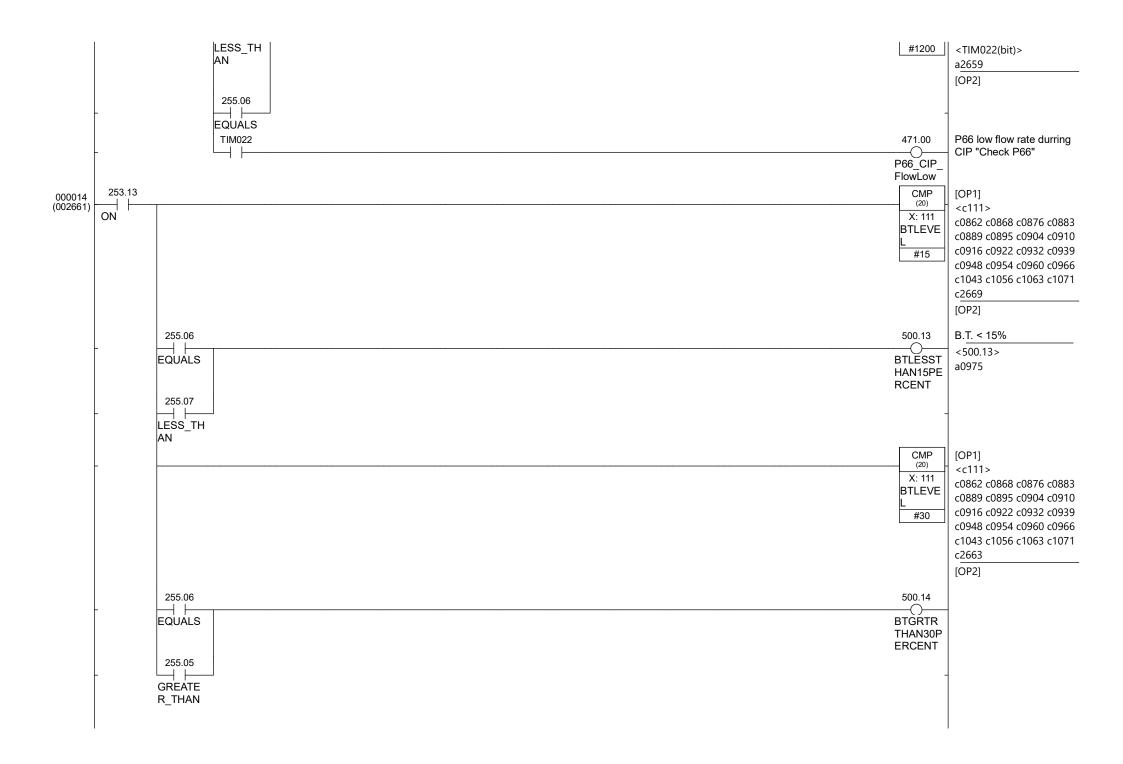


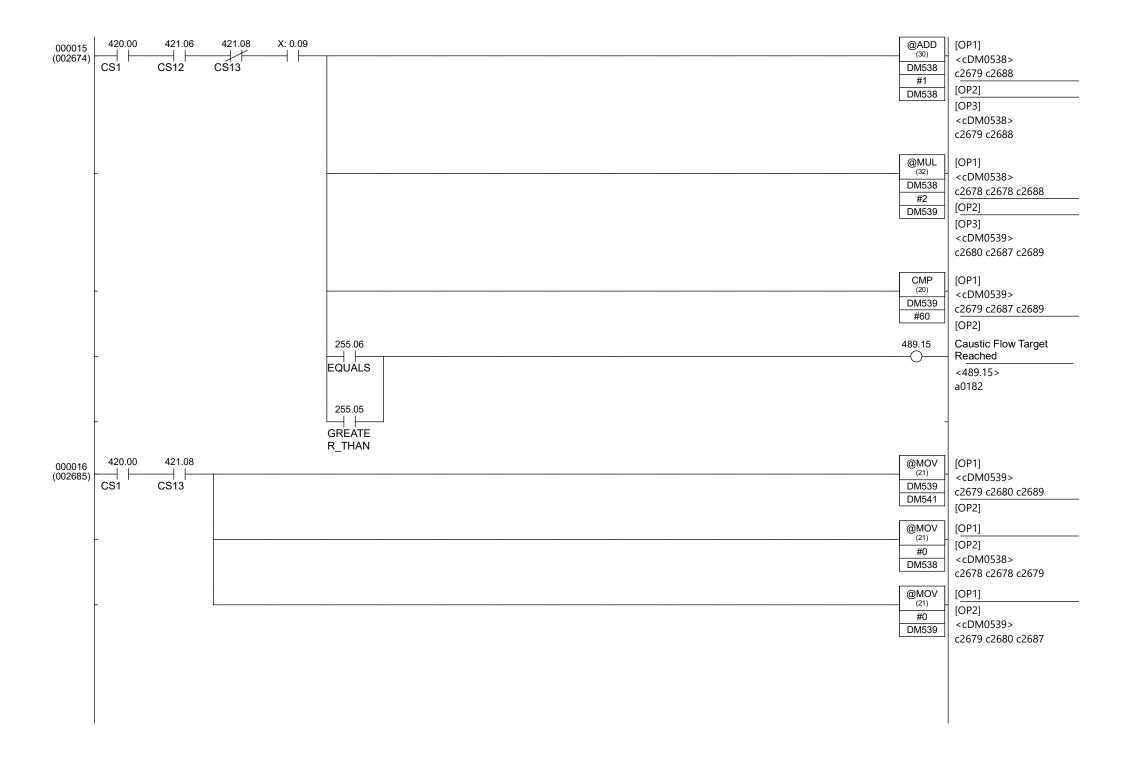
255.07 315.03 LESS\_TH AN



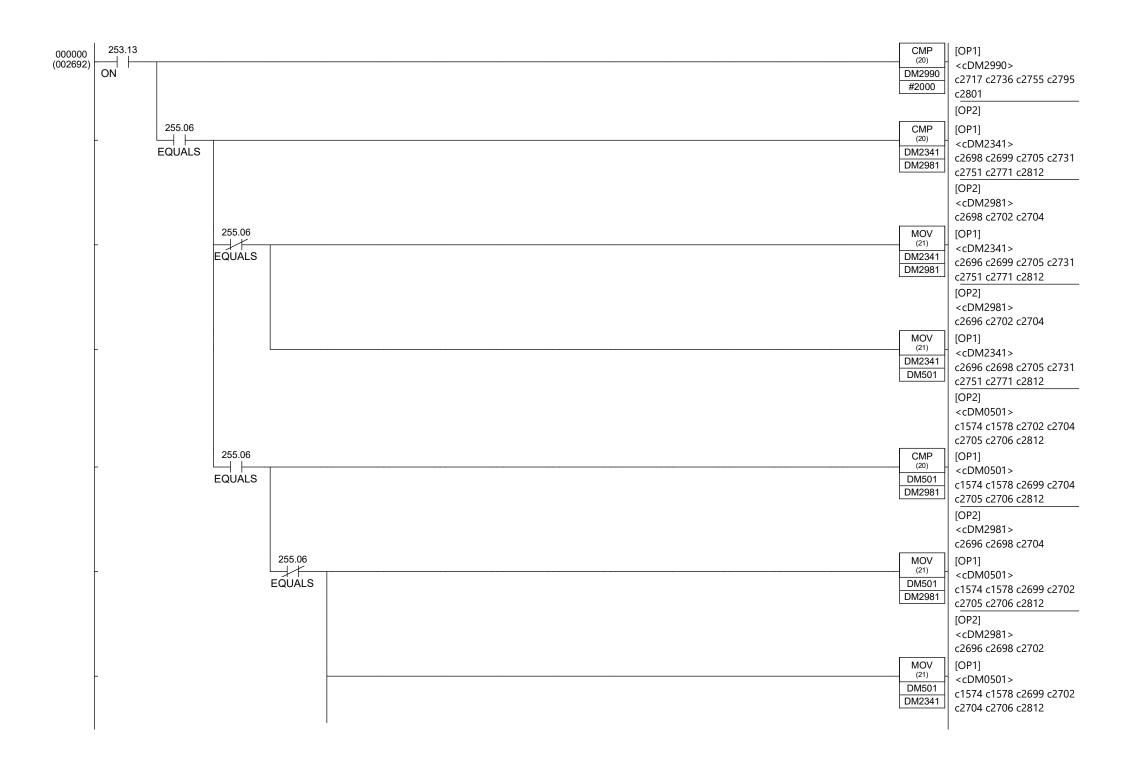


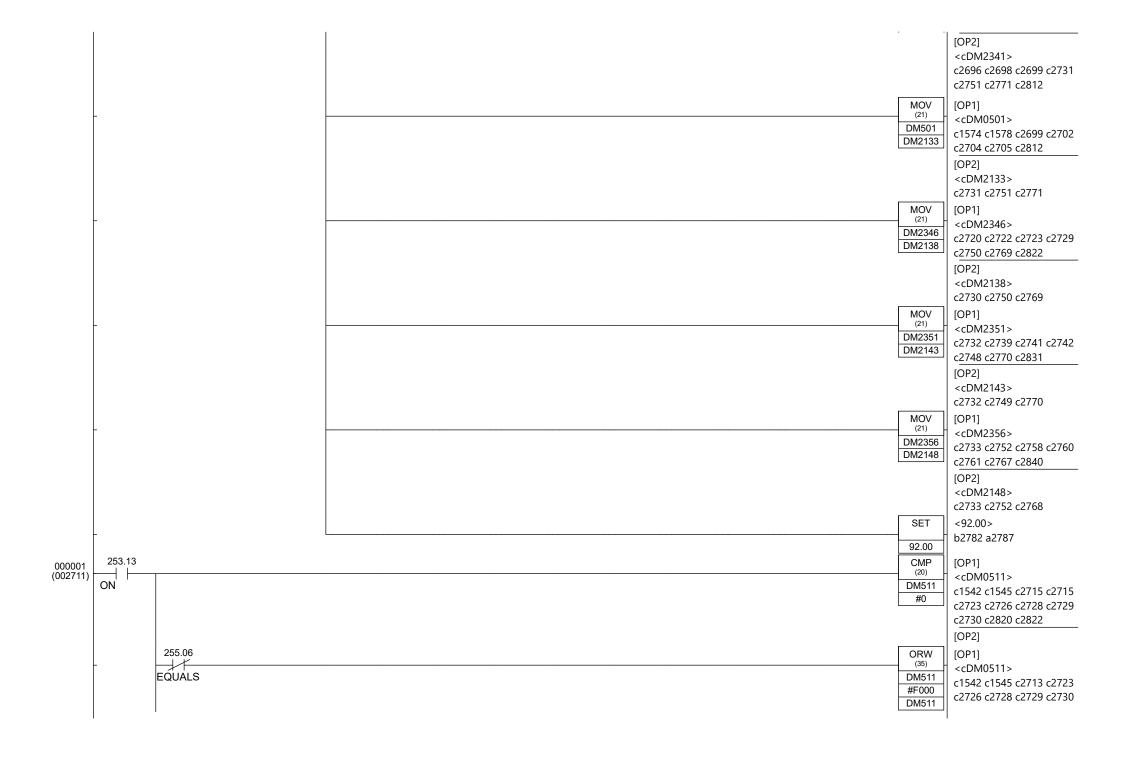


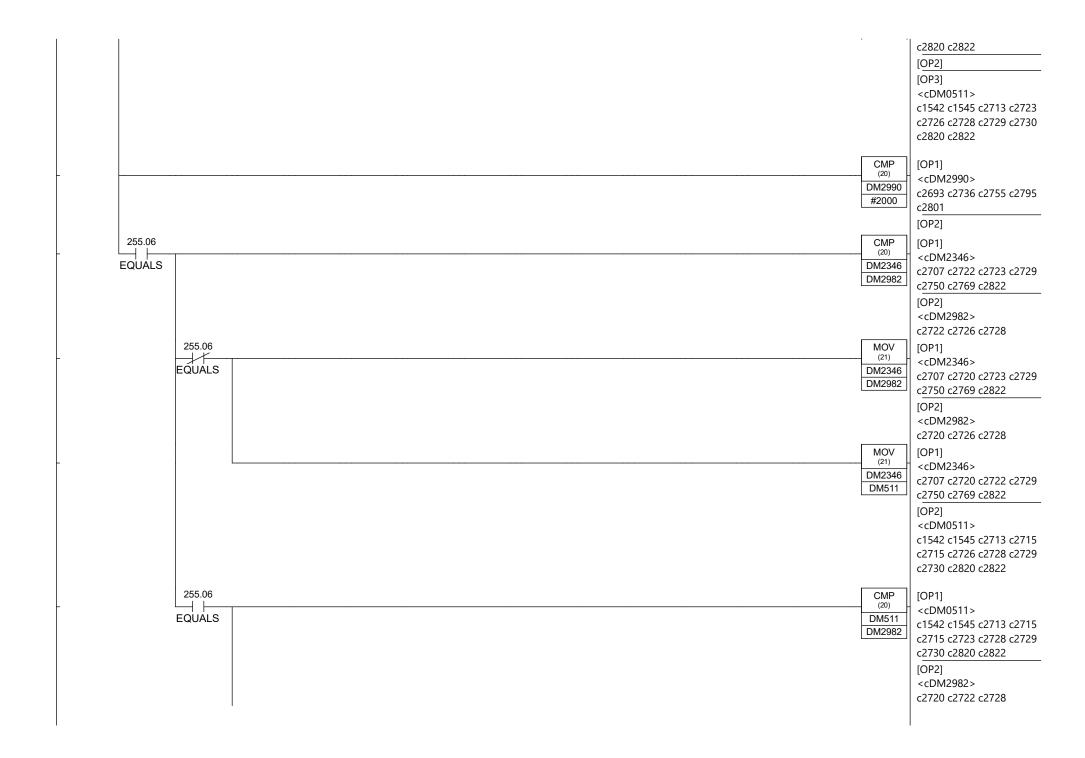




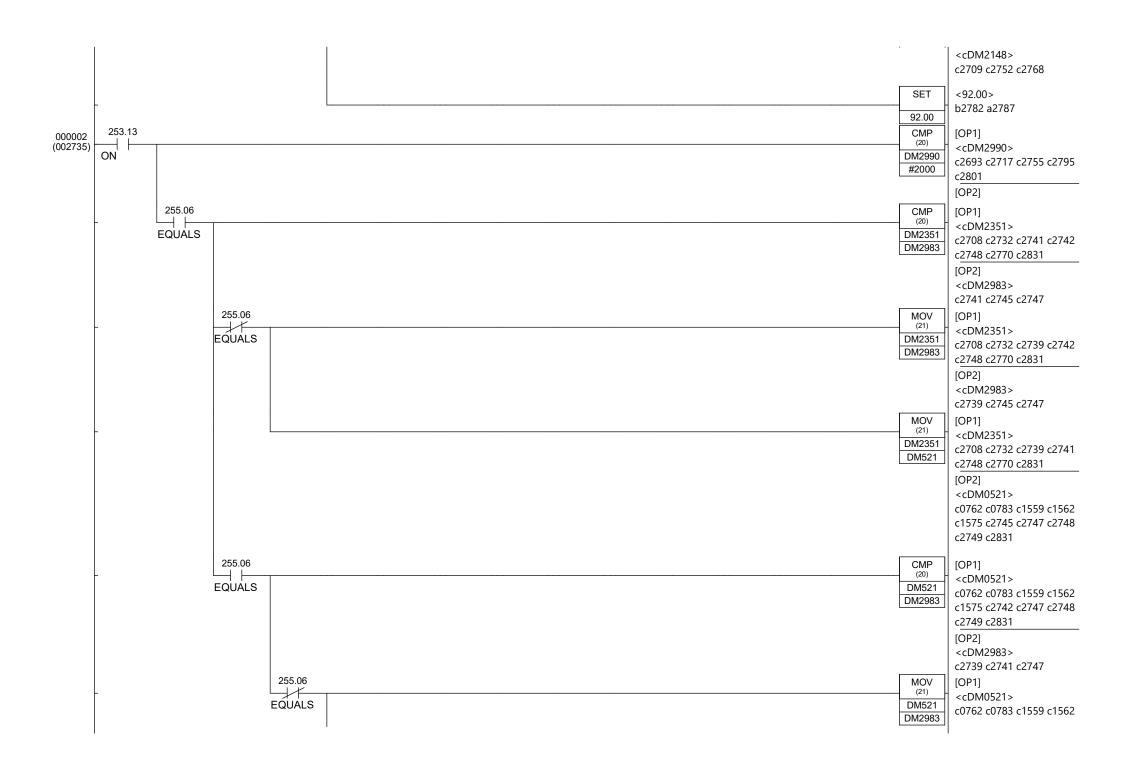




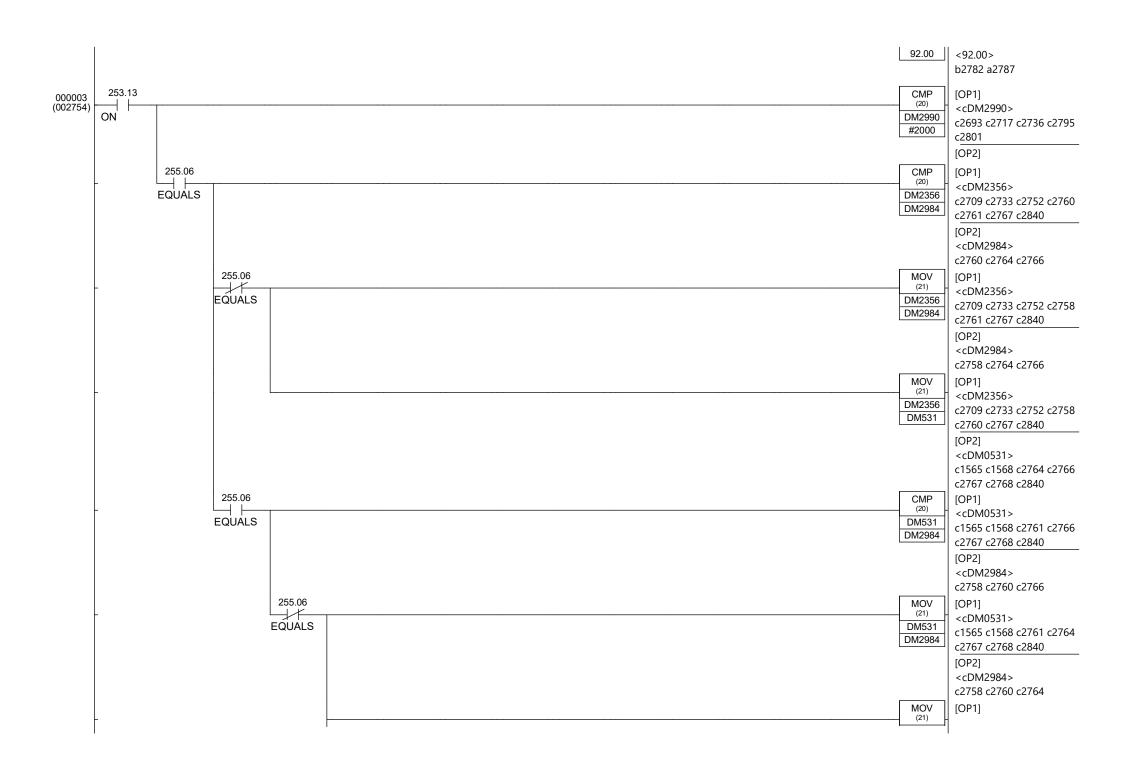




255.06	MOV (21)	[OP1]
EQUAL	DM511 DM2982	<cdm0511> c1542 c1545 c2713 c2715</cdm0511>
	DIVIZ902	c2715 c2723 c2726 c2729 c2730 c2820 c2822
		[OP2]
		<cdm2982> c2720 c2722 c2726</cdm2982>
-	MOV (21)	[OP1] <cdm0511></cdm0511>
	DM511 DM2346	c1542 c1545 c2713 c2715 c2715 c2723 c2726 c2728
		c2730 c2820 c2822
		[OP2] <cdm2346></cdm2346>
		c2707 c2720 c2722 c2723 c2750 c2769 c2822
-	MOV (21)	[OP1]   <cdm0511></cdm0511>
	DM511 DM2138	c1542 c1545 c2713 c2715 c2715 c2723 c2726 c2728
		c2729 c2820 c2822
		[OP2] <cdm2138></cdm2138>
	MOV	c2707 c2750 c2769 [OP1]
	DM2341	<cdm2341> c2696 c2698 c2699 c2705</cdm2341>
	DM2133	c2751 c2771 c2812
		[OP2] <cdm2133></cdm2133>
	MOV	c2706 c2751 c2771 [OP1]
-	(21) DM2351	<cdm2351></cdm2351>
	DM2143	c2708 c2739 c2741 c2742 c2748 c2770 c2831
		[OP2] <cdm2143></cdm2143>
	MCV	c2708 c2749 c2770
-	MOV (21) DM2356	[OP1] <cdm2356></cdm2356>
	DM2148	c2709 c2752 c2758 c2760 c2761 c2767 c2840
		[OP2]



-	MOV (21) DM52 DM23	<cdm0521></cdm0521>
_	MOV (21)  DM52  DM21-	<cdm0521> c0762 c0783 c1559 c1562 c1575 c2742 c2745 c2747 c2748 c2831 [OP2] <cdm2143> c2708 c2732 c2770</cdm2143></cdm0521>
	(21) DM23: DM21:	<cdm2346> c2707 c2720 c2722 c2723 c2729 c2769 c2822 [OP2] <cdm2138> c2707 c2730 c2769</cdm2138></cdm2346>
_	(21) DM23: DM21:	<cdm2341> c2696 c2698 c2699 c2705 c2731 c2771 c2812 [OP2] <cdm2133> c2706 c2731 c2771</cdm2133></cdm2341>
	MOV (21)  DM23:  DM21-	<cdm2356> c2709 c2733 c2758 c2760 c2761 c2767 c2840 [OP2] <cdm2148> c2709 c2733 c2768</cdm2148></cdm2356>
-	SET	_}



		DM531 DM2356	<cdm0531> c1565 c1568 c2761 c2764</cdm0531>
			c2766 c2768 c2840
			[OP2]
			<cdm2356> c2709 c2733 c2752 c2758</cdm2356>
			c2760 c2761 c2840
		MOV	[OP1]
	_	(21)	<cdm0531></cdm0531>
		DM531 DM2148	c1565 c1568 c2761 c2764
			c2766 c2767 c2840
			[OP2] <cdm2148></cdm2148>
			c2709 c2733 c2752
		MOV	[OP1]
	-	(21) DM2346	<cdm2346></cdm2346>
		DM2138	c2707 c2720 c2722 c2723
			c2729 c2750 c2822 [OP2]
			<cdm2138></cdm2138>
			c2707 c2730 c2750
		MOV (21)	OP1]
	=	DM2351	<cdm2351></cdm2351>
		DM2143	c2708 c2732 c2739 c2741 c2742 c2748 c2831
			[OP2]
			<cdm2143></cdm2143>
			c2708 c2732 c2749
	_	MOV (21)	[OP1]
		DM2341	<cdm2341> c2696 c2698 c2699 c2705</cdm2341>
		DM2133	c2731 c2751 c2812
			[OP2]
			<cdm2133></cdm2133>
			c2706 c2731 c2751
	-	SET	<92.00> b2782 a2787
		92.00	1 52702 42707
000004 (002773)	289.08 	@CLC (41)	
(002113)			
		@ADD (30)	[OP1]
		#3	[OP2] <cdm2995></cdm2995>
		DM2995 DM2995	<cdm2995>   c2777 c2779 c2780 c2784</cdm2995>
		DM2995	c2788 c2790 c2795
	l		

