Performance Analysis and Optimization

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Resources

Reference card for OpenMP

- http://openmp.org/mp-documents/OpenMP3.1-CCard.pdf

Reference for Vector-Intrinsics

- https://software.intel.com/sites/landingpage/Intrins icsGuide/

Exercises

– github.com/g-koutsou/LAP2015/

git clone https://github.com/g-koutsou/LAP2015.git

Generic instructions:

- A Makefile for each exercise subdirectory
 - Type make to compile
- A job script, run.sh, requesting 1 node for 5-15 mins.
 - Type qsub run.sh to submit job
 - Type qstat -u \$USER to see the status (queued, running, completing)
 - Upon completion a file with a .log extension contains the output of the run
- ___TODO___ tags in code indicate where you need to add code for completing the exercises

Exercise 1

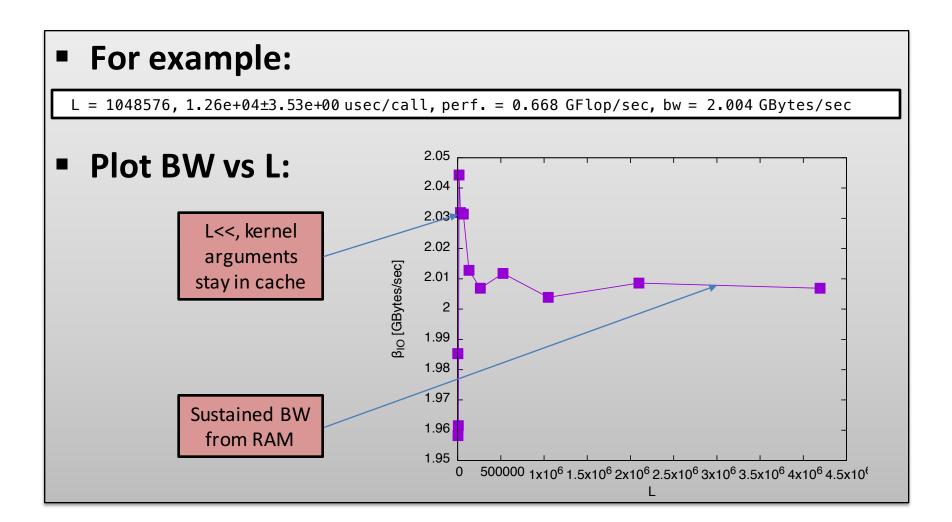
— Subdirs: Ex1a, Ex1b, Ex1c

Ex1a

– Complex, single precision, axpy kernel:

```
for(i=0; i<L; i++)
y[i] = a*x[i] + y[i]
```

- Modify program to report
 - Time per kernel call (μsec)
 - Floating point rate (Gflop/sec)
 - I/O rate (Gbytes/sec)



Implement Kernel using vector-intrinsics

```
for(i=0; i<L; i+=2) {
   y[i ].re = a.re*x[i ].re - a.im*x[i ].im + y[i ].re
   y[i ].im = a.re*x[i ].im + a.im*x[i ].re + y[i ].im
   y[i+1].re = a.re*x[i+1].re - a.im*x[i+1].im + y[i+1].re
   y[i+1].im = a.re*x[i+1].im + a.im*x[i+1].re + y[i+1].im
}</pre>
```

Implement Kernel using vector-intrinsics

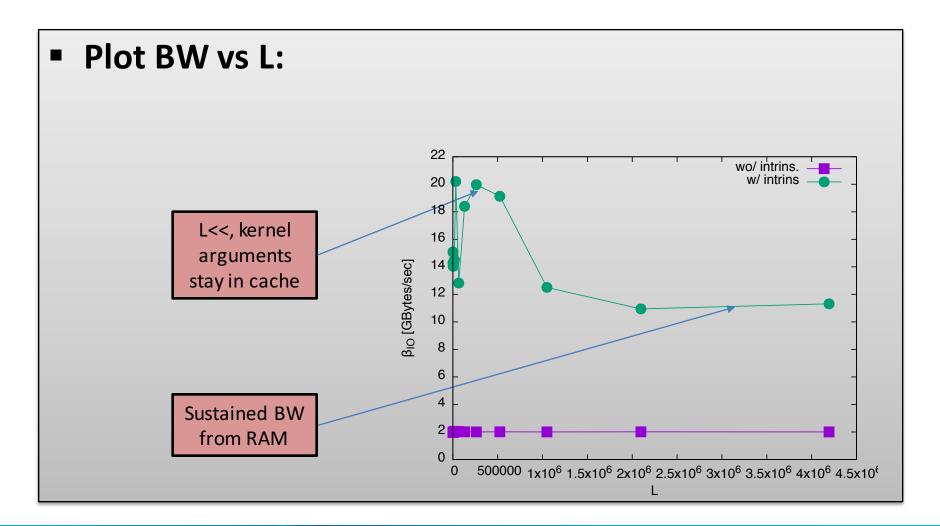
Implement Kernel using vector-intrinsics

```
for(i=0: i<|; i+=2) {
   y[i ].re
   y[i ].im
   y[i ].im
   y[i+1].re
   y[i+1].im = a.re*x[i ].im + a.im*x[i ].re + y[i ].im
   y[i+1].im = a.re*x[i+1].re
   y[i+1].im = a.re*x[i+1].im + a.im*x[i+1].re + y[i+1].im
}</pre>
```

Implement Kernel using vector-intrinsics

For every (double-) iteration

- One shuffle
- Two add
- Two mul
- Two loads, one store

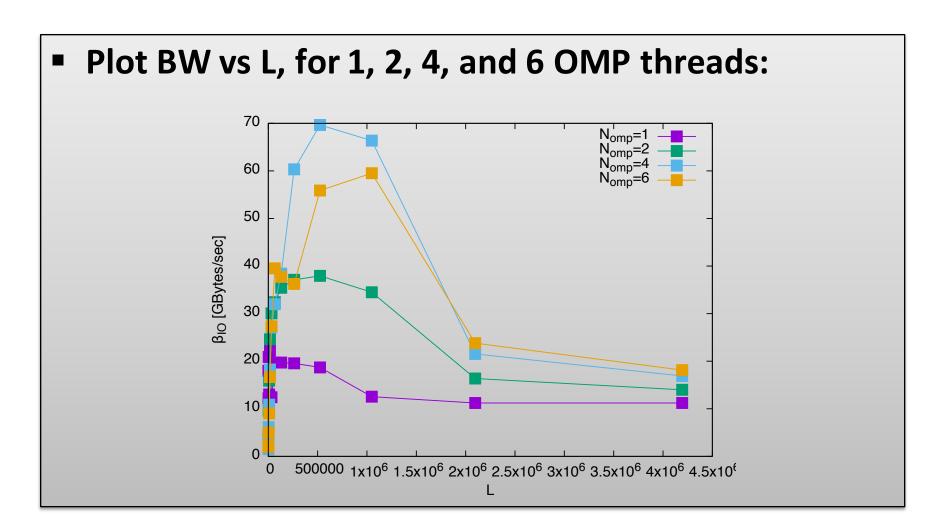




- Add OMP pragmas to parallelize the i-loop
- Use:

```
- #pragma omp parallel
{
    ...
}
- #pragma omp for
```

Which variables need to be made local (private)?



Ex2

Array of double NxN matrices times constant NxN matrix,

mxam:

```
for(i=0; i<L; i++)
  for(a=0; a<N; a++)
    for(b=0; b<N; b++)
    for(c=0; c<N; c++)
    y[i][a][b] += A[a][c]*x[i][c][b]</pre>
```

- Purpose is **not** to optimise this kernel
- This kernel will allow us to explore the transition between computational-bound and bandwidth-bound kernels (how?)

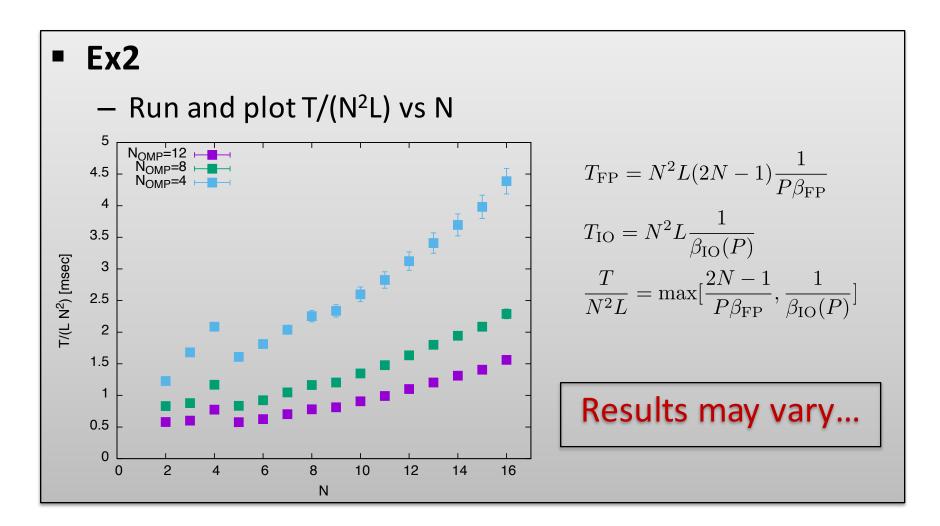
Ex2

Array of double NxN matrices times constant NxN matrix,

mxam:

```
for(i=0; i<L; i++)
  for(a=0; a<N; a++)
   for(b=0; b<N; b++)
    for(c=0; c<N; c++)
    y[i][a][b] += A[a][c]*x[i][c][b]</pre>
```

- As in Ex1, modify program to report
 - Time per kernel call (μsec)
 - Floating point rate (Gflop/sec)
 - I/O rate (Gbytes/sec)
- Run and plot: T/(N**2*L) vs N

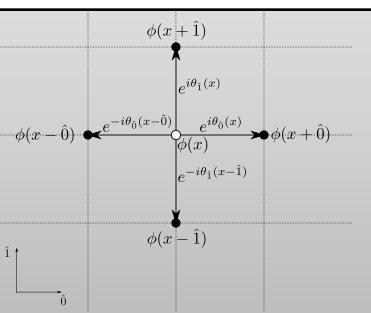


Ex3

- Laplacian stencil, with U(1) coupling

$$\phi(x) \leftarrow 4\phi(x) - \sum_{\mu} [\phi(x+\hat{\mu})u_{\hat{\mu}}(x) + \phi(x-\hat{\mu})u_{\hat{\mu}}^*(x-\hat{\mu})]$$

$$\phi(x) \in \mathbb{C}$$
 $u_{\hat{\mu}}(x) \in U(1)$
 $2D \text{ case } \Rightarrow \mu = 0, 1$



Ex3a

```
for(int y=0; y<L; y++)
  for(int x=0; x<L; x++) {</pre>
    int v00 = y*L + x;
    int v0p = y*L + (x+1)%L;
    int vp0 = ((y+1)%L)*L + x;
    int v0m = y*L + (L+x-1)%L;
    int vm0 = ((L+y-1)%L)*L + x;
    _Complex float p;
    p = phi_in[v0p]*u[UIDX(v00, 0)];
    p += phi_in[vp0]*u[UIDX(v00, 1)];
    p += phi_i[v0m]*conj(u[UIDX(v0m, 0)]);
    p += phi_in[vm0]*conj(u[UIDX(vm0, 1)]);
    phi_out[v00] = 4*phi_in[v00] - p;
```

Ex3b

- Re-order data into a vectorisable form
- Need 4 consecutive elements on which the same operation will be carried out

