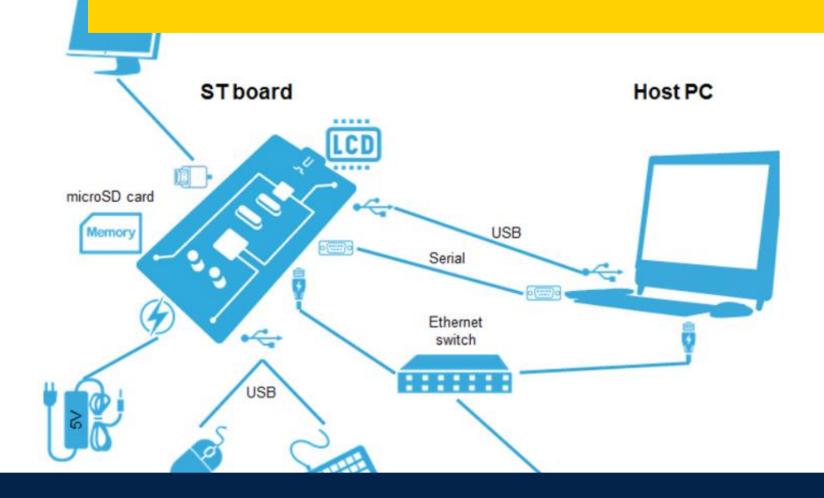
OpenSTLinux Developer Package

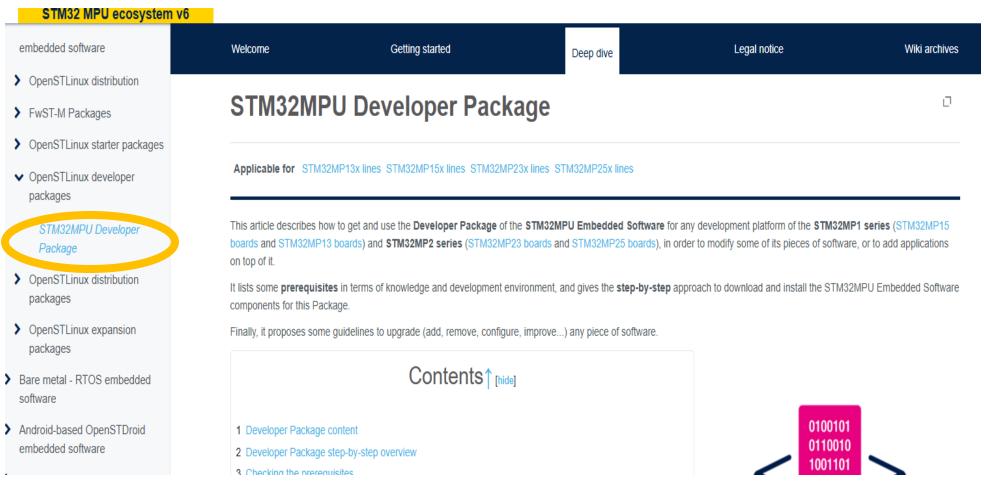




WIKI PAGE for OpenSTLinux Developer Package

Refer to:

https://wiki.st.com/stm32mpu/wiki/STM32MPU_Developer_Package

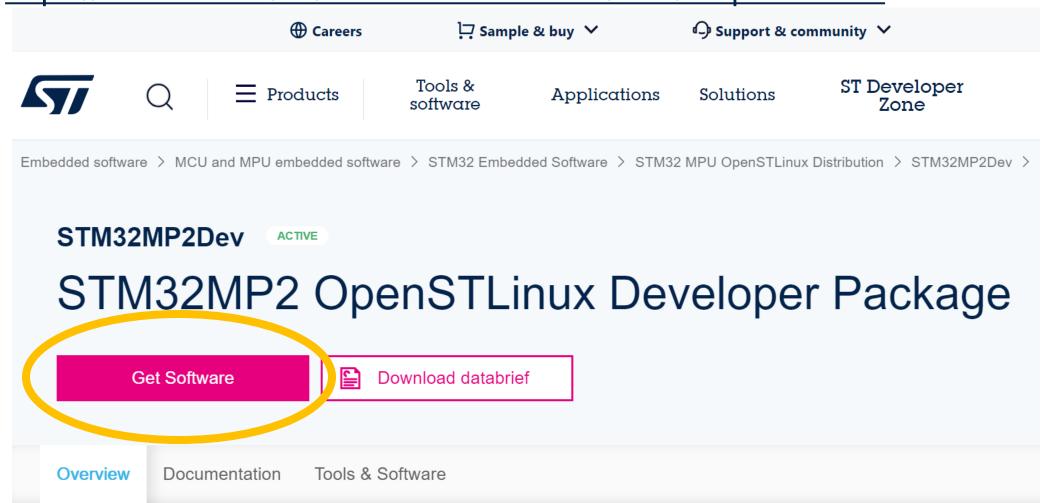




OpenSTLinux Developer Package - DOWNLOAD

Refer to:

https://www.st.com/en/embedded-software/stm32mp2dev.html



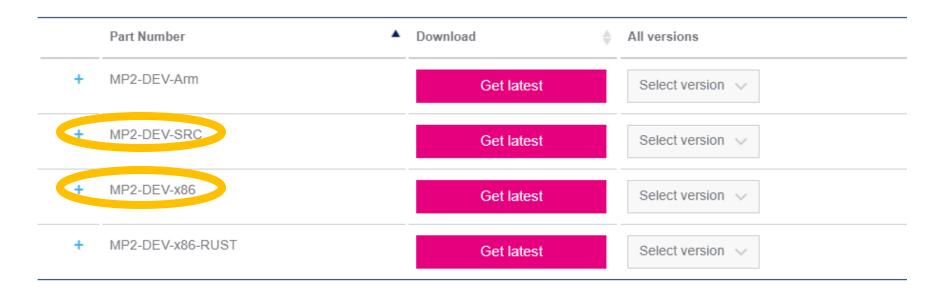


OpenSTLinux Developer Package - DOWNLOAD

Refer to:

https://www.st.com/en/embedded-software/stm32mp2dev.html

Get Software



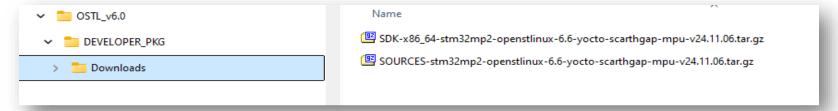


SDK ENVIRONMENT SETUP

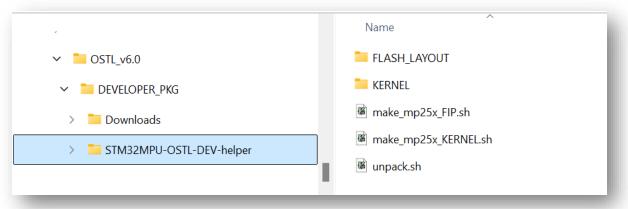


Materials:

1. Toolchain and SOURCES:



- 2. STM32MPU-OSTL-DEV-helper:
 - From: https://github.com/stm32-hotspot/STM32MPU-OSTL-DEV-helper





Tree operations:

- 1. Unpack the 2 tar.gz archives
- 2. Install the cross compiler toolchain
- 3. Extract and patch the lowlevel firmware sources









1. Unpack tar.gz archives

```
$ cd ~/OSTL_v6.0/DEVELOPER_PKG/Downloads/
$ tar xzf en.SDK-x86_64-stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06.tar.gz &
$ tar xzf en.SOURCES-stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06.tar.gz -C ../ &
$ terminator &
```

```
CTOCWL00617:~/OSTL_v6.0/DEVELOPER_PKG/Downloads
  paga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/Downloads]
 ls -1h
total 1.3G
-rwxr-xr-x 1 gpaga gpaga 900M Nov 25 14:33 SDK-x86 64-stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06.tar.gz
rwxr-xr-x 1 gpaga gpaga 430M Nov 25 14:33 SOURCES-stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06.tar.gz
 gpaga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/Downloads]
 tar xzf SDK-x86 64-stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06.tar.gz &
[1] 1431119
 gpaga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/Downloads]
 tar xzf SOURCES-stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06.tar.gz -C ../ &
[2] 1431139
   Done
                              tar xzf SDK-x86 64-stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06.tar.gz
gpaga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/Downloads]
 terminator &
[3] 1431159
 gpaga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/Downloads]
                              tar xzf SOURCES-stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06.tar.gz -C ../
 gpaga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/Downloads]
```



2. Install the cross compiler toolchain

- \$ cd stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sdk/
- \$ sudo ./st-image-weston-openstlinux-weston-stm32mp2.rootfs-x86_64-toolchain-5.0.3-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06.sh

```
↑ CTOCWL00617:~/OSTL_v6.0/DEVELOPER_PKG/Downloads/stm32mp2-openstlinux-6.6-yocto-scarthqap-mpu-v24.11.06/sdk

  gpaga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/Downloads]
 cd stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sdk/
 gpaga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/Downloads/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sdk]
 sudo ./st-image-weston-openstlinux-weston-stm32mp2.rootfs-x86 64-toolchain-5.0.3-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06.sh
ST OpenSTLinux - Weston - (A Yocto Project Based Distro) SDK installer version 5.0.3-openstlinux-6.6-vocto-scarthgap-mpu-v24.11.06
Enter target directory for SDK (default: /opt/st/stm32mp2/5.0.3-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06):
The directory "/opt/st/stm32mp2/5.0.3-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06" already contains a SDK for this architecture.
If you continue, existing files will be overwritten! Proceed [y/N]? Y
Setting it up...done
SDK has been successfully set up and is ready to be used.
Each time you wish to use the SDK in a new shell session, you need to source the environment setup script e.g.
$ . /opt/st/stm32mp2/5.0.3-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/environment-setup-cortexa35-ostl-linux
gpaga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/Downloads/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sdk
```



3. Extract and patch the lowlevel firmware sources

- \$ cd ../stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch64-ostl-linux/
- \$ cp -r ../../STM32MPU-OSTL-DEV-helper/ .
- \$./STM32MPU-OSTL-DEV-helper/unpack.sh

```
↑ CTOCWL00617:~/OSTL_v6.0/DEVELOPER_PKG/stm32mp2-openstlinux-6.6-yocto-scarthqap-mpu-v24.11.06/sources/aarch64-ostl-linux

↑ CTOCWL00617:~/OSTL_v6.0/DEVELOPER_PKG/stm32mp2-openstlinux-6.6-yocto-scarthqap-mpu-v24.11.06/sources/aarch64-ostl-linux

↑ CTOCWL00617:~/OSTL_v6.0/DEVELOPER_PKG/stm32mp2-openstlinux-6.6-yocto-scarthqap-mpu-v24.11.06/sources/aarch64-ostl-linux

↑ CTOCWL00617:~/OSTL_v6.0/DEVELOPER_PKG/stm32mp2-openstlinux-6.6-yocto-scarthqap-mpu-v24.11.06/sources/aarch64-ostl-linux

↑ CTOCWL00617:~/OSTL_v6.0/DEVELOPER_PKG/stm32mp2-openstlinux-6.6-yocto-scarthqap-mpu-v24.11.06/sources/aarch64-ostl-linux

↑ CTOCWL00617:~/OSTL_v6.0/DEVELOPER_PKG/stm32mp2-openstlinux-6.6-yocto-scarthqap-mpu-v24.11.06/sources/aarch64-ostl-linux

↑ CTOCWL00617:~/OSTL_v6.0/DEVELOPER_PKG/stm32mp2-openstlinux

↑
                                                                                                                                                                                                                                                                                                            @CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/Downloads/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sdk]
  cd ../../stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch64-ostl-linux/
  gpaga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-vocto-scarthgap-mpu-v24.11.06/sources/aarch64-ostl-linux]
    ./STM32MPU-OSTL-DEV-helper/unpack.sh
Processing folder: external-dt-6.0
          Unpacking ... Patching ... Done.
                                                                                                                --> Operation completed successfully.
Processing folder: gcnano-driver-stm32mp-6.4.19-stm32mp2-r1-rc8
          Unpacking ... Patching ... Done.
                                                                                                                 --> Operation completed successfully.
Processing folder: linux-stm32mp-6.6.48-stm32mp-r1
          Unpacking ... Patching .... Done.
                                                                                                                 --> Operation completed successfully.
Processing folder: optee-os-stm32mp-4.0.0-stm32mp-r1
                                                                                                                 --> Operation completed successfully.
          Unpacking ... Patching .... Done.
Processing folder: tf-a-stm32mp-v2.10.5-stm32mp-r1
          Unpacking ... Patching .... Done.
                                                                                                                 --> Operation completed successfully.
Processing folder: u-boot-stm32mp-v2023.10-stm32mp-r1
          Unpacking ... Patching .... Done.
                                                                                                                 --> Operation completed successfully.
Processing folder: stm32mp-ddr-phy-A2022.11
          Unpacking ... Patching ... Done.
                                                                                                                 --> Operation completed successfully.
  gpaga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.1<u>1.06/sources/aarch64-ostl-linux</u>]
```



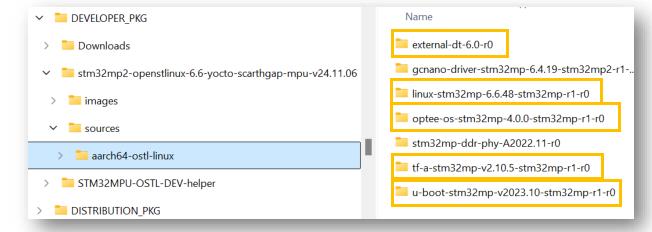
SDK BUILD



OpenSTLinux Developer Package - BUILD

We are now ready to build the low level components of our BSP:

- 1. TF-A + devicetree
- 2. OP-TEE + devicetree
- 3. U-BOOT + devicetree
- 4. Linux kernel + devicetree















FIP = Firmware Image Package

OpenSTLinux Developer Package - BUILD

FIP build:

./STM32MPU-OSTL-DEV-helper/make_mp25x_FIP.sh

```
↑ CTOCWL00617:~/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-vocto-scarthgap-mpu-v24.11.06/sources/aarch64-ostl-linux
 paga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch64-ostl-linux
 ./STM32MPU-OSTL-DEV-helper/make mp25x FIP.sh
nake -C /home/gpaga/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch64-ostl-linux/u-boot-
tm32mp-v2023.10-stm32mp-r1-r0/u-boot-stm32mp-v2023.10-stm32mp-r1 0=/home/gpaga/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-yoct
o-scarthgap-mpu-v24.11.06/sources/aarch64-ostl-linux/u-boot-stm32mp-v2023.10-stm32mp-r1-r0/u-boot-stm32mp-v2023.10-stm32mp-r1/../buil
//stm32mp25 defconfig stm32mp25 defconfig || exit 1
nake[1]: Entering directory '/home/gpaga/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch
.4-ostl-linux/u-boot-stm32mp-v2023.10-stm32mp-r1-r0/u-boot-stm32mp-v2023.10-stm32mp-r1
make[2]: Entering directory '/home/gpaga/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-vocto-scarthgap-mpu-v24.11.06/sources/aarch
fip/fip-stm32mp257f-dk-ddr-opteemin-emmc.bin' -> 'BUILD OUTPUT/fip/fip-ddr.bin'
/home/gpaga/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch64-ostl-linux/FIP artifacts/
fip/fip-stm32mp257f-dk-opteemin-emmc.bin' -> 'BUILD OUTPUT/fip/fip.bin'
/home/gpaga/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch64-ostl-linux/FIP artifacts/
arm-trusted-firmware/tf-a-stm32mp257f-dk-opteemin-programmer-usb.stm32' -> 'BUILD_OUTPUT/tfa/tfa_usb.stm32'
STM32MPU-OSTL-DEV-helper/FLASH LAYOUT/flash layout emmc.tsv' -> 'BUILD OUTPUT/FLASH LAYOUT/flash layout emmc.tsv'
STM32MPU-OSTL-DEV-helper/FLASH LAYOUT/flash layout sdcard.tsv' -> 'BUILD OUTPUT/FLASH LAYOUT/flash layout sdcard.tsv'
STM32MPU-OSTL-DEV-helper/FLASH_LAYOUT/flash.bat' -> 'BUILD_OUTPUT/FLASH_LAYOUT/flash.bat'
STM32MPU-OSTL-DEV-helper/FLASH_LAYOUT/flash.sh' -> 'BUILD_OUTPUT/FLASH_LAYOUT/flash.sh'
STM32MPU-OSTL-DEV-helper/FLASH_LAYOUT/metadata.bin' -> 'BUILD_OUTPUT/FLASH_LAYOUT/metadata.bin''
rw-r--r-- 1 gpaga gpaga 13M Nov 27 14:55 /tmp/stm32mp257f-dk_binaries.tar.gr-
  paga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch64-ostl-linux
```



OpenSTLinux Developer Package - BUILD

KERNEL build:

./STM32-OSTL-DEV-helper/make_mp25x_KERNEL.sh

```
Select CTOCWL00617:~/OSTL_v6.0/DEVELOPER_PKG/stm32mp2-openstlinux-6.6-yocto-scarthqap-mpu-v24.11.06/sources/aarch64-ostl-linux
      OCTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch64-ostl-linux
 ./STM32MPU-OSTL-DEV-helper/make_mp25x_KERNEL.sh
make[1]: Entering directory '/home/gpaga/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch
64-ostl-linux/linux-stm32mp-6.6.48-stm32mp-r1-r0/build'
         Makefile
*** Default configuration is based on 'defconfig'
 No change to .config
make[1]: Leaving directory '/home/gpaga/OSTL_v6.0/DEVELOPER_PKG/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch6
4-ostl-linux/linux-stm32mp-6.6.48-stm32mp-r1-r0/build'
Using ../build//.config as base
64-ostl-linux/linux-stm32mp-6.6.48-stm32mp-r1-r0/build'
 INSTALL ../../BUILD OUTPUT/kernel//lib/modules/6.6.48/modules.order
 INSTALL ../../BUILD OUTPUT/kernel//lib/modules/6.6.48/modules.builtin
 INSTALL ../../BUILD OUTPUT/kernel//lib/modules/6.6.48/modules.builtin.modinfo
 SYMLINK ../../BUILD OUTPUT/kernel//lib/modules/6.6.48/build
 DEPMOD ../../BUILD OUTPUT/kernel//lib/modules/6.6.48
make[1]: Leaving directory '/home/ 1/0STL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch6
4-ostl-linux/linux-stm32mp-6.6.48-stm32mp-r1-r0/build'
../build/arch/arm64/boot/Image.gz' -> '../../BUILD_OUTPUT/kernel/Image.gz'
../build/arch/arm64/boot/dts/st/stm32mp257f-dk.dtb' -> '../../BUILD OUTPUT/kernel/stm32mp257f-dk.dtb'
 gpaga@CTOCWL00617 ~/OSTL v6.0/DEVELOPER PKG/stm32mp2-openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06/sources/aarch64-ostl-linux]
```



OpenSTLinux Developer Package - BUILD OUTPUT

- BUILD OUTPUT/tfa/tfa_usb.stm32
- BUILD OUTPUT/tfa/tfa_emmc.stm32
- BUILD_OUTPUT/fip/fip.bin
- BUILD_OUTPUT/fip/fip-ddr.bin
- BUILD_OUTPUT/fip/fip_usb.bin
- BUILD_OUTPUT/kernel/Image.gz
- BUILD_OUTPUT/kernel/stm32mp257f-dk.dtb
- BUILD_OUTPUT/kernel/lib/modules/

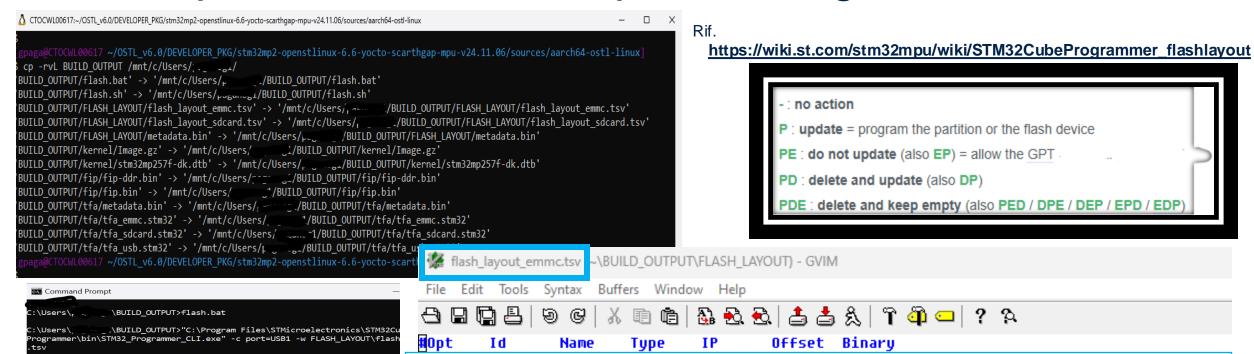
```
Opt
                                    Part
                                                  Name
                                                                           Type
                                                                                                         Device
                                                                                                                          Offset
                                                                                                                                                     Binary
                                                                                                                                                     tf-a-usb.stm32
                                    0x01
                                                  fsbl-boot
                                                                           Binary
                                                                                                                          0x0
                                                                                                        none
FLASH
                                    0x02
                                                  fip-ddr
                                                                           FIP
                                                                                                                                                     fip-ddr.bin
                                                                                                        none
                                                                                                                          0x0
HELPERS
                                    0x03
                                                  fip-boot
                                                                           FIP
                                                                                                                          0x0
                                                                                                                                                     fip.bin
                                                                                                        none
                                    0x04
                                                  fsbla1
                                                                           Binary
                                                                                                        mmc1
                                                                                                                          boot1
                                                                                                                                                     tf-a-emmc.stm32
                                    0x05
                                                  fsbla2
                                                                                                                          boot2
                                                                                                                                                     tf-a-emmc.stm32
                                                                           Binary
                                                                                                        mmc1
                                                                           FWU MDATA
                                    0x06
                                                  metadata1
                                                                                                                          0x00080000
                                                                                                                                                     metadata.bin
                                                                                                        mmc1
                                    0x07
                                                  metadata2
                                                                           FWU MDATA
                                                                                                                          0x00100000
                                                                                                                                                     metadata.bin
                                                                                                        mmc1
                                    0x08
                                                                           FIP
                                                                                                                          0x00180000
                                                                                                                                                     fip.bin
                                                  fip-a
                                                                                                        mmc1
```

00617 ~/OSTL_v6.0/DEVELOPER_PKG/stm32mp2-openstlinux-6.6-yocto-scarthga

BOARD UPDATE



OpenSTLinux Developer Package – FLUSH OPT1

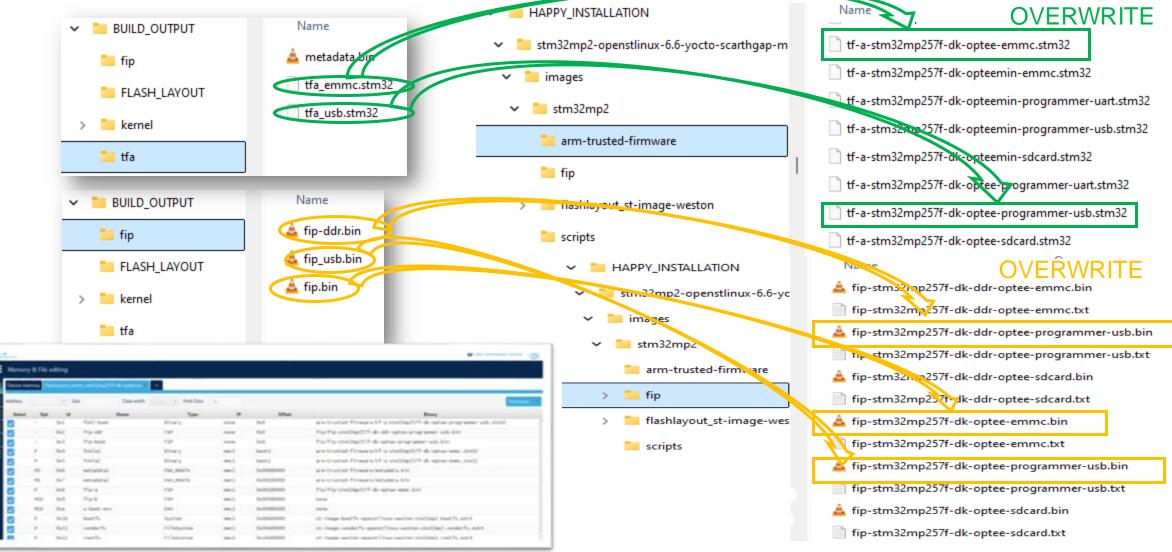


C:\Users	_`\BUILD_OUTPUT>flash.bat
	\BUILD_OUTPUT>"C:\Program Files\STMicroelectronics\STM32Cu in\STM32_Programmer_CLI.exe" -c port=USB1 -w FLASH_LAYOUT\flash
	STM32CubeProgrammer v2.17.0
Manuf. ID Product ID	: High Speed (480MBit/s) : STMicroelectronics : DFU in HS Mode @Device ID /0x505, @Revision ID /0x2000 : 002F002C4136500800373653 : 1.1
Download in	100%
	during download operation: 00:00:00.900
	ARITID CUTPUTATION "C.) Program Files) STM: angelestropies) S

DFU protocol: 1.1	
Download in Progress:	100%
ile download complete Time elapsed during download operation: 00:00:00.9	99
RUNNING Program PartID: :0x08 Start operation done successfully at partition 0x0 -lashing service completed successfully	8
C:\Users\p \BUILD_OUTPUT>rem "C:\Program Fil	es\STMicroelectronics\S

스 및 및 실 ७ © X ® @ \$\ & & \ \ \ & \ & \ ♣ & \ X ↑ ♠ □ ? %								
#Opt	Id	Name Type	IP Offset	Binary				
_	0x 01	fsb1-boot	Binary	none	0 x 0	tfa/tfa_usb.stm32		
-	0x 02	fip-ddr	FIP	none	0 x 0	fip/fip-ddr.bin		
_	0x 03	fio-boot	FIP	none	0 x 0	fin/fin.bin		
Р	0x 04	fsbla1	Binary	Amc 1	boot1	tfa/tfa_emmc.stm32		
P	0x 05	fsb1a2	Binary	mmc1	boot2	tfa/tfa_emmc.stm32		
PE	0x 06	metadata1	FWU_MDATA	mmc1	0x 0008 0000	tfa/metadata.bin		
PE	0x 07	metadata2	FWU_MDATA	mmc1	0x 001 00000	tfa/metadata.bin		
P	0x 08	fip-a	FIP	mmc1	0x 0018 0000	fip/fip.bin		
PED	0x 09	fip-b	FIP	mmc1	0x 0058 0000	fip/fip.bin		
PED	0x 0A	u-boot-env	ENV	mmc1	0x 0098 0000	none		
PE	0x10	bootfs	System	mmc1	0x 00A 00000	none		
PE	0x11	vendorfs	FileSystem	mmc1	0x 04A 00000	none		
PE	0x12	rootfs	FileSystem	mmc1	0x14400000	none		
PE	0x13	userfs	FileSystem	mmc1	0xD4400000	none		

OpenSTLinux Developer Package - FLUSH OPT2





STM32MP2x power and flexibility 2/2





OpenSTLinux Distribution Package













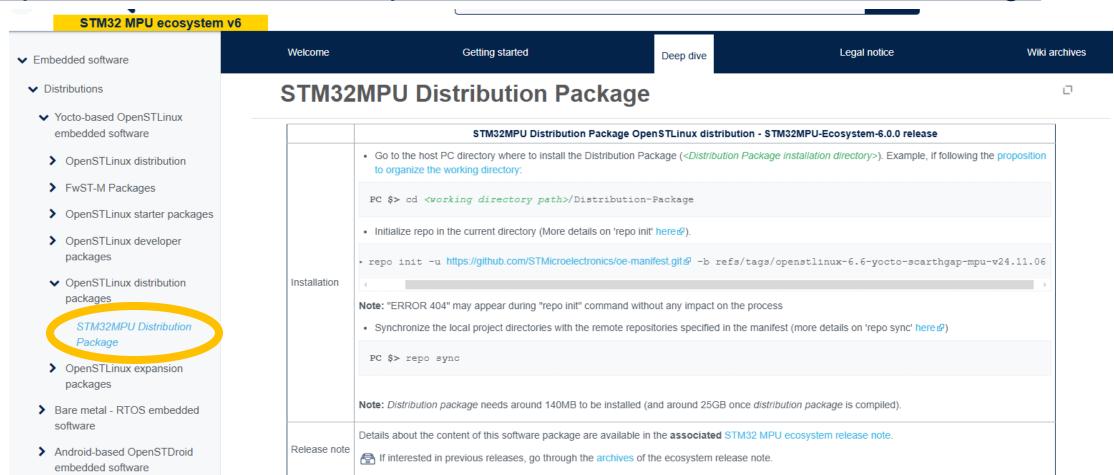




WIKI PAGE for OpenSTLinux Distribution Package

Refer to:

https://wiki.st.com/stm32mpu/wiki/STM32MPU_Distribution_Package





OpenSTLinux Distribution Package - SETUP

- Setup commands:

 - \$ cd ~/OSTL_v6.0/DISTRIBUTION_PKG
 - \$ repo init -u https://github.com/STMicroelectronics/oe-manifest.git -b refs/tags/openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06
 - \$ repo sync -j8

```
∆ CTOCWL00617:(DISTRIBUTION_PKG_DEMO) ~/OSTL_v6.0/DISTRIBUTION_PKG_DEMO

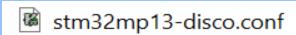
                                                                                                                              - - ×
    OSTL_v6.0/DISTRIBUTION_PKG_DEMO/
          CWL00617 ~/OSTL_v6.0/DISTRIBUTION_PKG_DEMO]
 repo init -u https://github.com/STMicroelectronics/oe-manifest.git -b refs/tags/openstlinux-6.6-yocto-scarthgap-mpu-v24.11.06
Oownloading Repo source from https://gerrit.googlesource.com/git-repo
   You should upgrade soon:
   cp /home/gpaga/OSTL_v6.0/DISTRIBUTION_PKG_DEMO/.repo/repo/repo /usr/bin/repo
Your identity is: gpaga <giuseppe.pagano@st.com>
If you want to change this, please re-run 'repo init' with --config-name
repo has been initialized in /home/gpaga/OSTL_v6.0/DISTRIBUTION_PKG_DEMO
  gpaga@CTOCWL00617 ~/OSTL_v6.0/DISTRIBUTION_PKG_DEMO`
 repo sync -j8
   A new version of repo (2.48) is available.
   cp /home/gpaga/OSTL v6.0/DISTRIBUTION PKG DEMO/.repo/repo/repo /usr/bin/repo
etching: 100% (7/7), done in 28.224s
Checking out: 85% (6/7), done in 0.580s
Checking out: 14% (1/7), done in 0.104s
repo sync has finished successfully.
       @CTOCWL00617 ~/OSTL v6.0/DISTRIBUTION PKG DEMO]
```



OpenSTLinux Distribution Package – OpenSTLinux

Brief Yocto ABC:

- 3 main definitions/variables:
 - DISTRO [openstlinux-weston]
 - MACHINE [MyBoard]
 - IMAGE [Mylmage]
- 2 concepts
 - Overlay
 - Recipe
- 1 tool:
 - bitbake





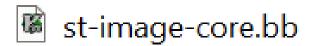
stm32mp15-eval.conf

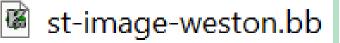
stm32mp21-disco.conf

stm32mp23-disco.conf

stm32mp25-disco.conf

stm32mp25-eval.conf







OpenSTLinux Distribution Package – OpenSTLinux

Rif: https://wiki.st.com/stm32mpu/wiki/STM32MPU_Distribution_Package

Yocto ABC:

- DISTRO [openstlinux-weston]
- MACHINE [MyBoard]
- IMAGE [Mylmage]

5.1. Initializing the OpenEmbedded build environment↑

The OpenEmbe Ided environment setup script must be run once in each new working terminal in which you use the BitBake or devtool tools (see later):

DISTRO=openstlinux-weston MACHINE= <machine> source layers/meta-st/scripts/envsetup.sh

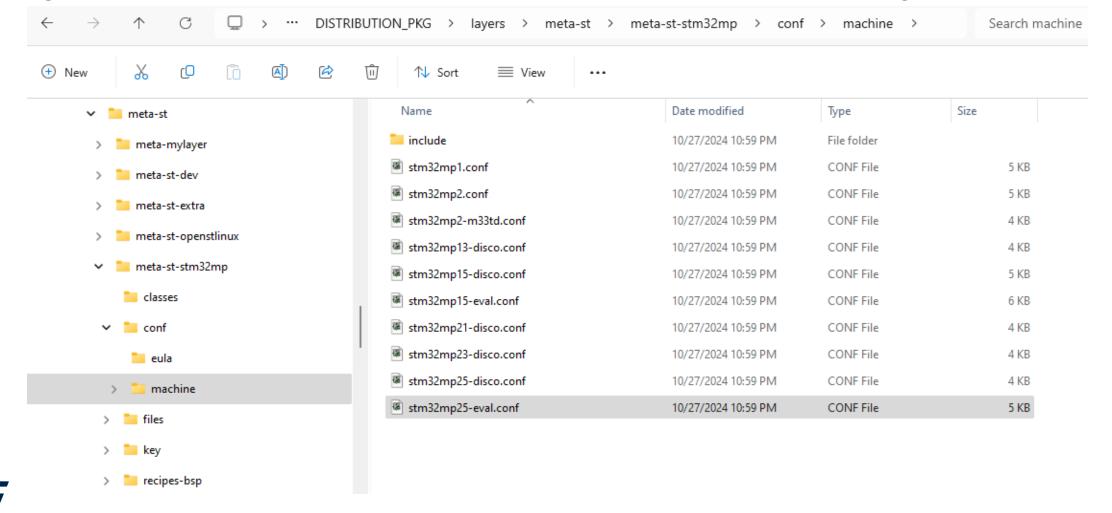
The bitbake <image> command is used to build the image. <image> specifies the targeted image, st-image-weston here (Weston image) support).

PC \$> bitbake st-image-weston



OpenSTLinux Distribution Package – MACHINE defs

https://github.com/STMicroelectronics/meta-st-stm32mp/tree/scarthgap/conf/machine



OpenSTLinux Distribution Package – MACHINE defs

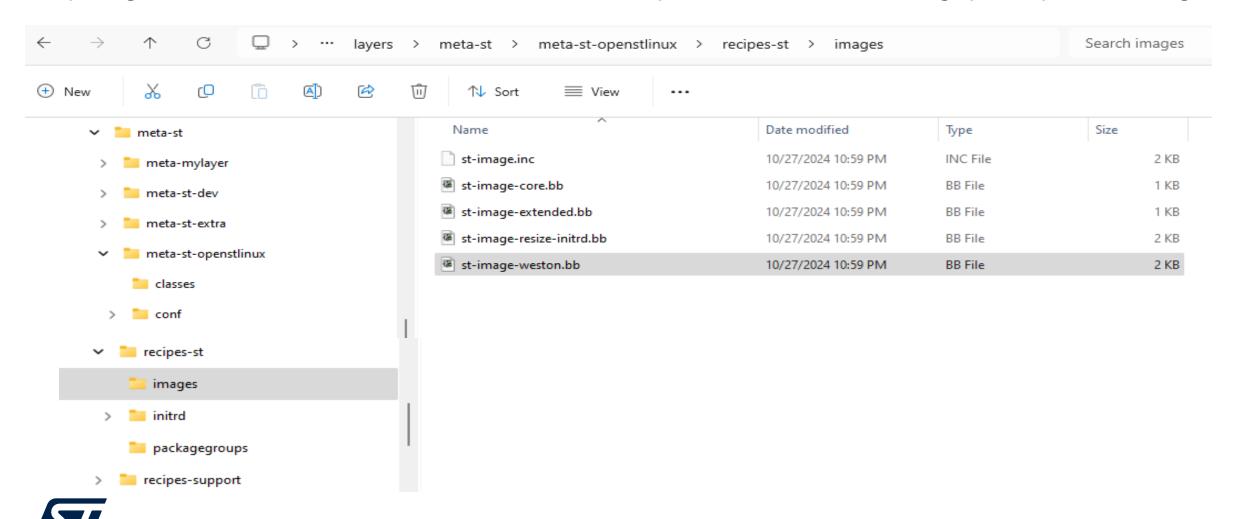
https://github.com/STMicroelectronics/meta-st-stm32mp/blob/scathgap/conf/machine/stm32mp25-eval.conf

```
# Chip architecture
DEFAULTTUNE = "cortexa35"
include conf/machine/include/arm/armv8a/tune-cortexa35.inc
# boot scheme
BOOTSCHEME LABELS = "optee"
# boot device
# Define the boot device supported
BOOTDEVICE LABELS += "emmc"
BOOTDEVICE LABELS += "nor-sdcard"
BOOTDEVICE LABELS += "sdcard"
# Machine settings
# =================
# activate external dt
EXTERNAL DT ENABLED = "11"
# Define list of devicetree per supported storage
STM32MP_DT_FILES_EMMC += "stm32mp257f-ev1"
STM32MP DT FILES SDCARD += "stm32mp257f-ev1"
                        += "stm32mp257f-ev1"
STM32MP DT FILES NOR
EXTERNAL DEVICETREE SDCARD = "stm32mp257f-ev1-ca35tdcid-ost1"
EXTERNAL DEVICETREE SDCARD =+ "stm32mp257f-ev1-ca35tdcid-ost1-m33-examples"
EXTERNAL DEVICETREE EMMC = "stm32mp257f-ev1-ca35tdcid-ost1"
```



OpenSTLinux Distribution Package – IMAGE defs

https://github.com/STMicroelectronics/meta-st-openstlinux/tree/scarthgap/recipes-st/images



OpenSTLinux Distribution Package – IMAGE defs

https://github.com/STMicroelectronics/meta-st-openstlinux/blob/scarthgap/recipes-st/images/st-image-weston.bb

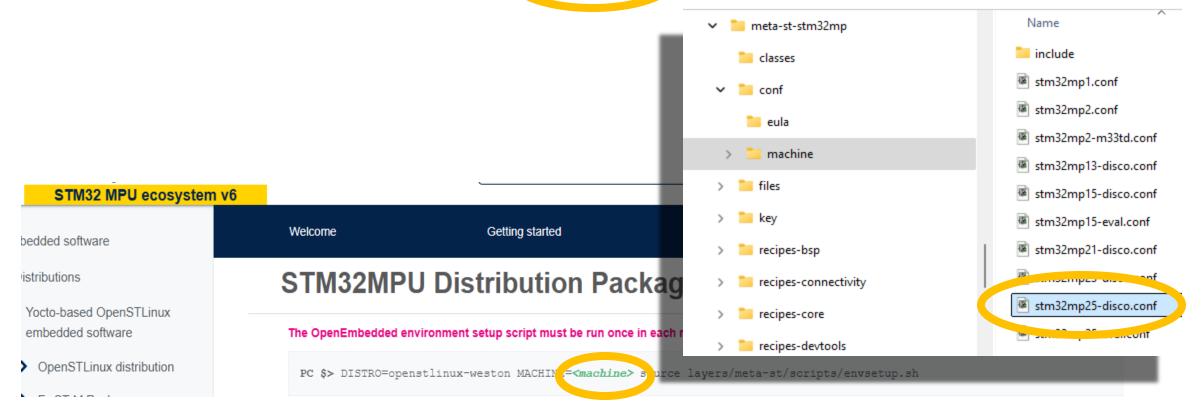
```
SUMMARY = "OpenSTLinux weston image with basic Wayland support (if enable in distro)."
LICENSE = "Proprietary"
include recipes-st/images/st-image.inc
inherit core-image features check
# let's make sure we have a qood imaqe...
REQUIRED DISTRO FEATURES = "wayland"
IMAGE LINGUAS = "en-us"
IMAGE FEATURES += "\
    splash
    package-management
    ssh-server-dropbear \
    hwcodecs
    tools-profile
    eclipse-debug
# INSTALL addons
CORE IMAGE EXTRA INSTALL += " \
    resize-helper \
    st-hostname \
    packagegroup-framework-core-base
    packagegroup-framework-tools-base
                                        `
    packagegroup-framework-core
                                        `
    packagegroup-framework-tools
                                        `
    packagegroup-framework-core-extra
    ${@bb.utils.contains('COMBINED_FEATURES', 'optee', 'packagegroup-optee-core', '', d)} \
    ${@bb.utils.contains('COMBINED FEATURES', 'optee', 'packagegroup-optee-test', '', d)} \
```



OpenSTLinux Distribution Package – ENV SETUP

Rif: https://wiki.st.com/stm32mpu/wiki/STM32MPU_Distribution_Package

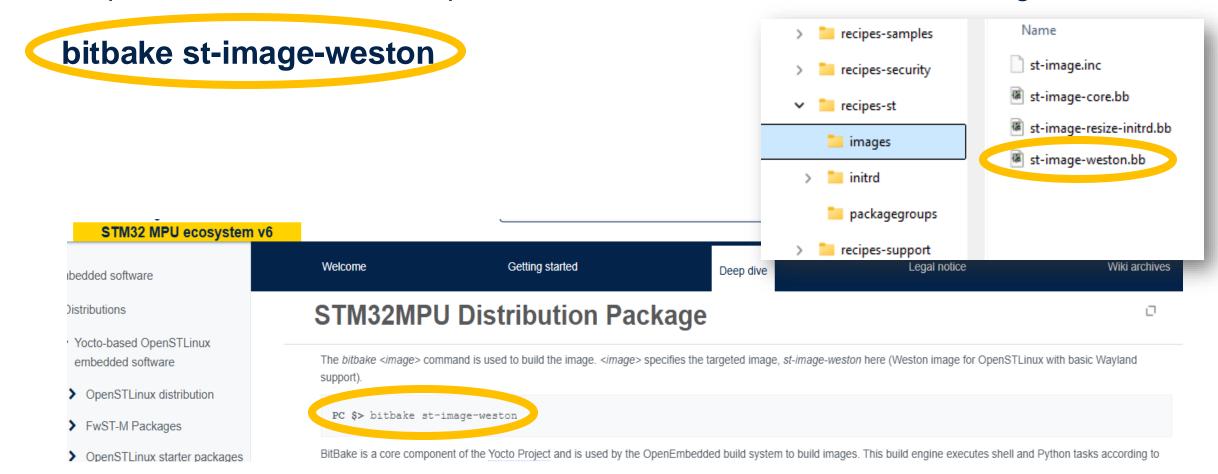
DISTRO=openstlinux-weston MACHINE=<machine>source layers/meta-st/scripts/envsetup.sh





OpenSTLinux Distribution Package – BUILD image

Rif: https://wiki.st.com/stm32mpu/wiki/STM32MPU_Distribution_Package





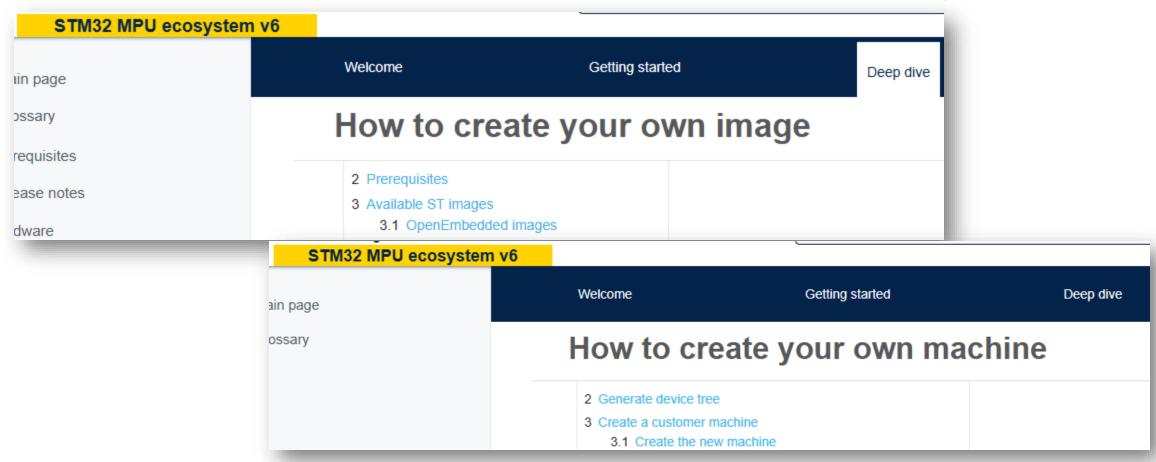
CUSTOMIZATION of OpenSTLinux Distribution Package





OpenSTLinux Distribution Package – CUSTOMIZATION

https://wiki.st.com/stm32mpu/wiki/How_to_create_your_own_image



https://wiki.st.com/stm32mpu/wiki/How to create your own machine



OSTL Distribution Package – CUSTOMIZATION

- \$ DISTRO=openstlinux-weston MACHINE=stm32mp25-myboard \
 source layers/meta-st/scripts/envsetup.sh
- \$ bitbake-layers -h
- \$ bitbake-layers create-layer ../layers/meta-st/meta-my-new-layer/
- \$ mkdir -p ../layers/meta-st/meta-my-new-layer/conf/machine/
- \$ cp -v ../layers/meta-st/meta-st-stm32mp/conf/machine/stm32mp25-disco.conf \ ../layers/meta-st/meta-my-new-layer/conf/machine/stm32mp25-myboard.conf
- \$ mkdir -p ../layers/meta-st/meta-my-new-layer/recipes-st/images/
- \$ cp -v ../layers/meta-st/meta-st-openstlinux/recipes-st/images/st-image-core.bb \ ../layers/meta-st/meta-my-new-layer/recipes-st/images/myimage.bb
- \$ bitbake-layers add-layer ../layers/meta-st/meta-my-new-layer/
- \$ bitbake myimage



OpenSTLinux Distribution Package – ENV SETUP

\$ DISTRO=openstlinux-weston MACHINE=stm32mp25-myboard source layers/meta-st/scripts/envsetup.sh

DISTRO=openstlinux-weston MACHINE=stm32mp25-myboard source layers/meta-st/scripts/envsetup.sh [HOST DISTRIB check] inux Distrib: Ubuntu Linux Release: 22.04 Required packages for Linux Distrib: bsdmainutils build-essential chrpath cpio debianutils diffstat gawk gcc-multilib git git-lfs iputils-ping libegl1-mesa ect python3-pip socat texinfo unzip wget xterm xz-utils zstd Check OK: all required packages are installed on host. [source layers/openembedded-core/oe-init-build-env][with previous config] Configuration files have been created for the following configuration: : openstlinux-weston DISTRO DISTRO_CODENAME : scarthgap stm32mp25-myboard BB NUMBER THREADS : PARALLEL MAKE : -j 8 BUILDDIR : build-openstlinuxweston-stm32mp25-myboard DOWNLOAD DIR : /home/gpaga/Public/oe-downloads SSTATE_DIR /home/gpaga/OSTL_v6.0/DISTRIBUTION_PKG/sstate-cache SOURCE MIRROR URL: http://freenas.gnb.st.com/pub/yocto/stm-opensdk/scarthgap/downloads SSTATE MIRRORS <disable> WITH_EULA_ACCEPTED: YES Available images for OpenSTLinux layers are: Official OpenSTLinux images: OpenSTLinux weston image with basic Wayland support (if enable in distro) st-image-weston Other OpenSTLinux images: Supported images: st-image-core OpenSTLinux core image You can now run 'bitbake <image>' gpaga@HOSTPC ~/OSTL_v6.0/DISTRIBUTION_PKG/build-openstlinuxweston-stm32mp25-myboard]



OSTL Distribution Package – CREATE NEW LAYER

\$ bitbake-layers -h

```
$ bitbake-layers -h
NOTE: Starting bitbake server...
NOTE: Started PRServer with DBfile: /home/gpaga/OSTL v6.0/DISTRIBUTION PKG/build-openstlinuxweston
usage: bitbake-layers [-d] [-q] [-F] [--color COLOR] [-h] <subcommand> ...
BitBake layers utility
options:
  -d, --debug
                        Enable debug output
  -q, --quiet
                        Print only errors
                        Force add without recipe parse verification
  -F, --force
  --color COLOR
                        Colorize output (where COLOR is auto, always, never)
  -h, --help
                        show this help message and exit
subcommands:
  <subcommand>
                        Add one or more layers to bblayers.conf.
    add-layer
                        Remove one or more layers from bblayers.conf.
    remove-laver
                        flatten layer configuration into a separate output directory.
    flatten
                        show current configured layers.
    show-layers
                        list overlayed recipes (where the same recipe exists in another layer)
    show-overlayed
                        list available recipes, showing the layer they are provided by
    show-recipes
                        list bbappend files and recipe files they apply to
    show-appends
    show-cross-depends
                        Show dependencies between recipes that cross layer boundaries.
    laverindex-fetch
                        Fetches a layer from a layer index along with its dependent layers, and ad
    layerindex-show-depends
                        Find layer dependencies from layer index.
                        Create a basic layer
    create-laver
    create-layers-setup
                        Writes out a configuration file and/or a script that replicate the director
    save-build-conf
                        Save the currently active build configuration (conf/local.conf, conf/bblay
Use bitbake-layers <subcommand> --help to get help on a specific command
*[gpaga@HOSTPC ~/OSTL_v6.0/DISTRIBUTION_PKG/build-openstlinuxweston-stm32mp25-myboard]
```



OSTL Distribution Package – CREATE NEW LAYER

\$ bitbake-layers create-layer ../layers/meta-st/meta-my-new-layer/

```
$ bitbake-layers create-layer ../layers/meta-st/meta-my-new-layer
NOTE: Starting bitbake server...
NOTE: Started PRServer with DBfile: /home/gpaga/OSTL_v6.0/DISTRIBUTION_PKG/build-openstl:
Add your new layer with 'bitbake-layers add-layer ../layers/meta-st/meta-my-new-layer'
*[gpaga@HOSTPC ~/OSTL_v6.0/DISTRIBUTION_PKG/build-openstlinuxweston-stm32mp25-myboard]
$
```



OSTL Distribution Package – **POPULATE NEW LAYER**

- \$ mkdir -p ../layers/meta-st/meta-my-new-layer/conf/machine/ \$ cp -v ../layers/meta-st/meta-st-stm32mp/conf/machine/stm32mp25-disco.conf \ ../layers/meta-st/meta-my-new-layer/conf/machine/stm32mp25-myboard.conf
- \$ mkdir -p ../layers/meta-st/meta-my-new-layer/recipes-st/images/ \$ cp -v ../layers/meta-st/meta-st-openstlinux/recipes-st/images/st-image-core.bb \ ../layers/meta-st/meta-my-new-layer/recipes-st/images/myimage.bb

```
*[gpaga@CTOCWL00617 ~/OSTL_v6.0/DISTRIBUTION_PKG_DEMO/build-openstlinuxweston-stm32mp25-myboard]
$ mkdir -p ../layers/meta-st/meta-my-new-meta-layer/conf/machine/
*[gpaga@CTOCWL00617 ~/OSTL_v6.0/DISTRIBUTION_PKG_DEMO/build-openstlinuxweston-stm32mp25-myboard]
$ cp -v ../layers/meta-st/meta-st-stm32mp/conf/machine/stm32mp25-disco.conf ../layers/meta-st/meta-my-new-meta-layer/conf/machine/stm32mp25-myboard.conf
'../layers/meta-st/meta-st-stm32mp/conf/machine/stm32mp25-disco.conf' -> '../layers/meta-st/meta-my-new-meta-layer/conf/machine/stm32mp25-myboard.conf'
*[gpaga@CTOCWL00617 ~/OSTL_v6.0/DISTRIBUTION_PKG_DEMO/build-openstlinuxweston-stm32mp25-myboard]
$ mkdir -p ../layers/meta-st/meta-st/meta-my-new-layer/recipes-st/images/
*[gpaga@CTOCWL00617 ~/OSTL_v6.0/DISTRIBUTION_PKG_DEMO/build-openstlinuxweston-stm32mp25-myboard]
$ cp -v ../layers/meta-st/meta-st-openstlinux/recipes-st/images/st-image-weston.bb ../layers/meta-st/meta-my-new-layer/recipes-st/images/myimage.bb
'../layers/meta-st/meta-st-openstlinux/recipes-st/images/st-image-weston.bb' -> '../layers/meta-st/meta-my-new-layer/recipes-st/images/myimage.bb'
*[gpaga@CTOCWL00617 ~/OSTL_v6.0/DISTRIBUTION_PKG_DEMO/build-openstlinuxweston-stm32mp25-myboard]
$
```



OSTL Distribution Package – ADD NEW LAYER

\$ bitbake-layers add-layer ../layers/meta-st/meta-my-new-layer/

```
$ bitbake-layers add-layer ../layers/meta-st/meta-my-new-layer
NOTE: Starting bitbake server...
NOTE: Started PRServer with DBfile: /home/gpaga/OSTL_v6.0/DISTRIBUTION_PKG/build-openst
*[gpaga@HOSTPC ~/OSTL_v6.0/DISTRIBUTION_PKG/build-openstlinuxweston-stm32mp25-myboard]
$
```



OSTL Distribution Package – myboard.conf machine file

\$ gvim ../layers/meta-st/meta-mylayer/conf/machine/stm32mp25-myboard.conf

```
stm32mp25-myboard.conf (~/OSTL v6.0/DISTRIBUTION PKG/layers,
     Edit Tools Syntax Buffers Window Help
# activate external dt
EXTERNAL DT ENABLED = "1"
# Define list of devicetree per supported storage
STM32MP DT FILES EMMC = "stm32mp257f-myboard"
# STM32MP DT FILES SDCARD = "stm32mp257f-myboard"
# STM32MP DT FILES NOR = "stm32mp257f-myboard"
# EXTERNAL DEVICETREE SDCARD = "stm32mp257f-myboard"
EXTERNAL DEVICETREE EMMC = "stm32mp257f-myboard"
UBOOT CONFIG[default stm32mp25] = "stm32mp25 myboard defconfig,,u-boot.dtb"
# Machine features
# MACHINE FEATURES += "splashscreen"
# MACHINE FEATURES += "watchdog"
# MACHINE FEATURES += "bluetooth"
# MACHINE FEATURES += "wifi"
# GPU
# MACHINE FEATURES += "${@'gpu' if d.getVar('ACCEPT EULA '+d.getVar('MACHINE')) == '1' else ''}"
# MACHINE FEATURES += "${@'openvx' if d.getVar('ACCEPT EULA '+d.getVar('MACHINE')) == '1' else ''}"
```



OSTL Distribution Package – myimage.bb image file

\$ gvim ../layers/meta-st/meta-mylayer/recipes-st/images/myimage.bb

```
myimage.bb
File Edit Tools Syntax Buffers Window Help
SUMMARY = "My minimal image"
LICENSE = "MIT"
include recipes-st/images/st-image.inc
inherit core-image
IMAGE ROOTFS MAXSIZE = "33554432"
IMAGE FSTYPES += "${INITRAMFS FSTYPES}"
PACKAGE_INSTALL += " \
    kernel-imagebootfs \
IMAGE FEATURES = ""
CORE IMAGE EXTRA_INSTALL = ""
```



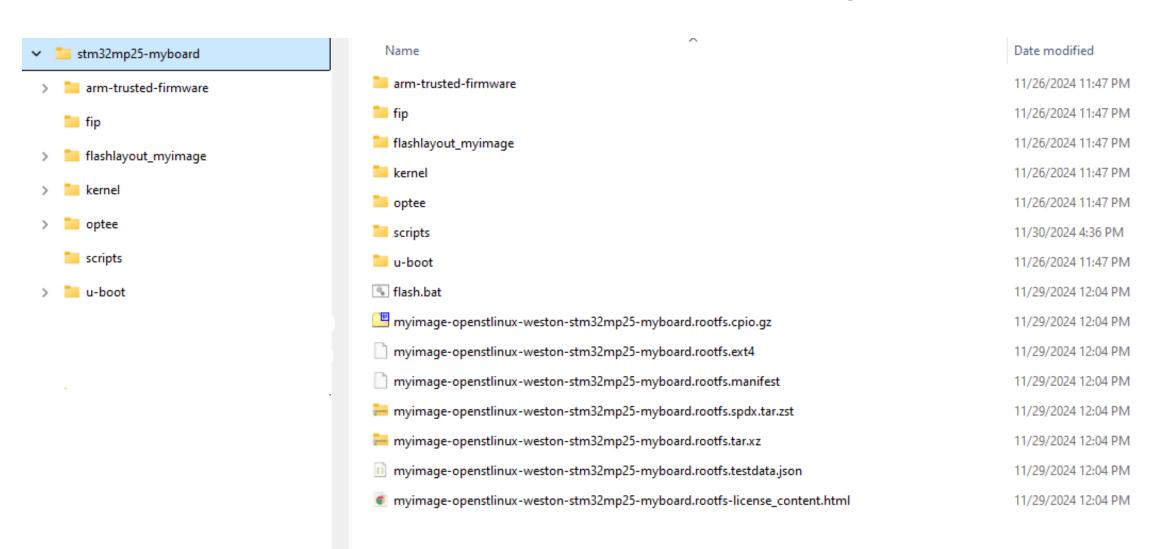
OSTL Distribution Package – **BUILD THE NEW IMAGE**

\$ bitbake myimage

paga@HOSTPC ~/OSTL v6.0/DISTRIBUTION PKG/build-openstlinuxweston-stm32mp25-myboard bitbake myimage NOTE: Started PRServer with DBfile: /home/gpaga/OSTL v6.0/DISTRIBUTION PKG/build-openstlinuxweston-stm32mp2 Loading cache: 100% | NOTE: /home/gpaga/OSTL v6.0/DISTRIBUTION PKG/layers/meta-st/meta-st-openstlinux/recipes-st/images/st-image-Parsing of 3034 .bb files complete (0 cached, 3034 parsed). 4982 targets, 739 skipped, 0 masked, 0 errors. NOTE: Resolving any missing task queue dependencies Build Configuration: = "2.8.0" BB VERSION BUILD SYS = "x86 64-linux" = "universal" NATIVELSBSTRING = "aarch64-ostl-linux" TARGET SYS MACHINE = "stm32mp25-myboard" DISTRO = "openstlinux-weston" DISTRO VERSION = "5.0.3-snapshot-20241102" = "aarch64 crc cortexa35" TUNE FEATURES TARGET FPU = "scarthgap" DISTRO CODENAME ACCEPT EULA stm32mp25-myboard = = "13.%" GCCVERSION PREFERRED_PROVIDER_virtual/kernel = "linux-stm32mp" meta-mylayer = "<unknown>:<unknown>" meta-python meta-oe meta-gnome meta-multimedia meta-networking = "HEAD:1235dd4ed4a57e67683c045ad76b6a0f9e896b45" meta-webserver = "HEAD:0831ee6057b49692e88dcca169250cb3e8f6c597" meta-st-stm32mp meta-st-openstlinux = "HEAD:6efa32c7e8162950b39827d18ffba23af01483d8" meta = "HEAD:236ac1b43308df722a78d3aa20aef065dfae5b2b" meta-my-new-layer = "<unknown>:<unknown>" Sstate summary: Wanted 60 Local 59 Mirrors 0 Missed 1 Current 1453 (98% match, 99% complete)################## NOTE: Executing Tasks NOTE: Tasks Summary: Attempted 3445 tasks of which 3441 didn't need to be rerun and all succeeded. NOTE: Writing buildhistory NOTE: Writing buildhistory took: 4 seconds [gpaga@HOSTPC ~/OSTL v6.0/DISTRIBUTION PKG/build-openstlinuxweston-stm32mp25-myboard]

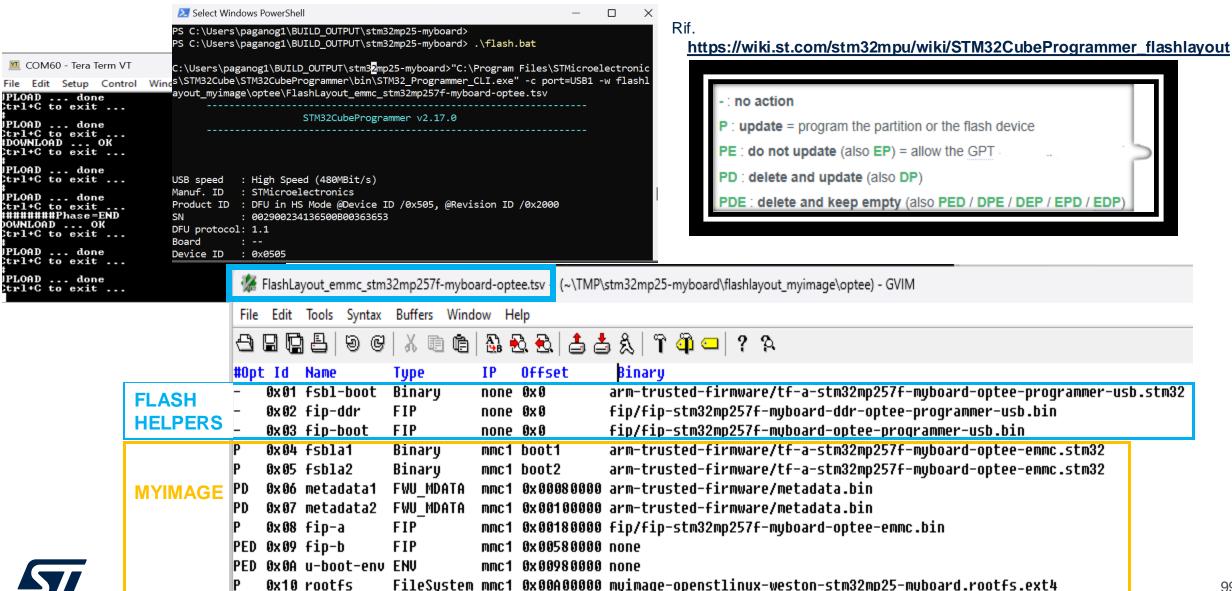


OpenSTLinux Distribution Package – MY IMAGE





OpenSTLinux Distribution Package – IMAGE FLUSH





OpenSTLinux installation check

Use serial console to run some simple commands:

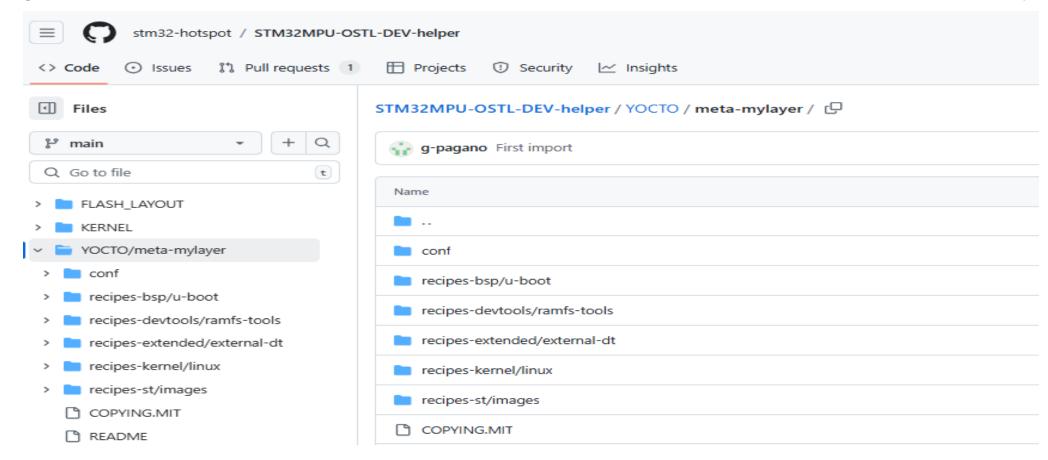
- free Discover how much DDR RAM is available on your system
- uptime Print load of your system
- df —h Print occupation of your storage [Disk Free]
- dmesg Show debug messages from Linux kernel
- dmesg | grep "version" Extract version string from debug Linux kernel messages
- (DS ax) Show active processes running on your Linux system
- gdisk Print GPT disk partition table



OpenSTLinux metalayer example "meta-mylayer"

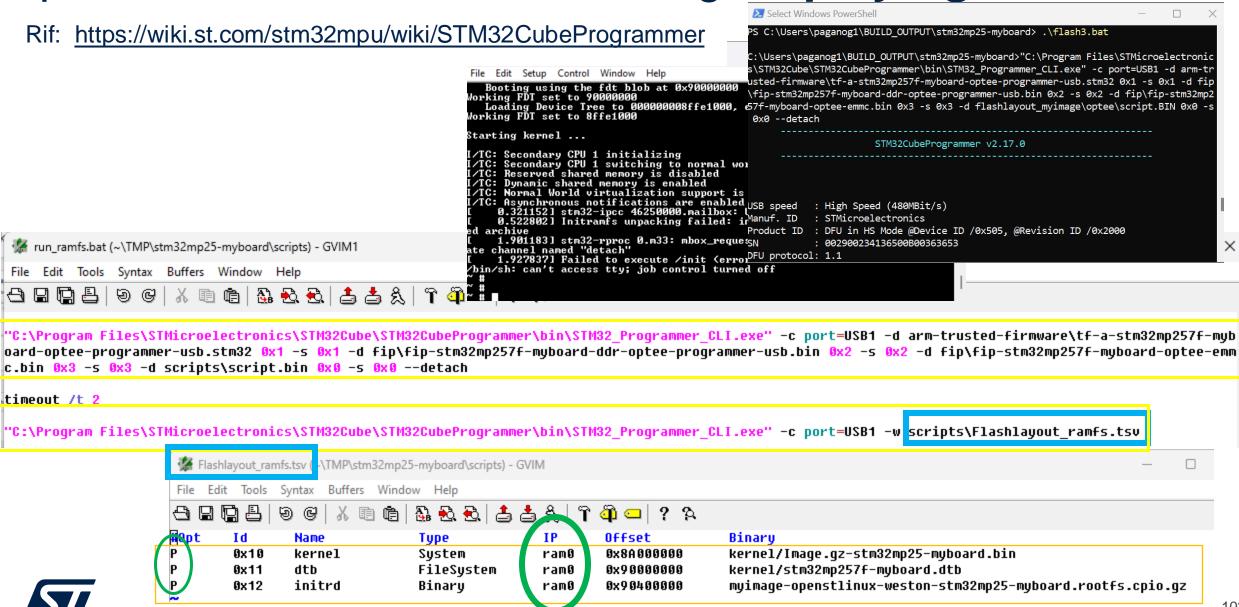
Rif:

https://github.com/stm32-hotspot/STM32MPU-OSTL-DEV-helper/tree/main/YOCTO/meta-mylayer





OpenSTLinux Distribution Package – playing with ramfs

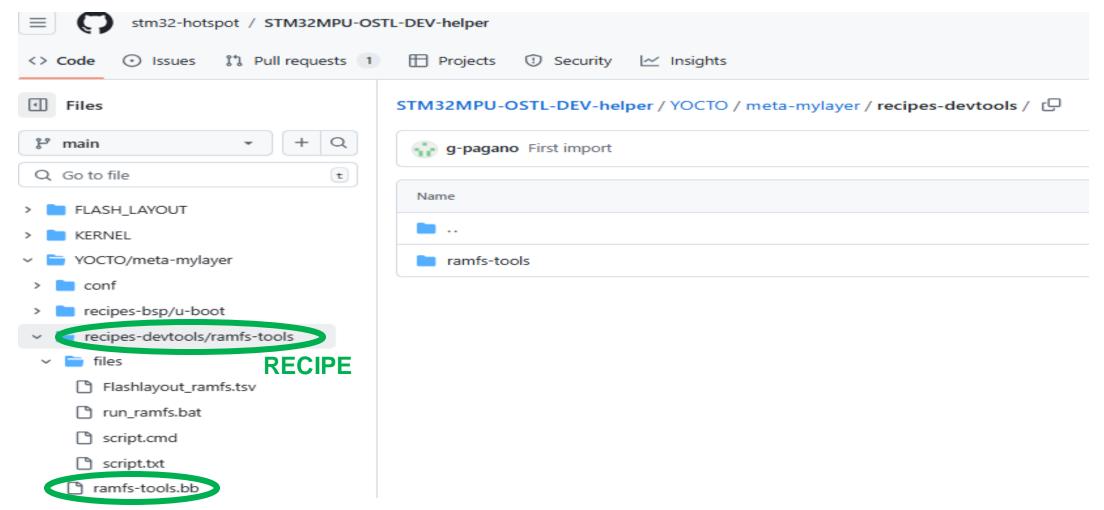




OpenSTLinux recipe example "ramfs-tools.bb"

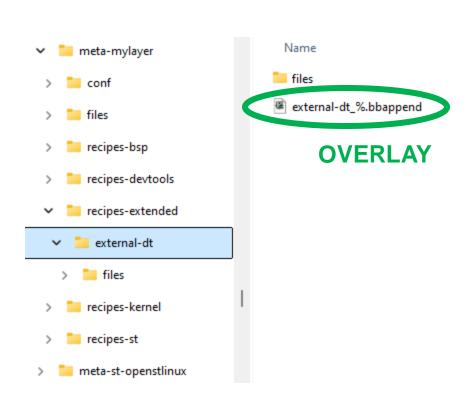
Rif:

https://github.com/stm32-hotspot/STM32MPU-OSTL-DEV-helper/tree/main/YOCTO/meta-mylayer/recipes-devtools



OSTL Distribution Package – ext devicetree files

\$ gvim ../layers/meta-st/meta-mylayer/recipes-extended/external-dt/external-dt_%.bbappend



```
external-dt %.bbappend (~/OSTL v6.0/DISTRIBUTION PKG/layers/meta-st
     Edit Tools Syntax Buffers Window Help
## SUMMARY = "Provides Device Tree files for STM32MP257 myboard"
## LICENSE = "GPL-2.0-only"
## LIC FILES CHKSUM = "file://${COMMON LICENSE DIR}/GPL-2.0-only;md5=801f80980d171dd6425610833a22dbe6"
FILESEXTRAPATHS:prepend := "${THISDIR}/files:"
SRC URI = " \
                file://devicetree/License.md \
                file://devicetree/README.md \
                file://devicetree/SECURITY.md \
                file://devicetree/linux/Makefile \
                file://devicetree/linux/stm32mp257f-myboard.dts \
                file://devicetree/linux/stm32mp257f-myboard-resmem.dtsi \
                file://devicetree/optee/conf.mk \
                file://devicetree/optee/stm32mp257f-myboard.dts \
                file://devicetree/optee/stm32mp257f-myboard-rcc.dtsi \
                file://devicetree/optee/stm32mp257f-myboard-resmem.dtsi \
                file://devicetree/optee/stm32mp257f-myboard-rif.dtsi \
                file://devicetree/tf-a/stm32mp257f-myboard.dts \
                file://devicetree/tf-a/stm32mp257f-myboard-fw-config.dts \
                file://devicetree/tf-a/stm32mp257f-myboard-fw-config.dtsi \
                file://devicetree/tf-a/stm32mp257f-myboard-rcc.dtsi \
                file://devicetree/u-boot/Makefile \
                file://devicetree/u-boot/stm32mp257f-myboard.dts \
                file://devicetree/u-boot/stm32mp257f-myboard-resmem.dtsi \
                file://devicetree/u-boot/stm32mp257f-myboard-u-boot.dtsi \
S = "${WORKDIR}/devicetree"
```



OSTL Distribution Package – ext devicetree files

Folder: layers/meta-st/meta-mylayer/recipes-extended/external-dt/files

