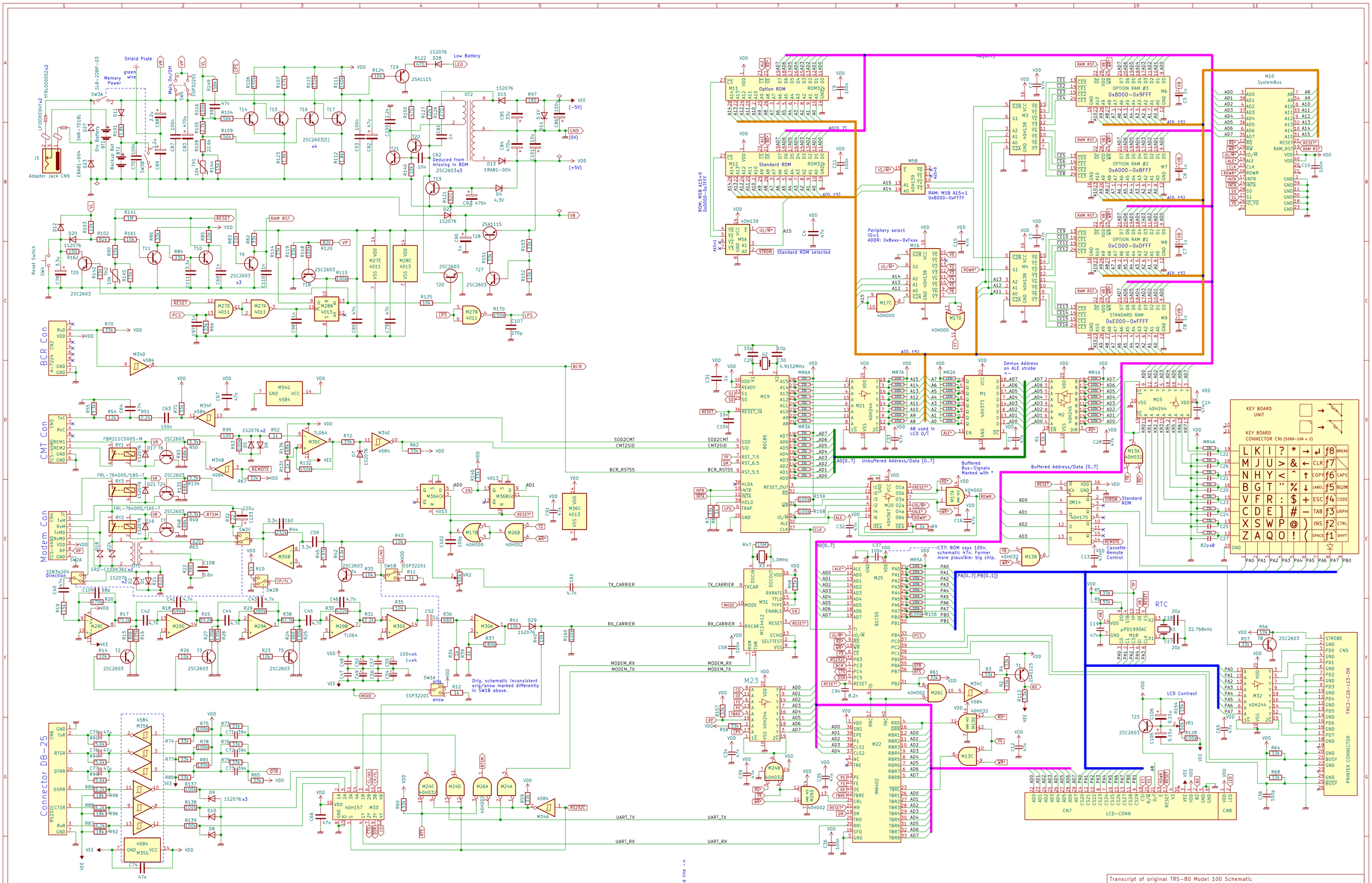
Adapter Jack CN9
Shield Plate
Memory Power
Dry Battery
Backup Bat
Main On/Off
Low Battery
Reset Switch
Option ROM
Standard ROM
ROM: MSB A15=0 0x0000-0x7FFF
RAM: MSB A15=1 0x8000-0xFFFF
Standard ROM selected
Periphery select IO=1 ADDR: 0x8xxx-0xFxxx
OPTION RAM #3 0x8000-0x9FFF
OPTION RAM #2 0xA000-0xBFFF
OPTION RAM #1 0xC000-0xDFFF
STANDARD RAM 0xE000-0xFFFF
M10 SystemBus
Demux Address on ALE strobe
AB used in LCD D/I
Unbuffered Address/Data [0..7]
Buffered Bus-Signals Marked with *
Buffered Address/Data [0..7]
Standard ROM
Cassette Remote Control
KEY BOARD UNIT
KEY BOARD CONNECTOR CN1 (5268-10A x 2)
RTC
LCD Contrast
BCR Con
CMT Con
Modem Con
Connector DB-25
CN5
PRINTER CONNECTOR
CN7
LCD-CONN
CN8
SOD2CMT
CMT2SID
BCR_RST55
TX_CARRIER
RX_CARRIER
MODEM_RX
MODEM_TX
UART_TX
UART_RX
Orig. schematic inconsistent orig/answ marked differently in SW1B above.
C37: BOM says 100n, schematic 47n. Former more plausible: blg chip.
<- avoid components on fold line ->
Transcript of original TRS-80 Model 100 Schematic
Please file mistakes at
https://github.com/hzeller/trs80-100-schematic (Henner Zeller; h.zeller@acm.org)
Kyocera / Tandy
Sheet: /
File: trs80-100.kicad_sch
Title: TRS-80 Model 100 Schematic
Size: A2
Date: 1983
Rev:
KiCad E.D.A. kicad 6.0.6
Id: 1/1