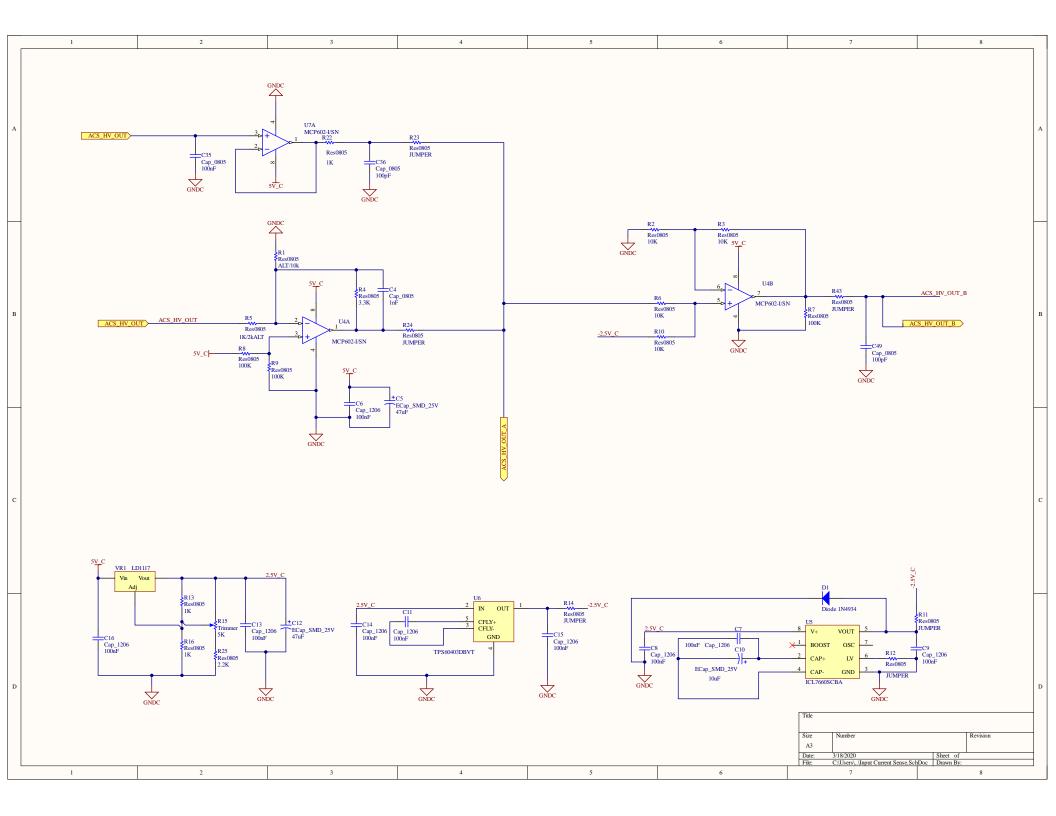
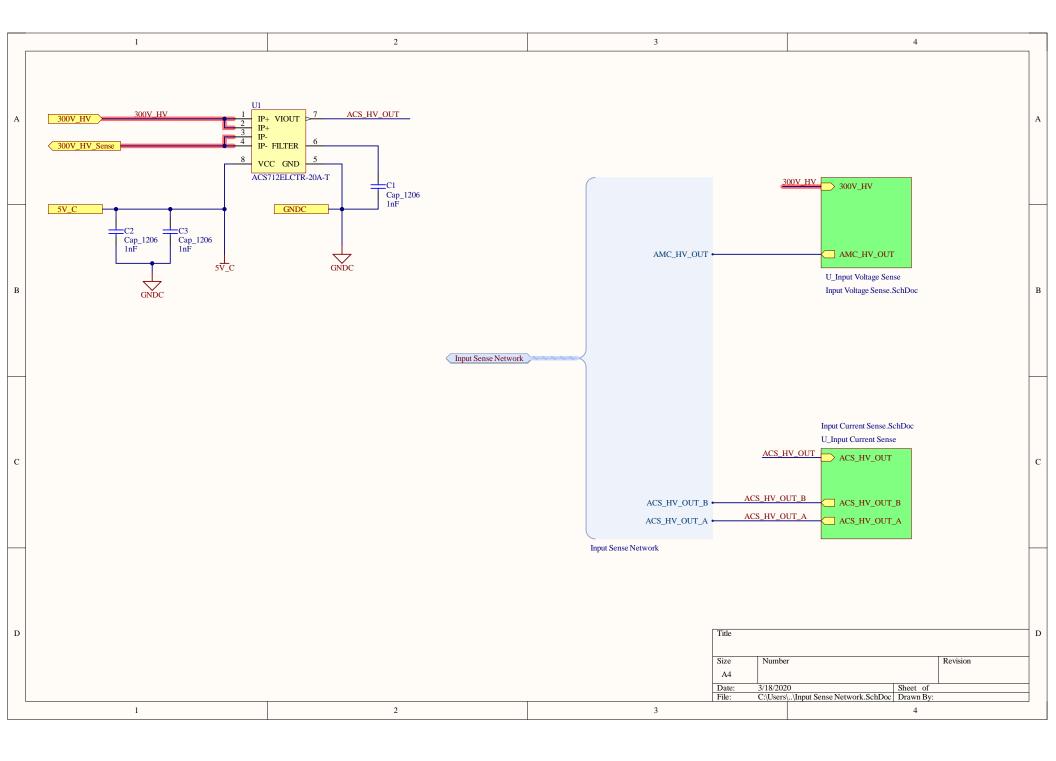
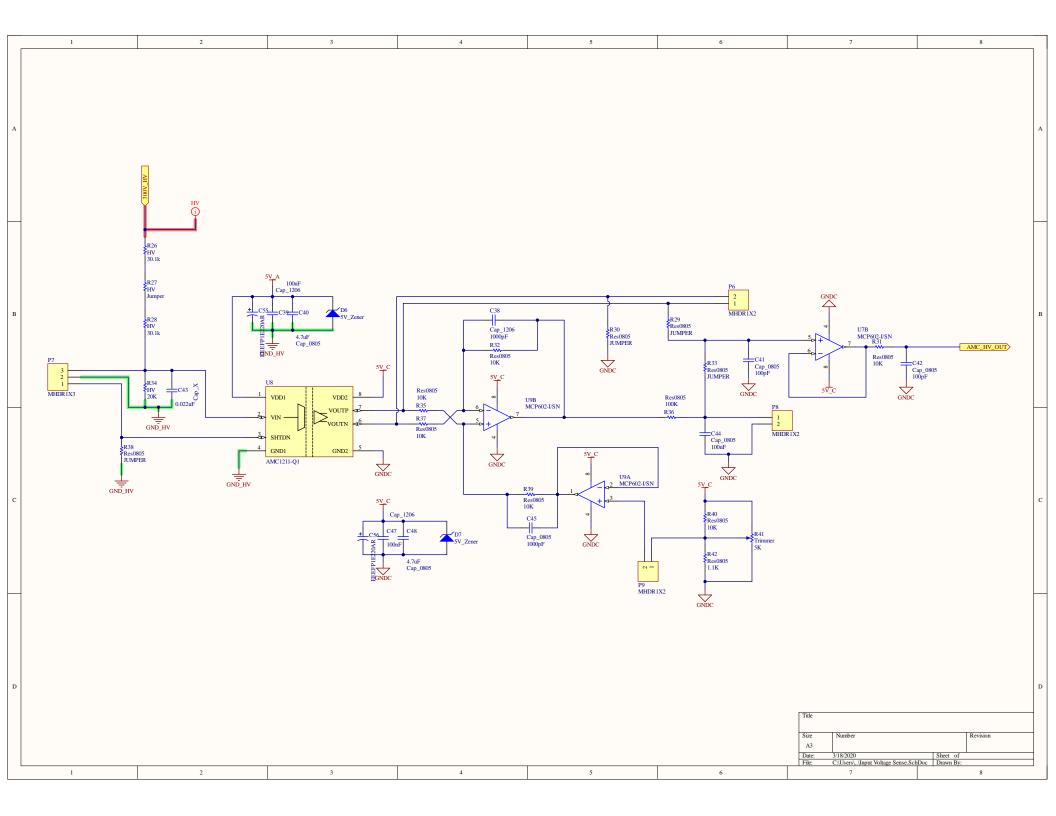


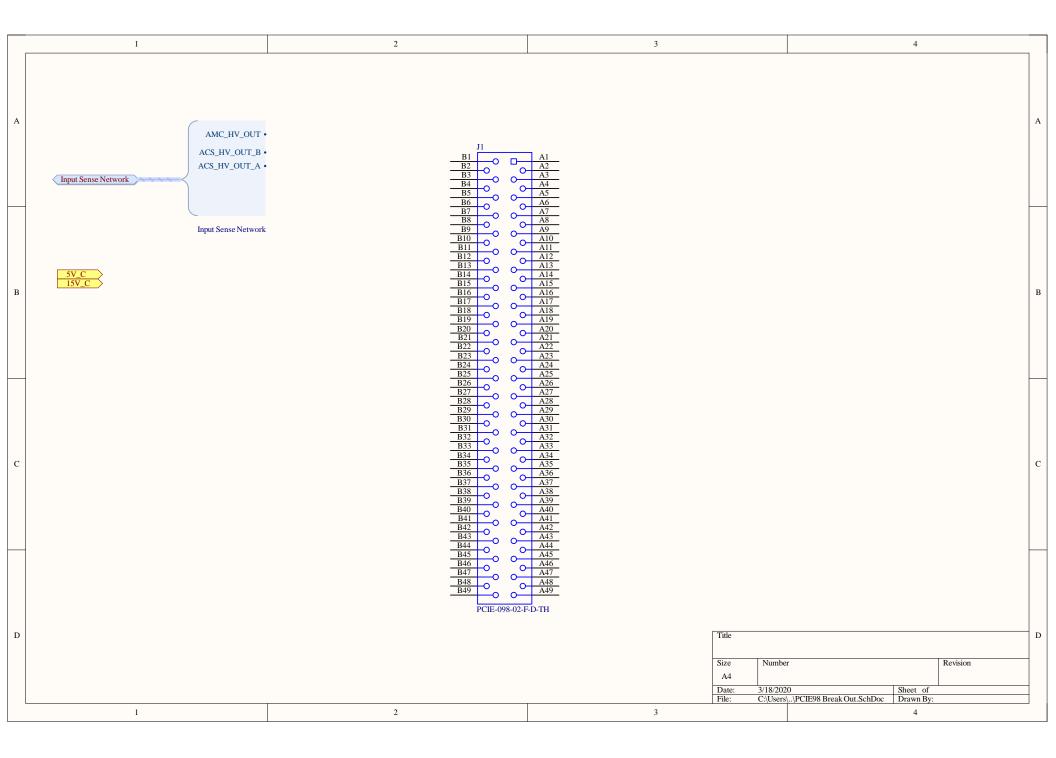
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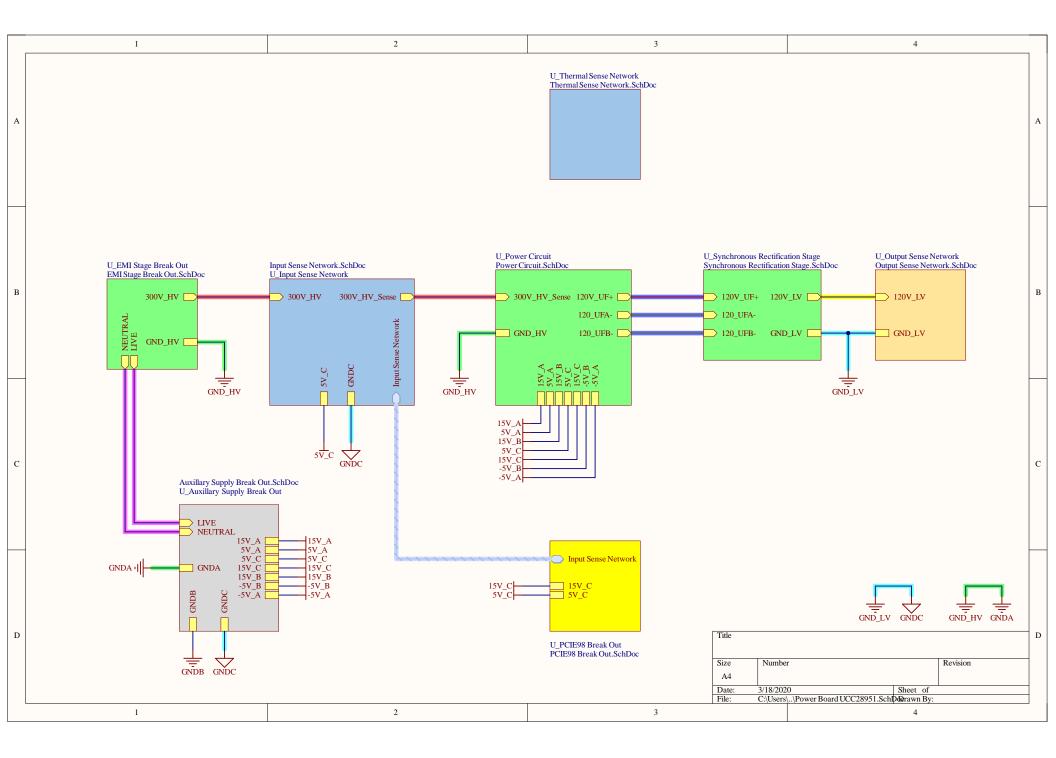


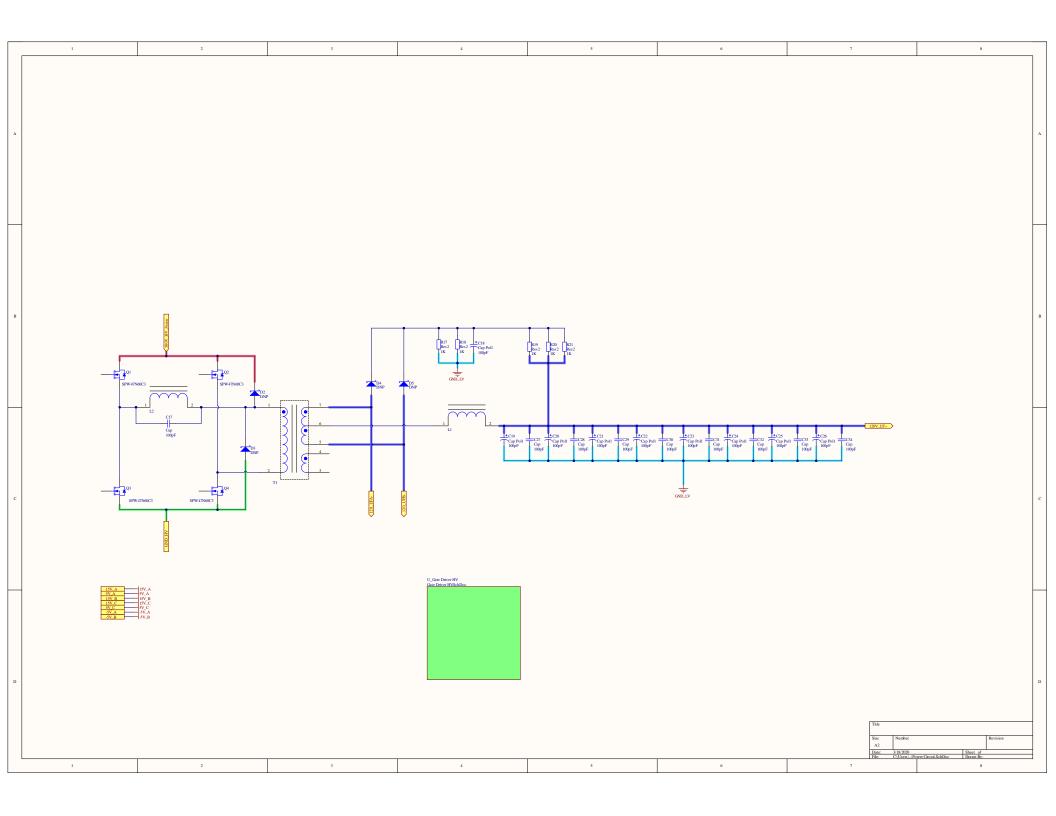




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В				В
С				С
D	1	2	Title Size	er Revision 20 Sheet of S\Thermal Sense Network.SchDoDrawn By: 4

