

# OpenGL hardware matrix

Extensions exposed by OpenGL implementations

**May 2014, G-Truc Creation**

GF / Fermi: GeForce 400 series, GeForce 500 series

GK / Kepler: GeForce 600 series, GeForce 700 series

GM / Maxwell: GeForce 750

EG / Evergreen: Radeon HD 5000 series, Radeon HD 6000 series

N.I. / Northern Islands: Radeon HD 6900 series

S.I. / Southern Islands: Radeon HD 7000 series, Radeon R7 250X, Radeon R7 265, Radeon R9 280

C.I. / Sea Islands: Radeon HD 7790, Radeon R7 240, Radeon R7 250, Radeon R7 260, Radeon R9 270

V.I. / Volcanic Islands: Radeon R9 290

IVB / Ivy Bridge: HD4000, HD2500

HSW / Haswell: Iris 5000 series, HD 4X00 series

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Nomenclature:

Supported

Not supported

Support added from previous report

OpenGL Extensions	GF	GK	GM	EG	N.I.	S.I.	C.I.	V.I.	IVB	HSW	Mesa	MacOS X
<u>KHR texture compression astc_ldr</u>	X	X	X	X	X	X	X	X	X	X	X	X
<u>ARB robustness</u>	V	V	V	X	X	X	X	X	V	V	X	X
<u>ARB sparse texture</u>	V	V	V	X	X	V	V	V	X	X	X	X
<u>ARB shading language include</u>	V	V	V	X	X	X	X	X	X	X	X	V
<u>ARB shader stencil export</u>	X	X	X	V	V	V	V	V	X	X	X	X
<u>ARB shader group vote</u>	V	V	V	X	X	V	V	V	X	X	X	X
<u>ARB shader draw parameters</u>	V	V	V	X	X	V	V	V	X	X	X	X
<u>ARB seamless cubemap per texture</u>	X	V	V	V	V	V	V	V	X	X	X	X
<u>ARB robustness isolation</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>ARB robust buffer access behavior</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>ARB debug output</u>	V	V	V	V	V	V	V	V	V	V	V	X
<u>ARB indirect parameters</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>ARB compute variable group size</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>ARB compatibility</u>	V	V	V	V	V	V	V	V	V	V	X	X
<u>ARB cl event</u>	X	X	X	X	X	X	X	X	X	X	X	X
<u>ARB bindless texture</u>	X	V	V	X	X	V	V	V	X	X	X	X
Support	69%	81%	81%	25%	25%	50%	50%	50%	19%	19%		6%

OpenGL Extensions	GF	GK	GM	EG	N.I.	S.I.	C.I.	V.I.	IVB	HSW	Mesa	MacOS X
<u>EXT texture sRGB decode</u>	V	V	V	V	V	V	V	V	V	V	X	V
<u>EXT texture mirror clamp</u>	V	V	V	V	V	V	V	V	X	X	X	V
<u>EXT shader integer mix</u>	V	V	V	V	V	V	V	V	V	V	V	X
<u>EXT shader image load formatted</u>	X	X	V	X	X	X	X	X	X	X	X	X
<u>EXT framebuffer multisample blit scaled</u>	V	V	V	X	X	X	X	X	X	X	X	V
<u>EXT direct state access</u>	V	V	V	V	V	V	V	V	X	X	X	X
<u>EXT depth bounds test</u>	V	V	V	X	X	V	V	V	X	X	X	V
<u>EXT clip control</u>	X	X	X	X	X	X	X	X	V	V	X	X

<u>NV vertex buffer unified memory</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>NV texture multisample</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>NV texture barrier</u>	V	V	V	V	V	V	V	V	X	X	X	V
<u>NV shader thread shuffle</u>	X	V	V	X	X	X	X	X	X	X	X	X
<u>NV shader thread group</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>NV shader buffer store</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>NV shader buffer load</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>NV shader atomic float</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>NV multisample coverage</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>NV explicit multisample</u>	V	V	V	V	V	V	V	V	X	X	X	X
<u>NV depth buffer float</u>	V	V	V	V	V	V	V	V	X	X	X	X
<u>NV copy image</u>	V	V	V	V	V	V	V	V	X	X	X	X
<u>NV bindless texture</u>	X	V	V	X	X	X	X	X	X	X	X	X
<u>NV bindless multi draw indirect</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>NV blend equation advanced</u>	V	V	V	X	X	X	X	X	X	X	X	X
<u>INTEL map texture</u>	X	X	X	X	X	X	X	X	X	V	X	X
<u>INTEL fragment shader ordering</u>	X	X	X	X	X	V	V	V	V	V	X	X
<u>INTEL conservative rasterization</u>	X	X	X	X	X	X	X	X	X	V	X	X
<u>AMD vertex shader viewport index</u>	X	X	X	V	V	V	V	V	X	X	X	X
<u>AMD vertex shader layer</u>	X	X	X	V	V	V	V	V	X	X	X	X
<u>AMD transform feedback4</u>	X	X	X	X	X	V	V	V	X	X	X	X
<u>AMD transform feedback3 lines triangles</u>	X	X	X	X	V	V	V	V	X	X	X	X
<u>AMD stencil operation extended</u>	X	X	X	X	X	V	V	V	X	X	X	X
<u>AMD sparse texture pool</u>	X	X	X	X	X	X	V	V	X	X	X	X
<u>AMD sparse texture</u>	X	X	X	X	X	V	V	V	X	X	X	X
<u>AMD shader trinary minmax</u>	X	X	X	X	X	V	V	V	X	X	X	X
<u>AMD shader stencil value export</u>	X	X	X	X	X	V	V	V	X	X	X	X
<u>AMD shader stencil export</u>	X	X	X	V	V	V	V	V	X	X	X	X
<u>AMD seamless cubemap per texture</u>	X	V	V	V	V	V	V	V	X	X	X	X
<u>AMD sample positions</u>	X	X	X	V	V	V	V	V	X	X	X	X
<u>AMD query buffer object</u>	X	X	X	V	V	V	V	V	X	X	X	X
<u>AMD pinned memory</u>	X	X	X	V	V	V	V	V	X	X	X	X
<u>AMD occlusion query event</u>	X	X	X	X	X	X	V	V	X	X	X	X





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