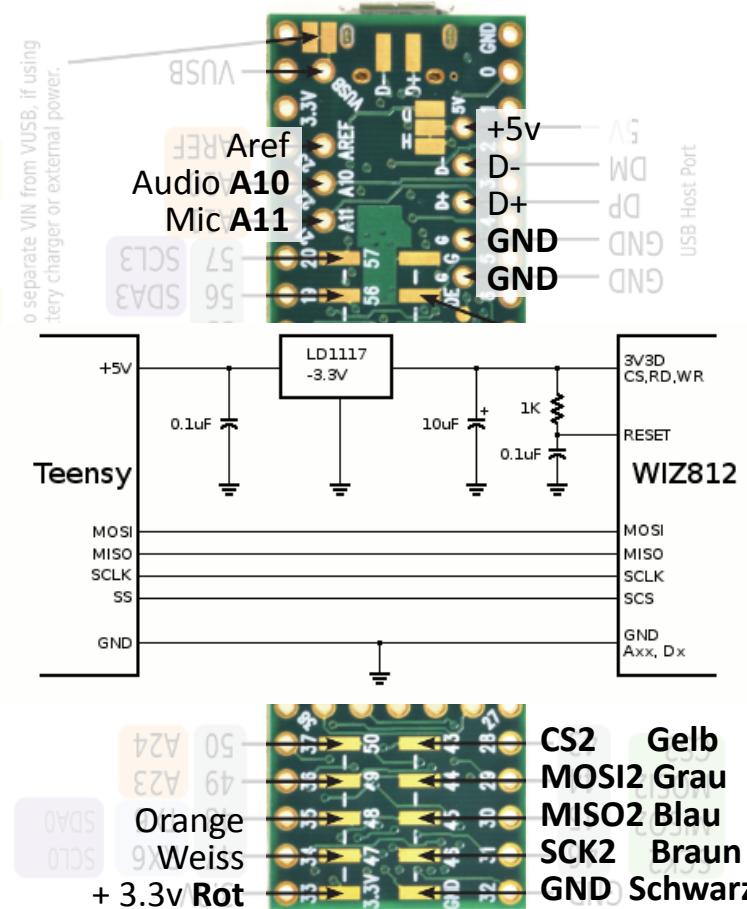
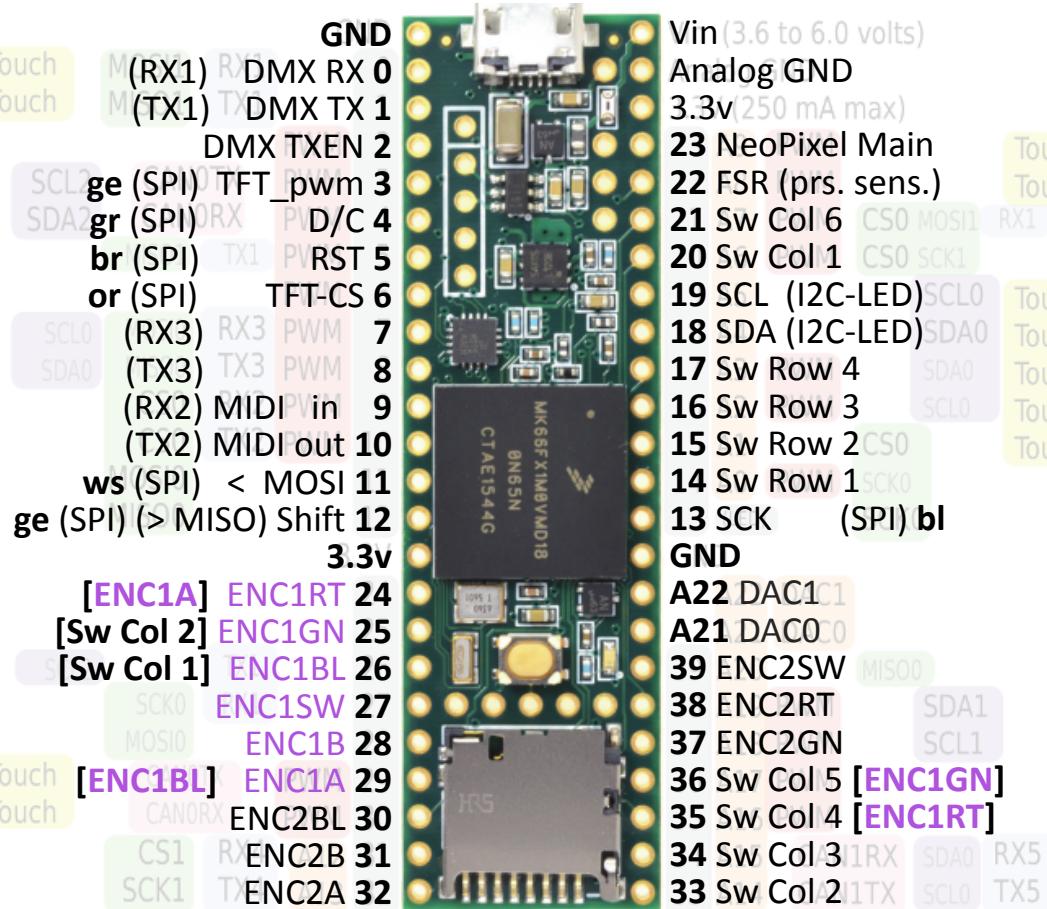
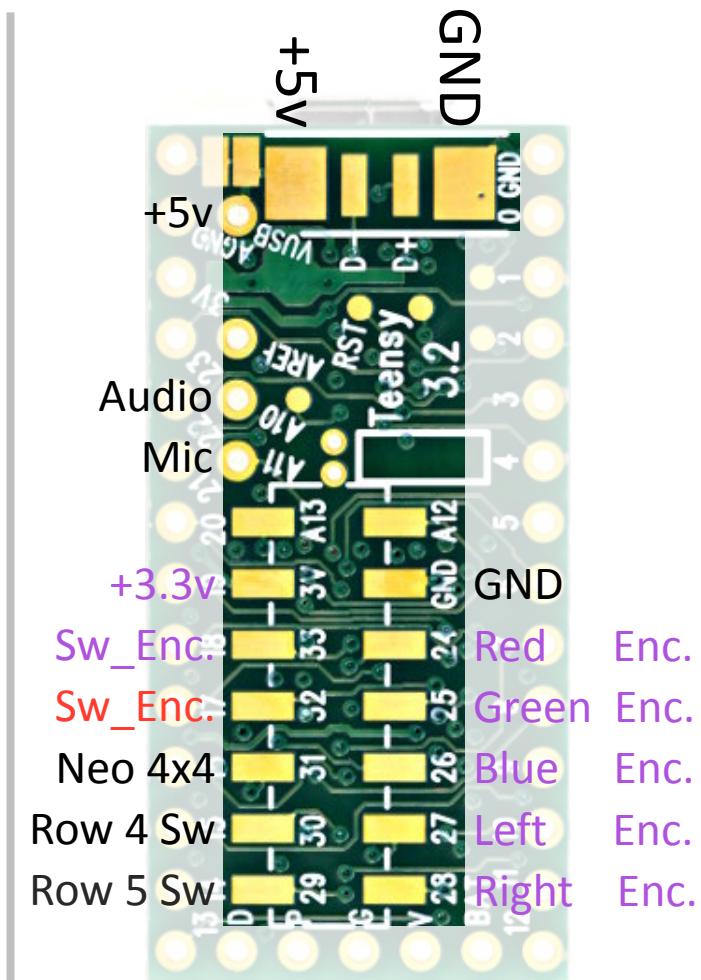


Beat Counter Teensy 3.6 - Port Usage

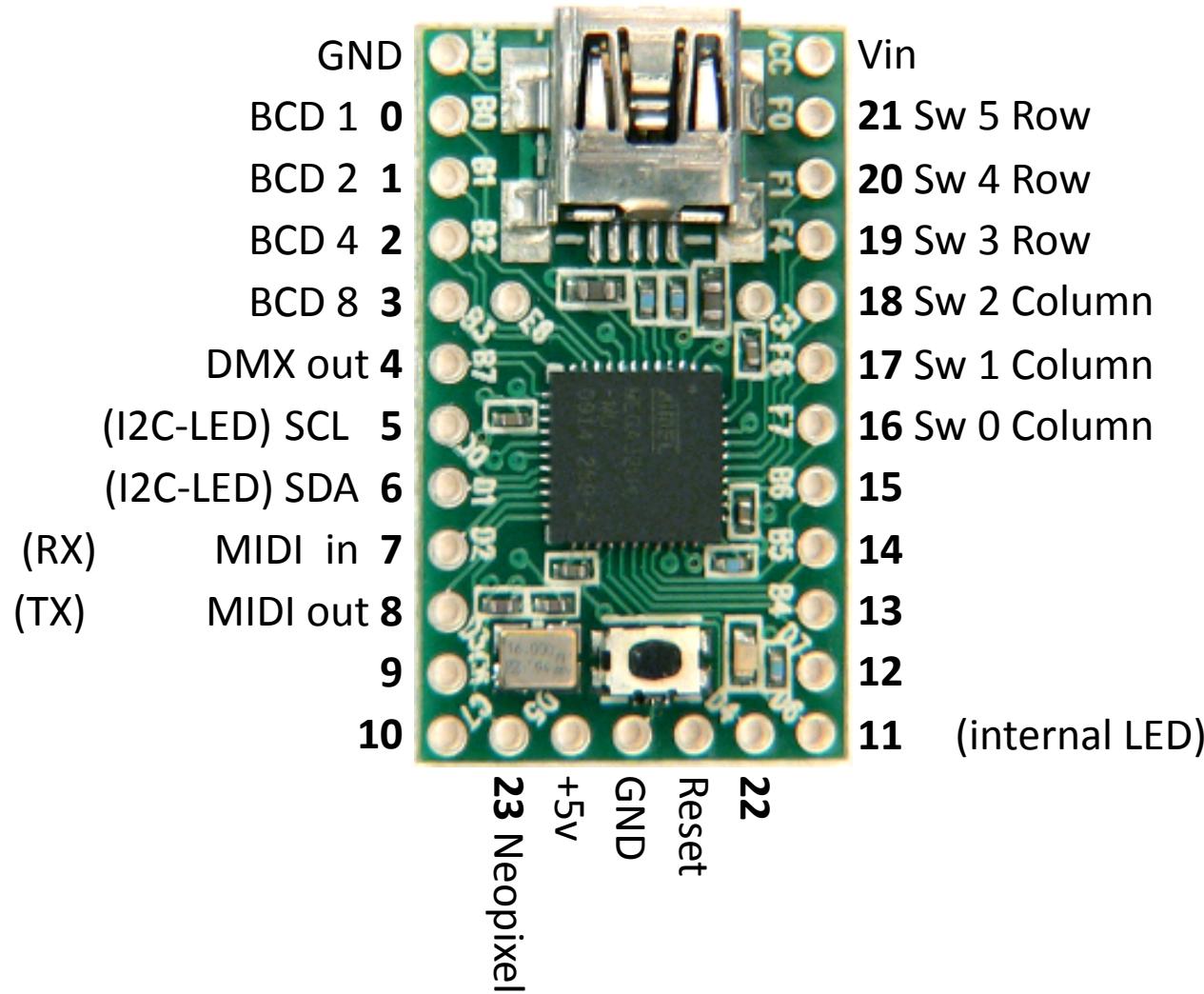


Beat Counter Teensy 3.2 - Port Usage

	GND	Vin
(RX1)	DMX RX 0	GND
(TX1)	DMX TX 1	3.3v
	DMX TXEN 2	23 NeoPixel Main
ge (SPI)	TFT_pwm 3	22 Sw Row 3
gr (SPI)	D/C 4	21 Sw Row 2
br (SPI)	RST 5	20 Sw Row 1
or (SPI)	TFT-CS 6	19 SCL (I2C-LED)
(RX3)	R_Enc 7	18 SDA (I2C-LED)
(TX3)	L_Enc 8	17 Sw Column 4
(RX2)	MIDI in 9	16 Sw Column 3
(TX2)	MIDI out 10	15 Sw Column 2
ws (SPI)	< MOSI 11	14 Sw Column 1
ge (SPI)	> MISO 12	13 SCK (SPI) bl
	VBat.	
	3.3v	
	GND	
	Program	
	DAC	



Beat Counter Teensy 2.0 - Port Usage



Welcome to Teensy® 4.0

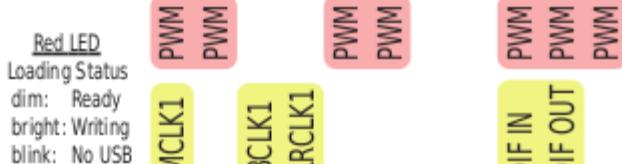
32 Bit Arduino-Compatible Microcontroller

To begin using Teensy, please visit the website & click [Getting Started](#).

www.pjrc.com/teensy

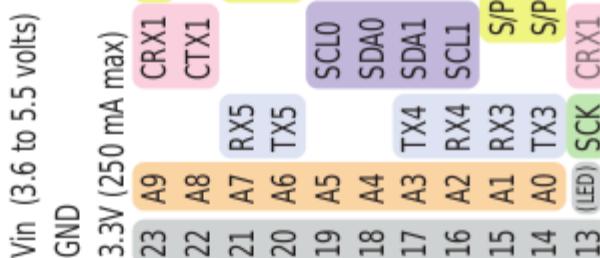
Digital Pins

`digitalRead`
`digitalWrite`
`pinMode`



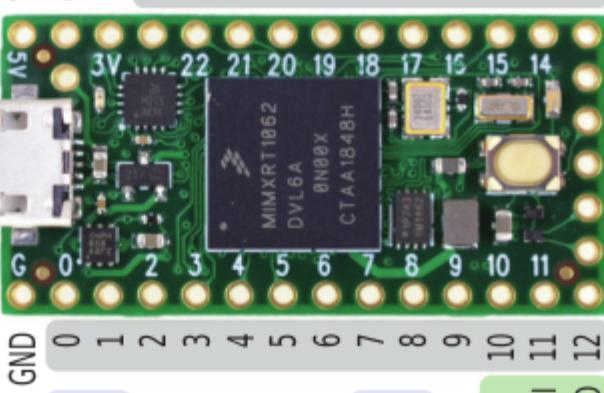
Analog Pins

`analogRead`



PWM Pins

`analogWrite`



Digital Audio

`Audio Library`

Serial Ports

Serial1 - Serial7

I²C Port

`Wire Library`

SPI Port

`SPI Library`

CAN Bus

`FlexCAN_t4 Library`

Teensy® 4.0 Back Side

Additional pins and features available on the back side

PWM	SCL2	TX6	A10	24	25	A11	RX6	SDA2	PWM
	MOSI1		A12	26	27	A13		SCK1	
PWM		RX7		28	29		TX7	PWM	
	CRX3			30	31		CTX3		
	OUT1B			32	33		MCLK2	PWM	

Cut to separate
VIN from VUSB
if using battery
or external power.



SD Card (4 bit SDIO)

SD Library
`SD.begin(BUILTIN_SDCARD)`

USB Host

`USBHost_t36 Library`

34	DAT1	MISO2	PWM
35	DAT0	MOSI2	PWM
GND			
36	CLK	CS2	PWM
3.3V			
37	CMD	SCK2	PWM
38	DAT3	RX5	PWM
39	DAT2	TX5	PWM

Teensy 4.0 signal pins
are **not** 5V tolerant.
Do not apply more than
3.3 volts to any pin,
except VIN or VUSB.

For solutions to the most common issues
and technical support, please visit:

www.pjrc.com/help

Teensy 4.0 System Requirements:

PC computer with Windows 7, 8, 10 or later
or Ubuntu Linux 14.04 or later
or Macintosh OS-X 10.8 or later
USB Micro-B Cable



Teensy 3.2

Digital Pins
`digitalRead`
`digitalWrite`
`pinMode`

Analog Pins
`analogRead`
`analogReference`
`analogReadRes`

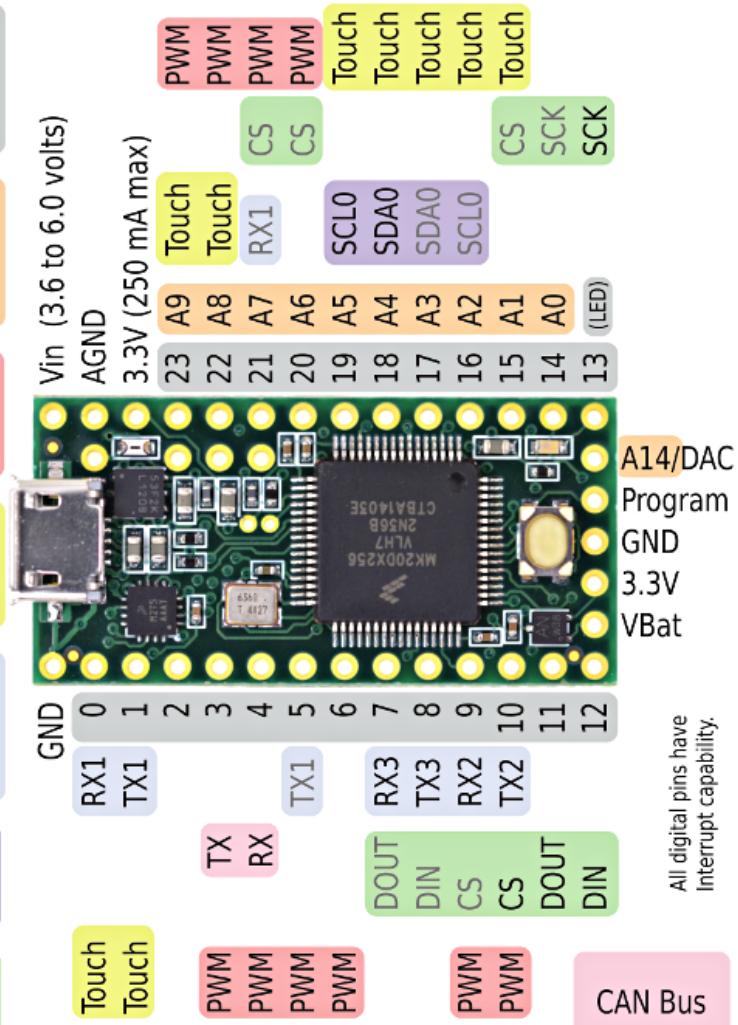
PWM Pins
`analogWrite`
`analogWriteRes`

Touch Sense Pins
`touchRead`

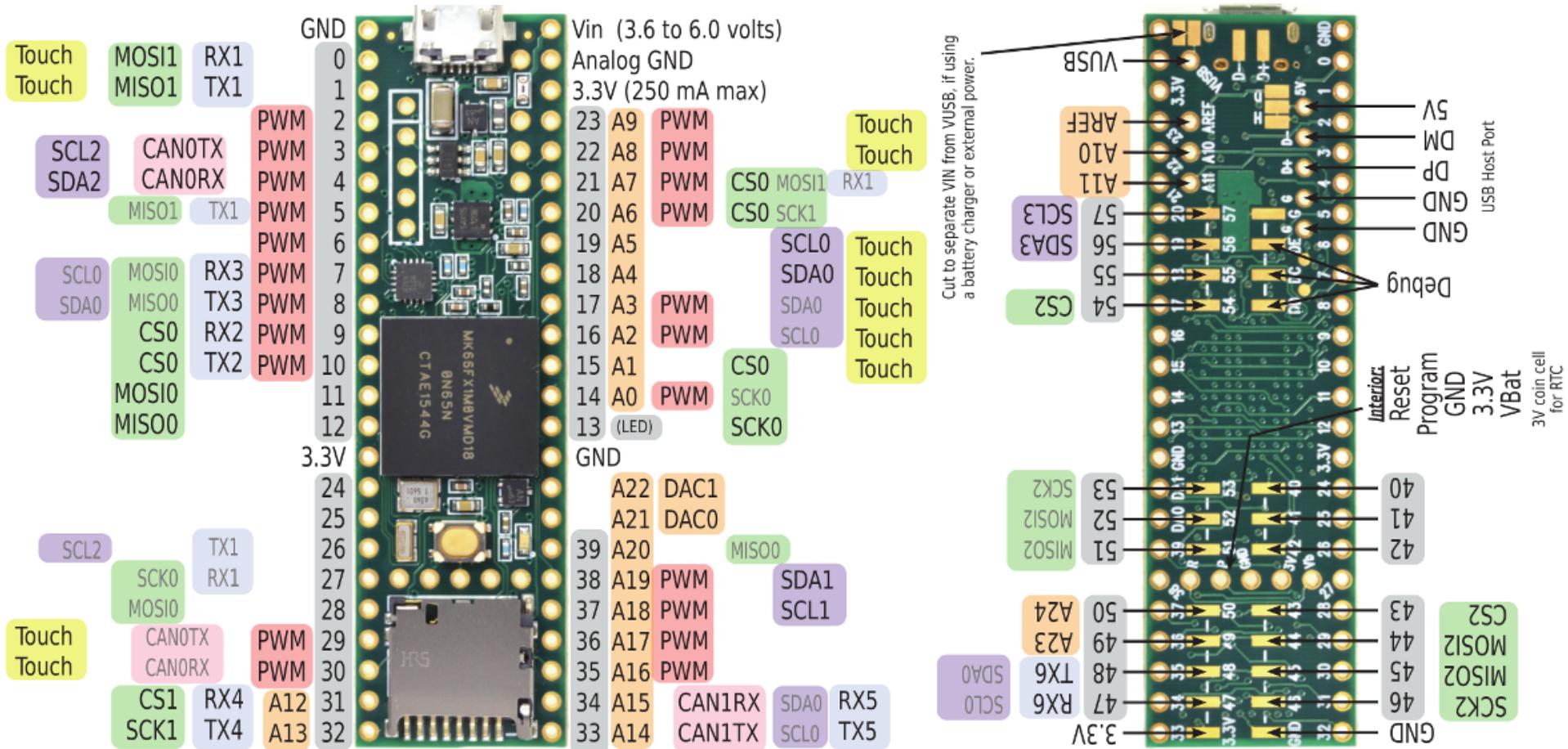
Serial Ports
`Serial1`
`Serial2`
`Serial3`

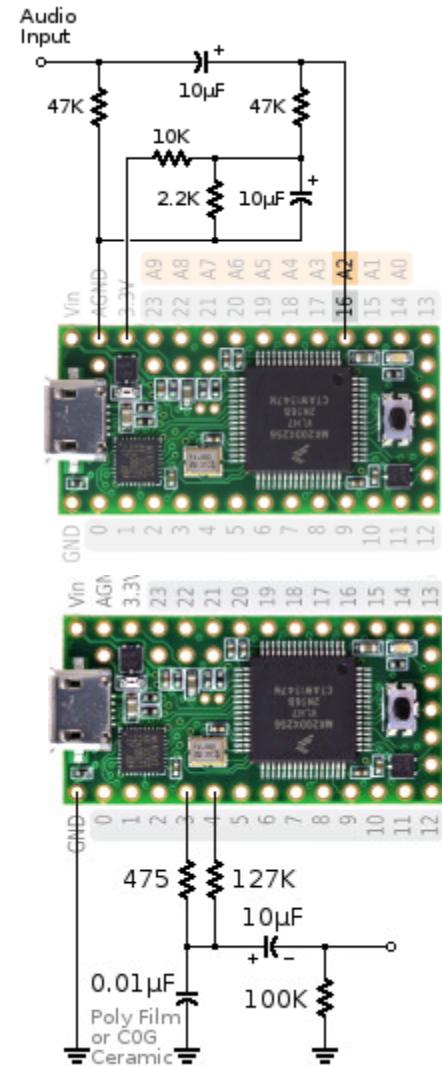
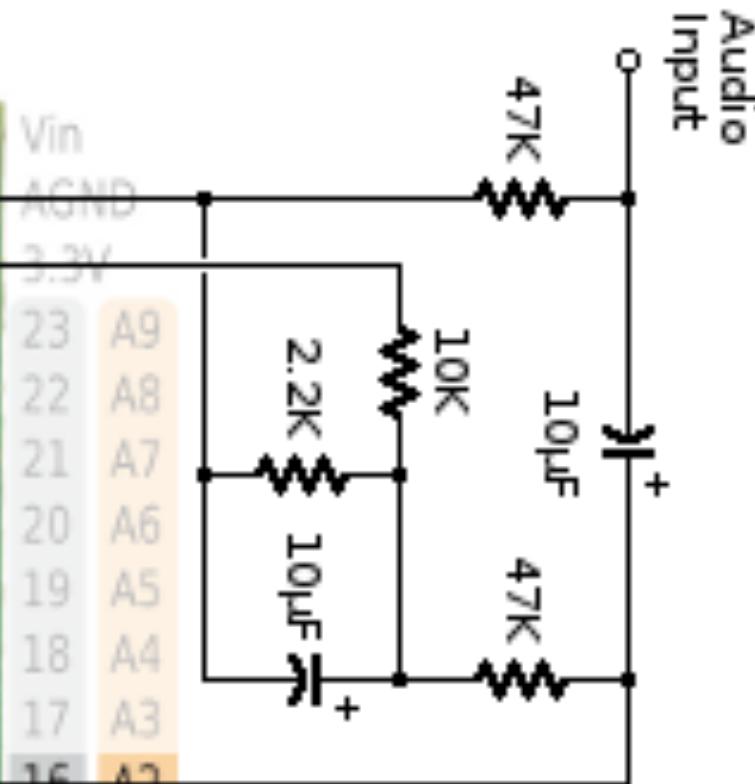
I²C Port
`Wire Library`

SPI Port
`SPI Library`

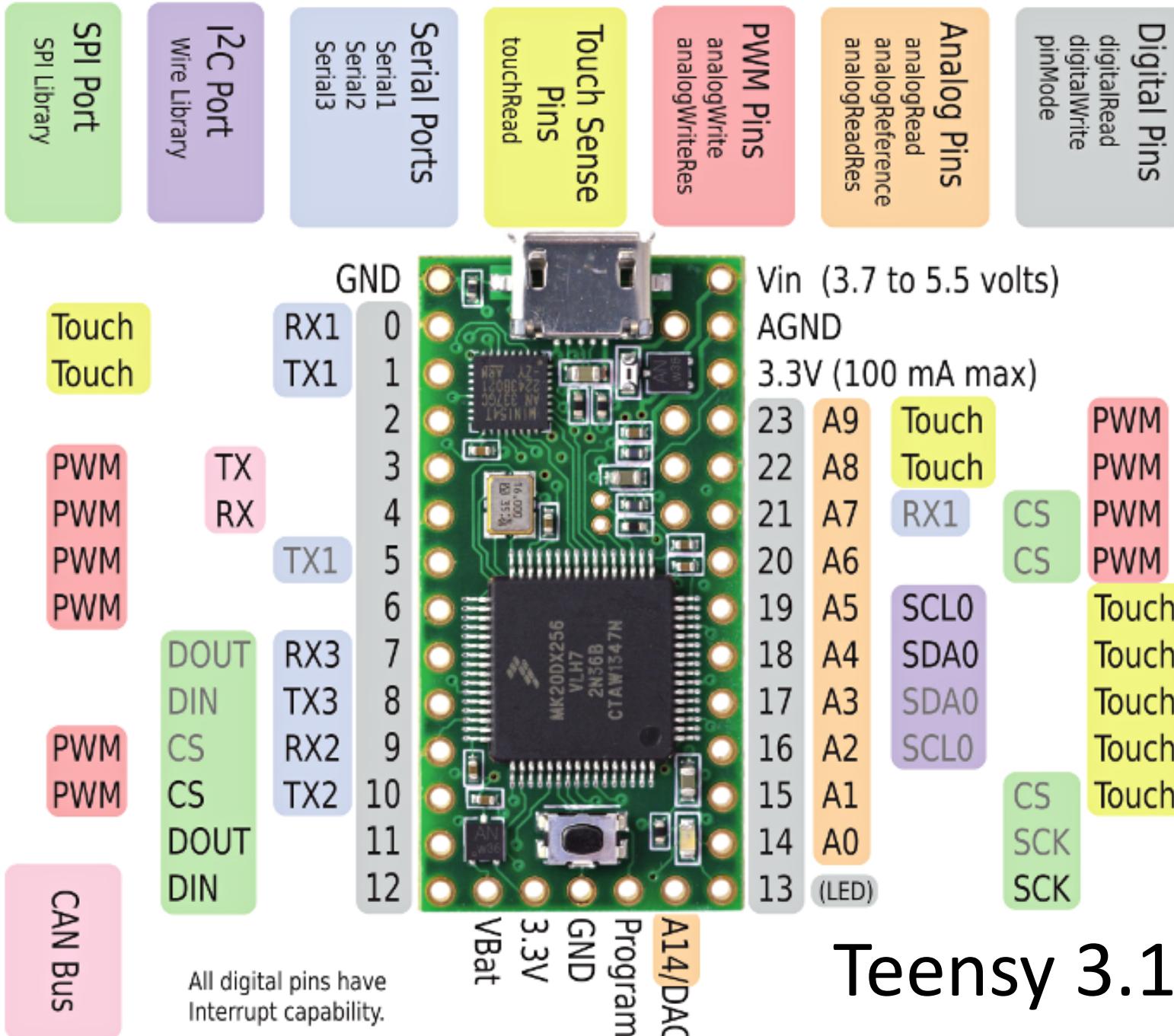


Teensy 3.6



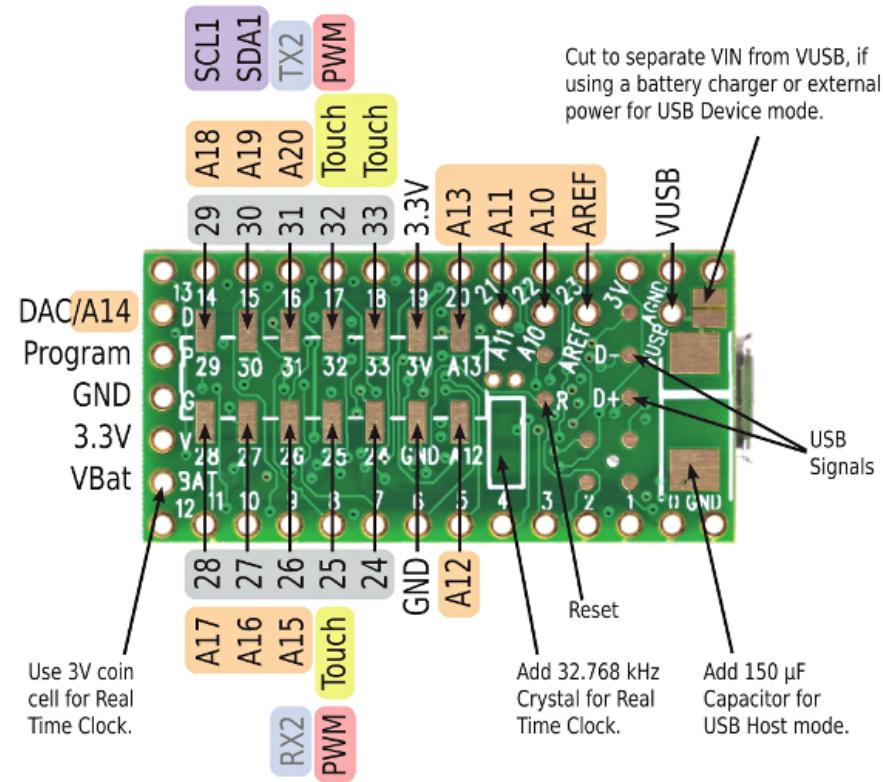
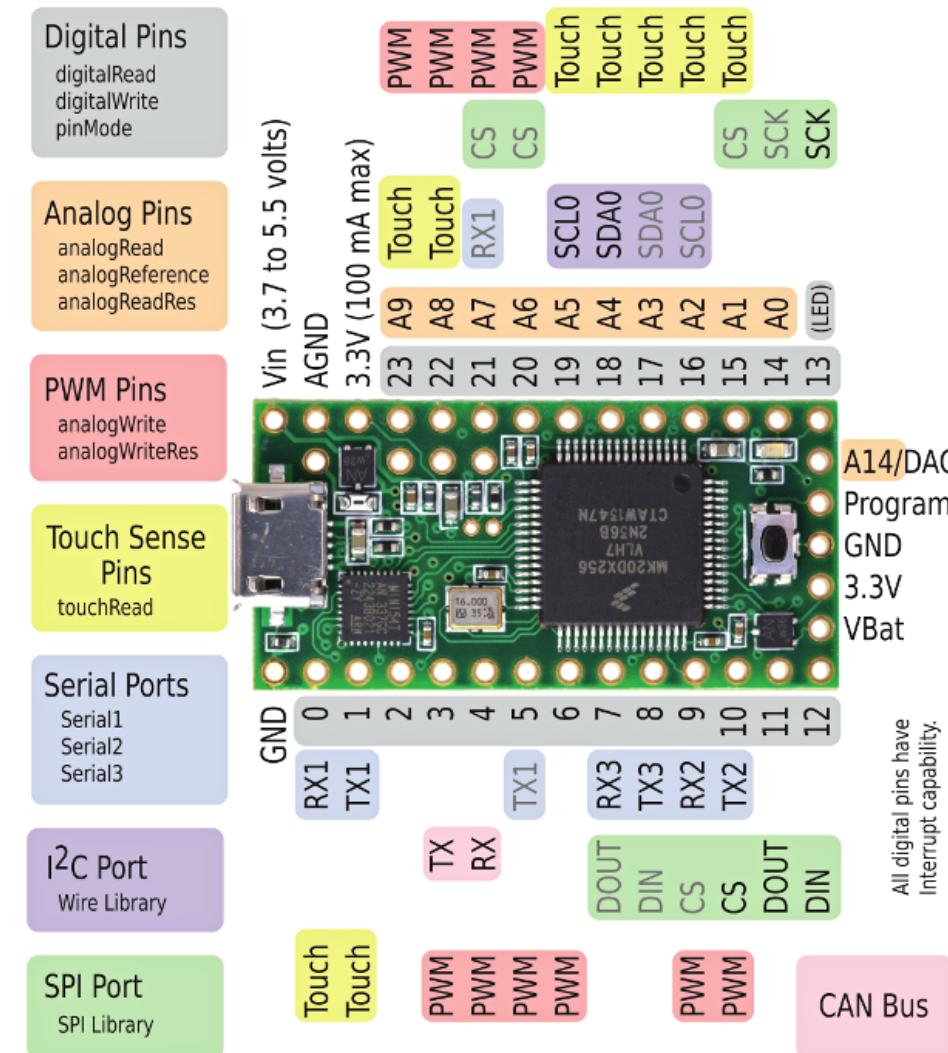


Teensy 3.1 Audio



Teensy 3.1

Teensy 3.1



Teensy LC

Digital Pins
digitalRead
digitalWrite
pinMode

Analog Pins
analogRead
analogReference
analogReadRes

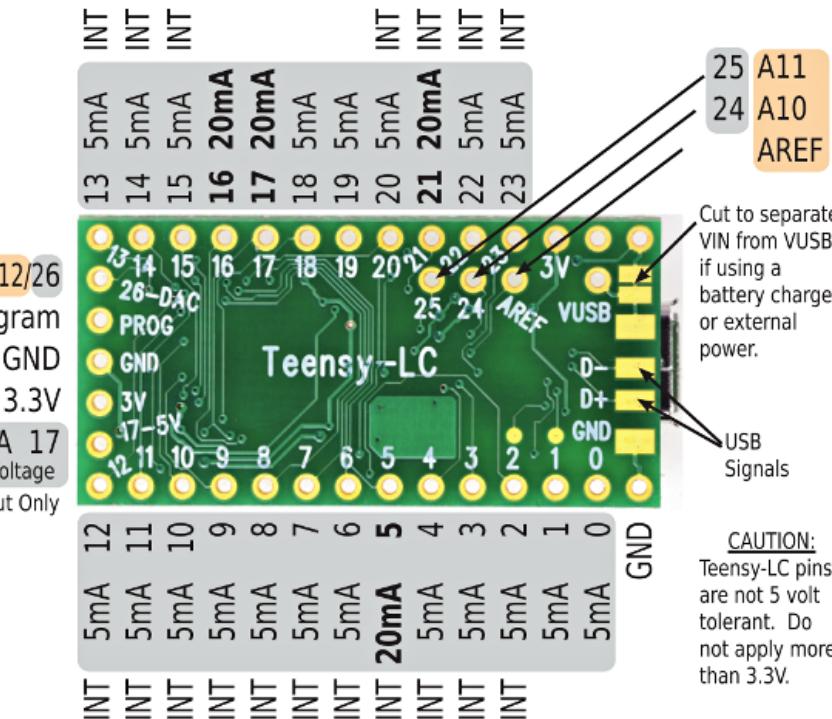
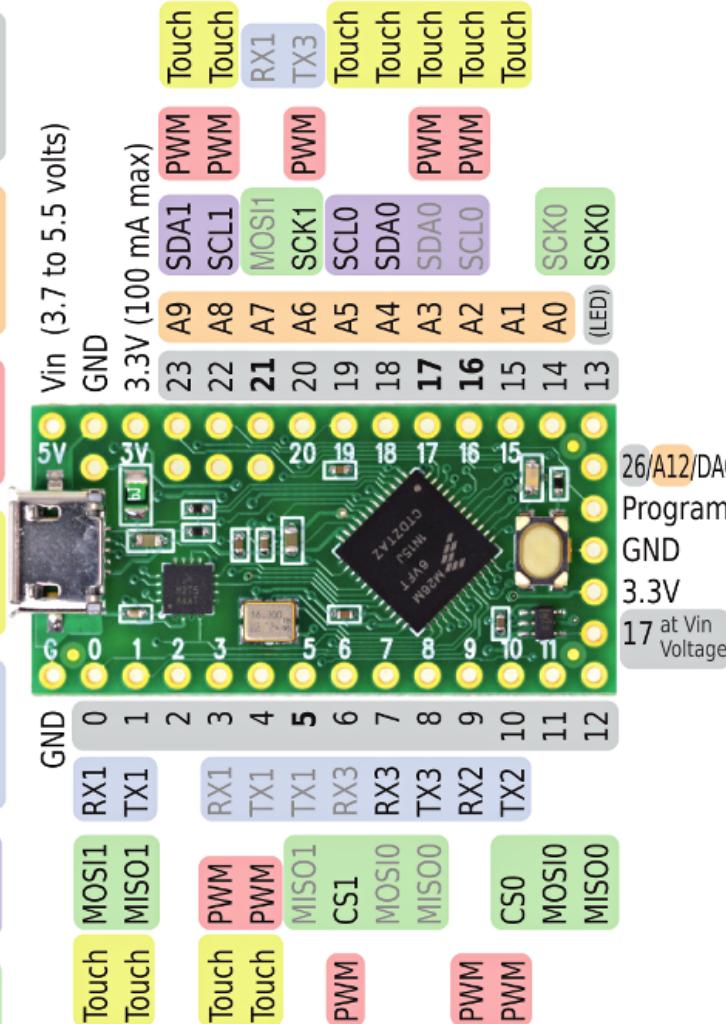
PWM Pins
analogWrite
analogWriteRes

Touch Sense Pins
touchRead

Serial Ports
Serial1
Serial2
Serial3

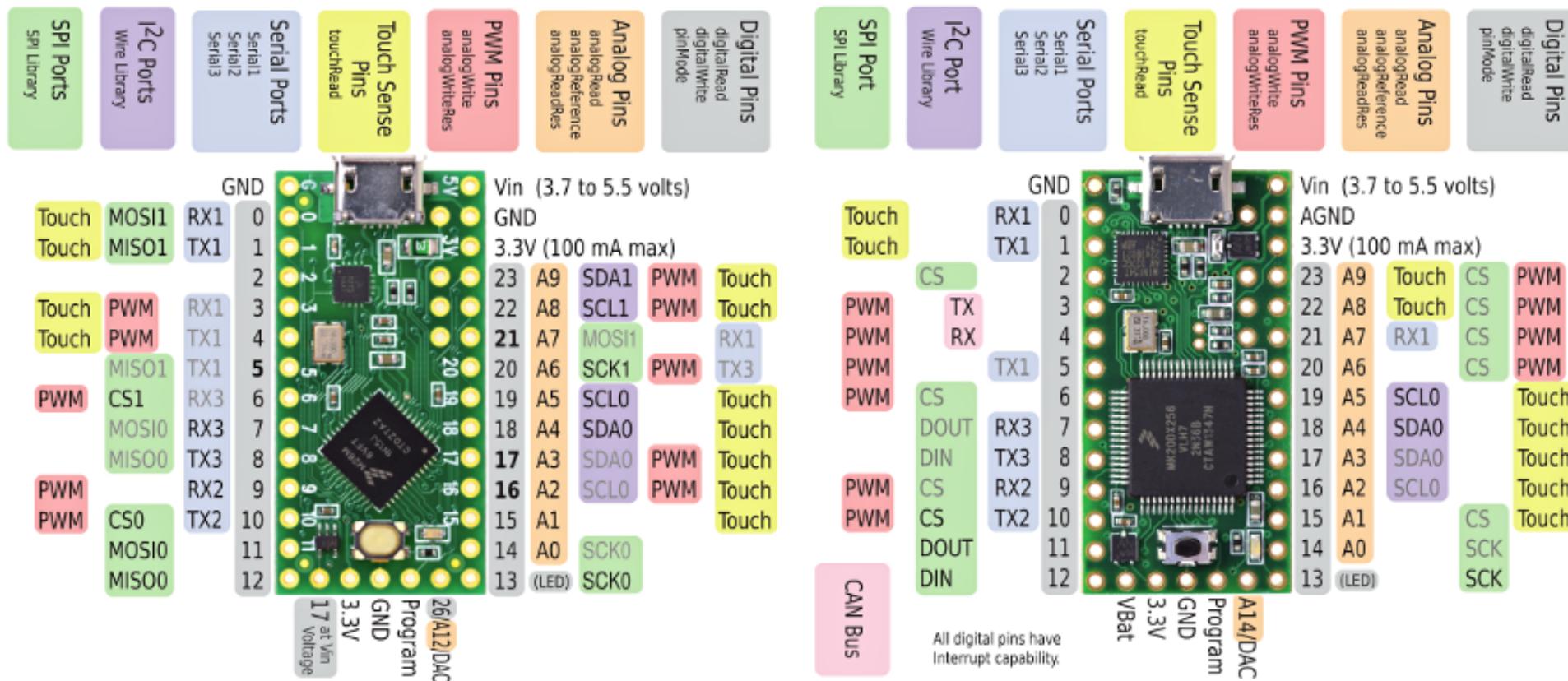
I²C Ports
Wire Library

SPI Ports
SPI Library



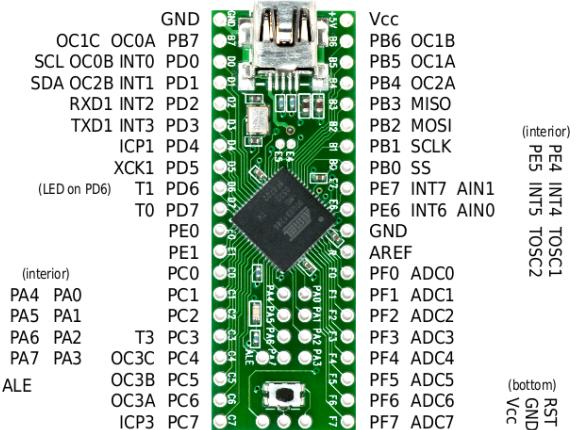
Teensy LC

Teensy 3.1

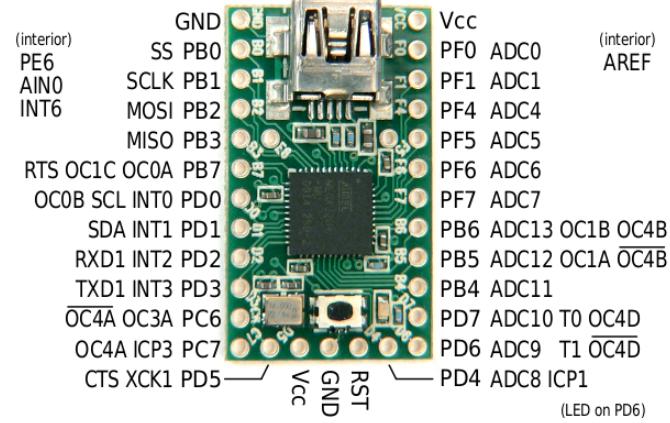
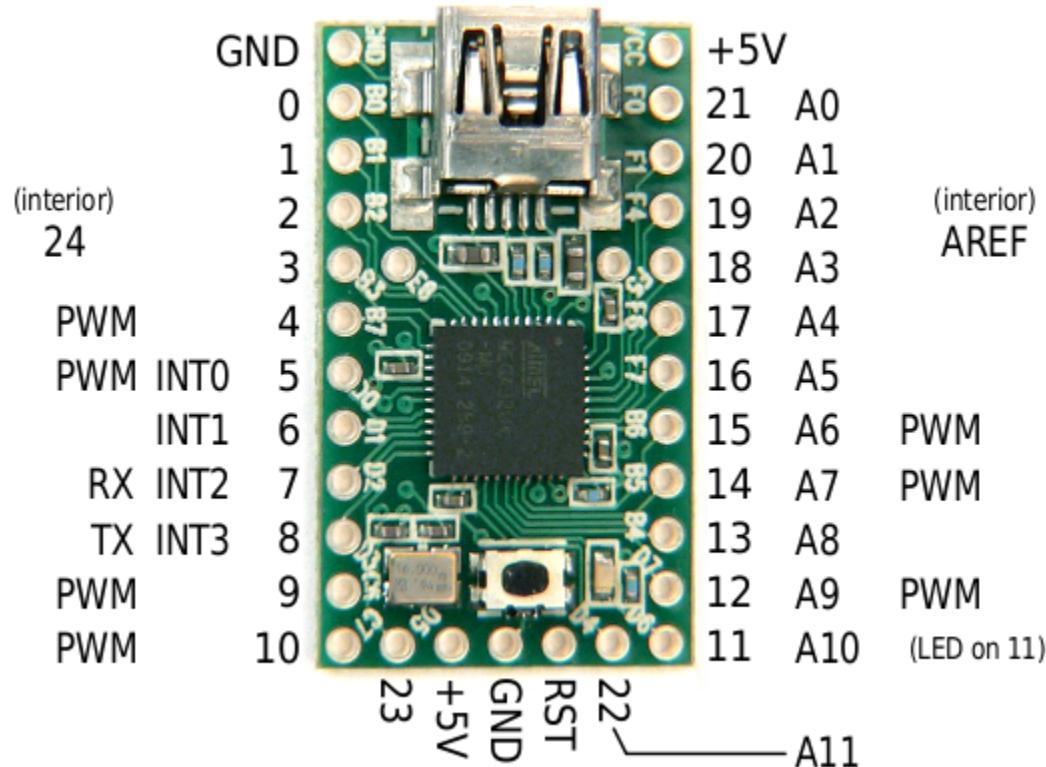


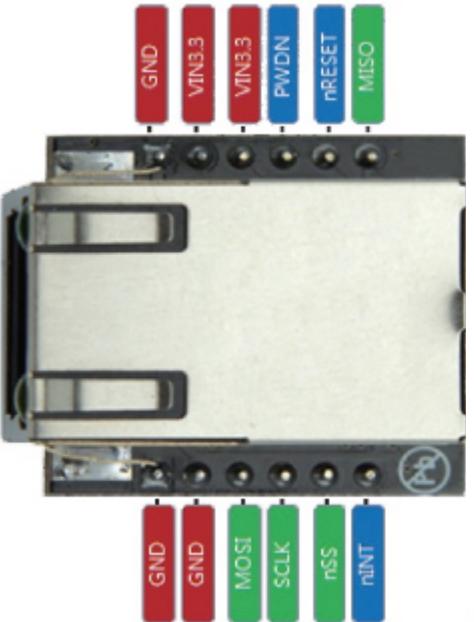
Teensy++ 2.0

	GND		+5V	
PWM	27		PWM	
PWM	INT0	0	PWM	
PWM	INT1	1	PWM	
RX	INT2	2		23
TX	INT3	3		22
		4		21
		5		20
(LED on 6)		6		19 INT7
		7		18 INT6
		8		GND
		9		AREF
(interior)		10		38 A0
32	28	11		39 A1
33	29	12		40 A2
34	30	13		41 A3
35	31	PWM	14	42 A4
		PWM	15	43 A5
		PWM	16	44 A6
			17	45 A7



Teensy 2.0





DIN CONNECTORS
Originally a European Audio Connector design, these DIN Connectors have found acceptance in computer and other electronic connector applications.

A combination of reliability and economy has prompted their widespread adoption.



Philmore Part #'s

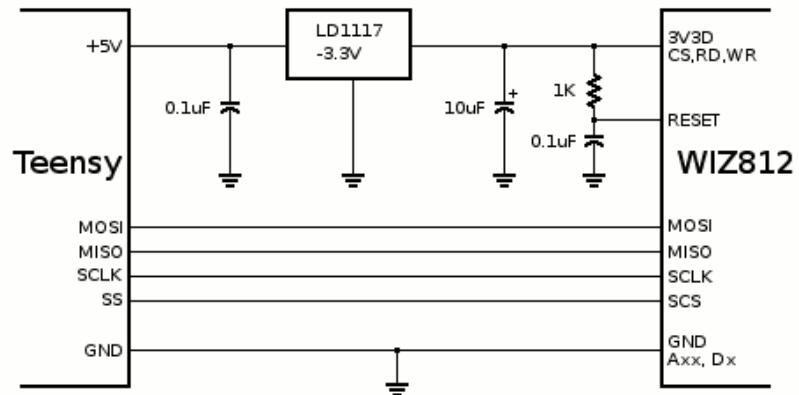
In-Line Male F10-A F10-B F10-C F10-D F10-E F10-F F10-G F10-H
Chassis Female E51 E55 E52 E53 E54 E59 E510 E511
In-Line Female EL1 EL5 EL2 EL3 EL4 EL9 EL10 EL11

TEENSY 3.1 / WIZ820io

WIRING DIAGRAM

TEENSY 3.1
PIN SIG
1 GND
2 3.3V
9 -
10 CS
11 DAT
12 DIN
13 SCK

WIZ820io
GND J2-1
3V3 J2-2
RESET J2-5
nSS J1-5
MOSI J2-3
MISO J2-6
SCLK J1-9



Pin Function	Teensy 1.0	Teensy 2.0	Teensy++ 1.0	Teensy++ 2.0	Teensy 3.0
MISO	11	3	23	23	12
MOSI	10	2	22	22	11
SCLK	9	1	21	21	13
SS	8	0	20	20	10

Case

