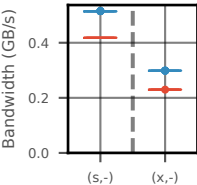
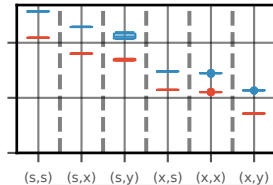


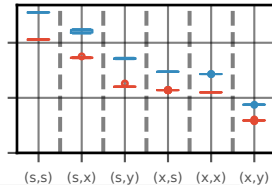
Interf. Cores = 0



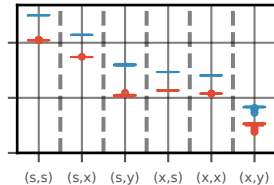
Interf. Cores = 1



Interf. Cores = 2



Interf. Cores = 3



Memory Targets: □ Obs + Int: BRAM □ Obs + Int: OCM