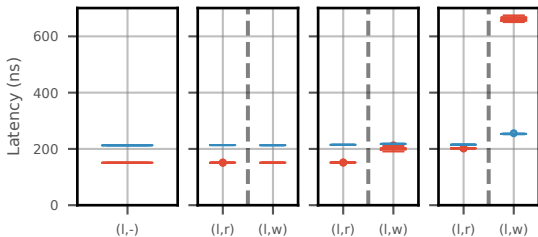


Interf. Cores = 0 Interf. Cores = 1 Interf. Cores = 2 Interf. Cores = 3



Memory Targets: ▭ Obs: DRAM, Int: PL-DRAM

▭ Obs: PL-DRAM, Int: DRAM

DRAM_PL-DRAM

PL-DRAM_DRAM

