MemoriaROM:memory\_rom Mux0 address[7..0] 2:0 SEL[2..0] saida[7..0] OUT 8'he3 DATA[7..0] Mux1 SEL[3..0] OUT 16'h13 DATA[15..0] Mux2 2:0 SEL[2..0] OUT 8'h10 DATA[7..0] Mux3 2,0 SEL[1..0] OUT 4'h2 DATA[3..0] Mux4 2:0 SEL[2..0] OUT 8'h14 DATA[7..0] Mux5 SEL[2..0] 2:0 OUT 8'h10 DATA[7..0] Mux6 SEL[2..0] 2:0 OUT 8'h2 DATA[7..0]