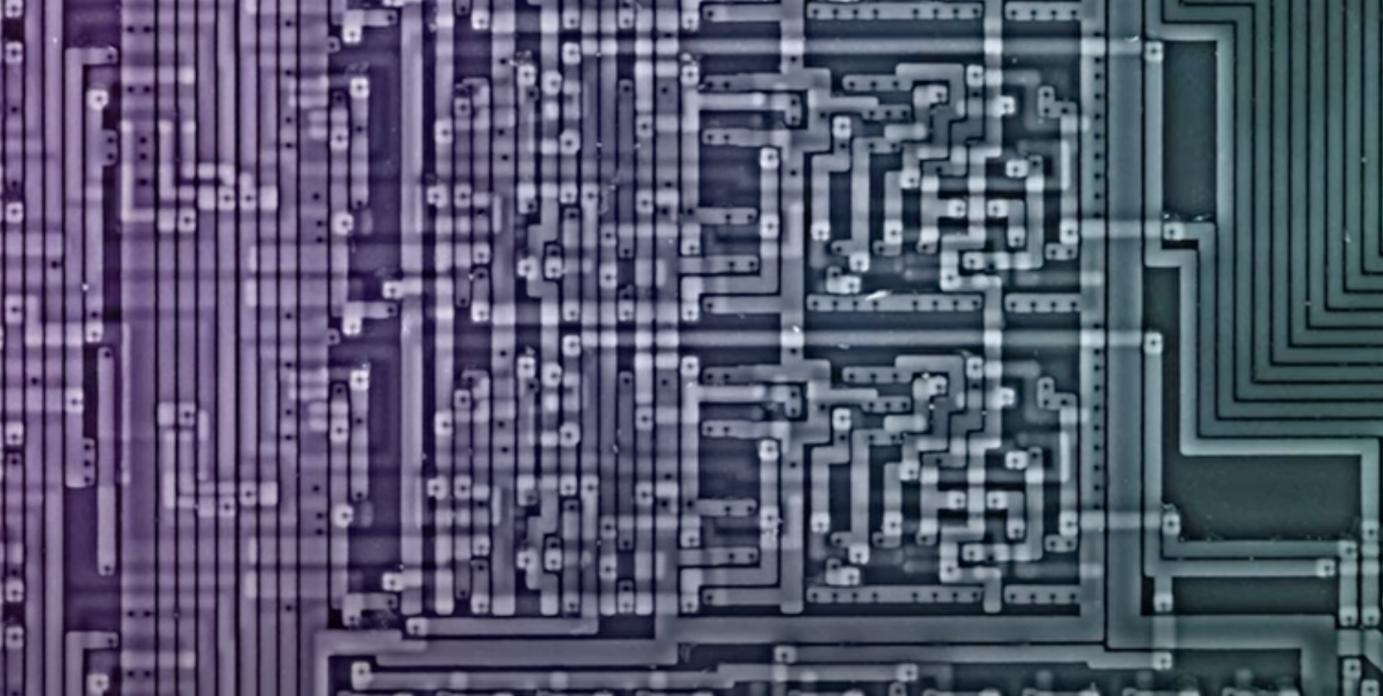
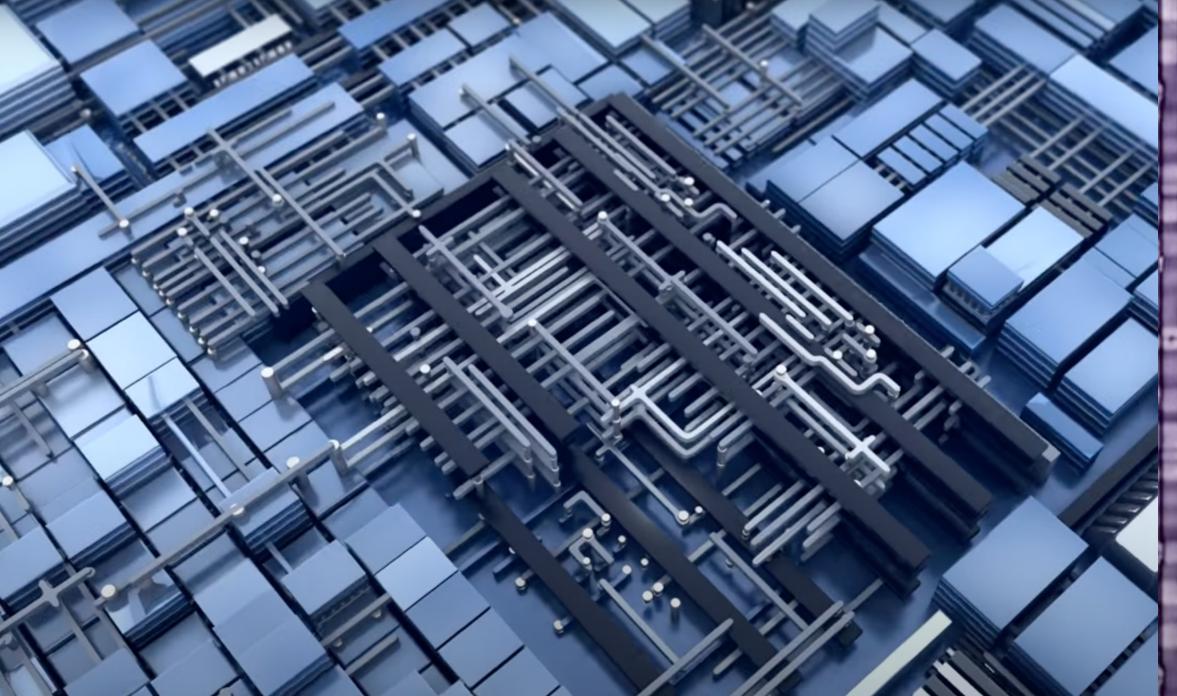
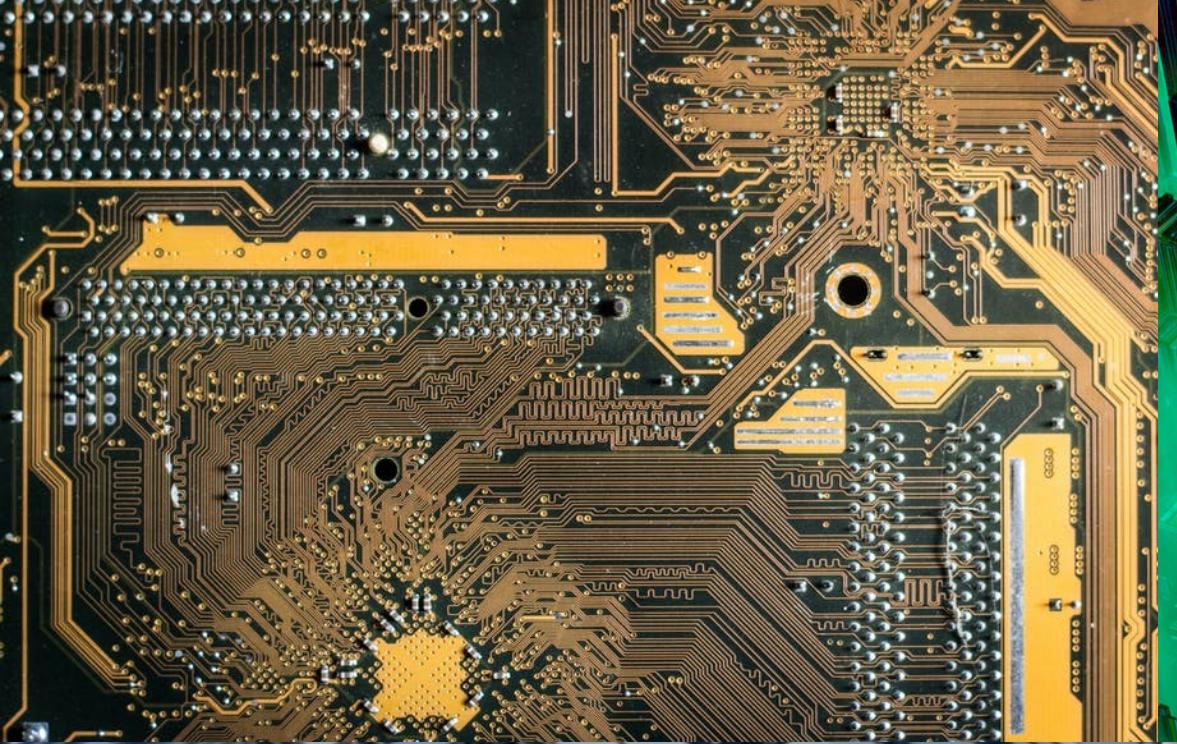
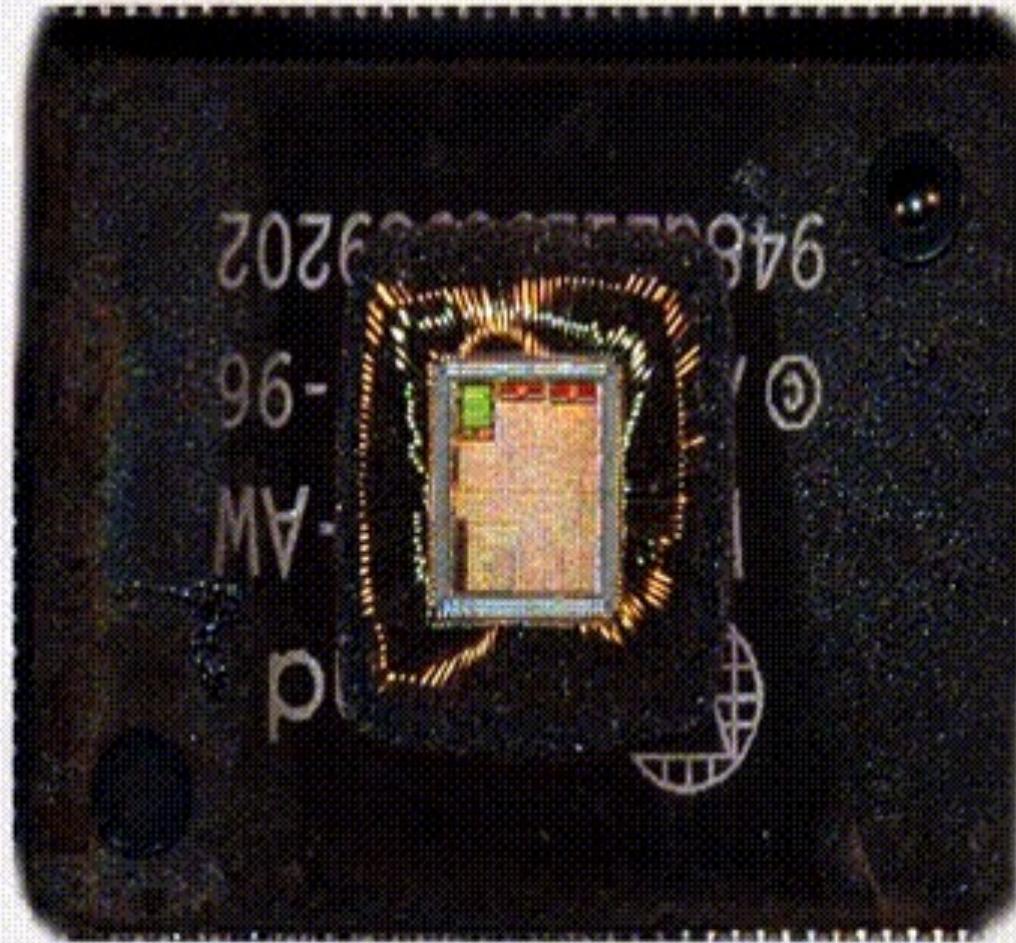
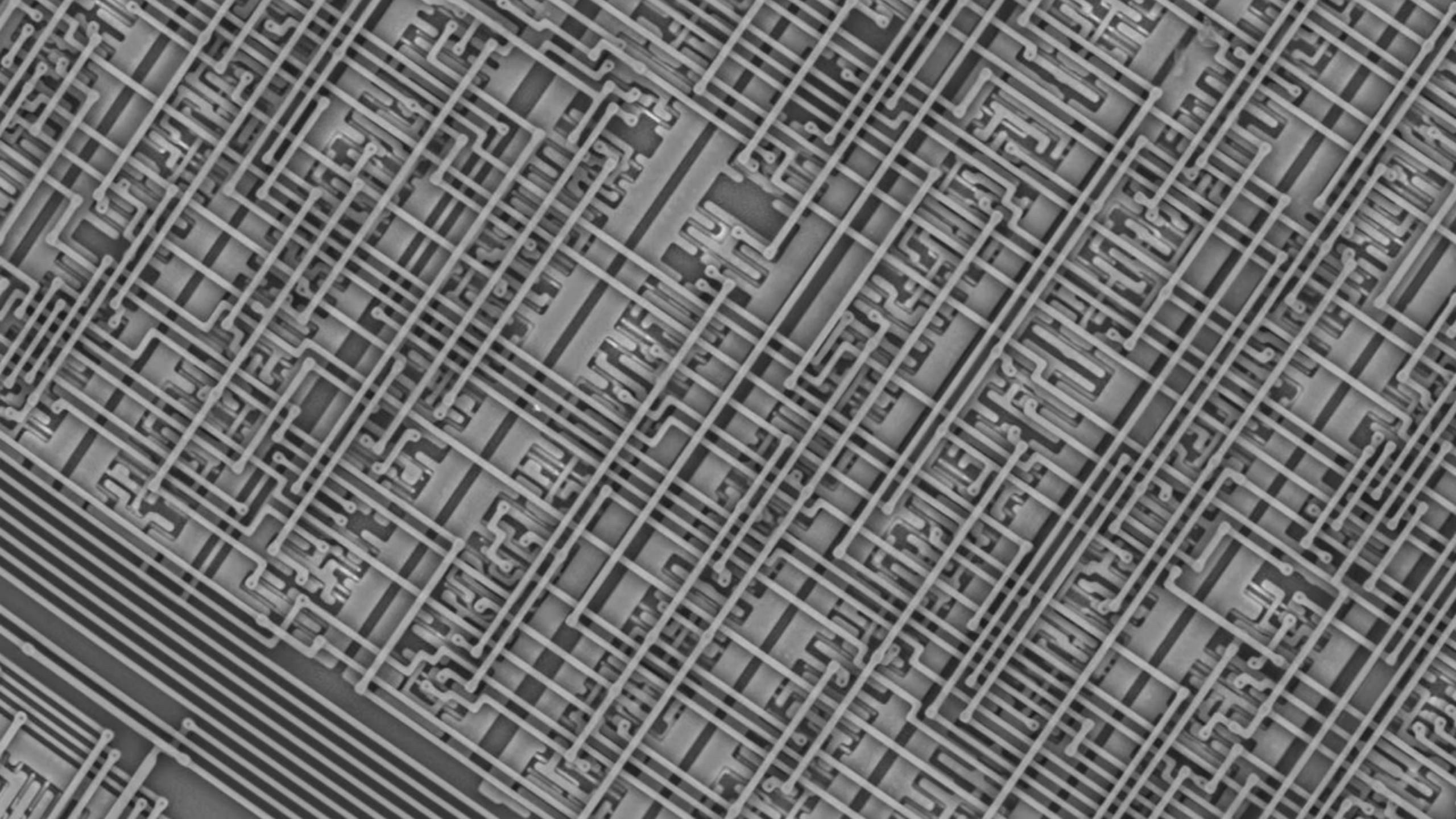


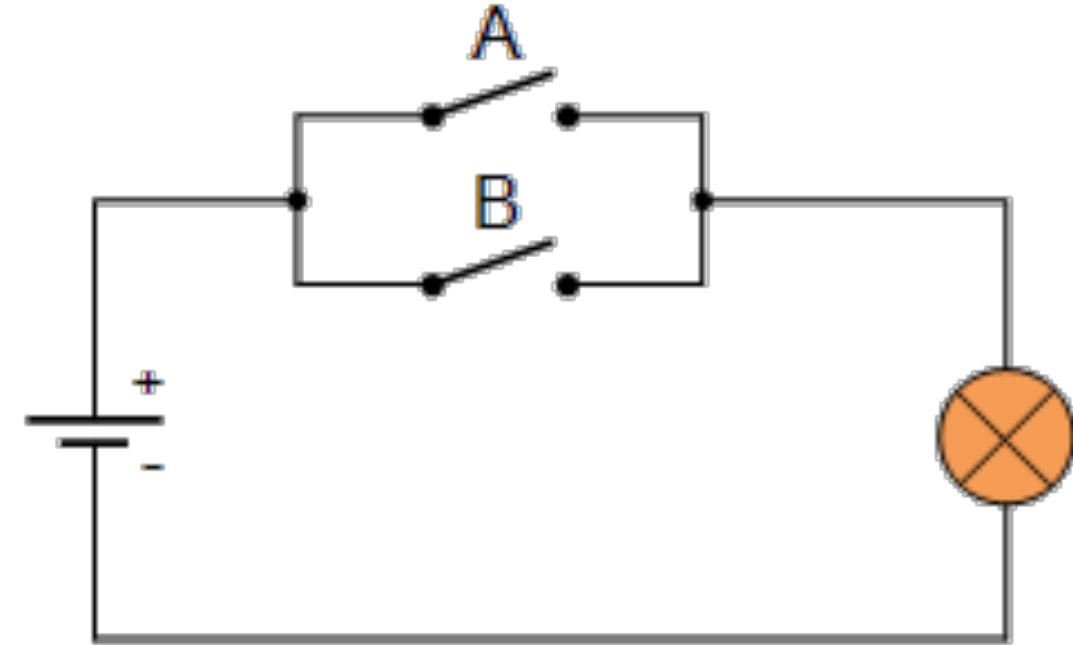
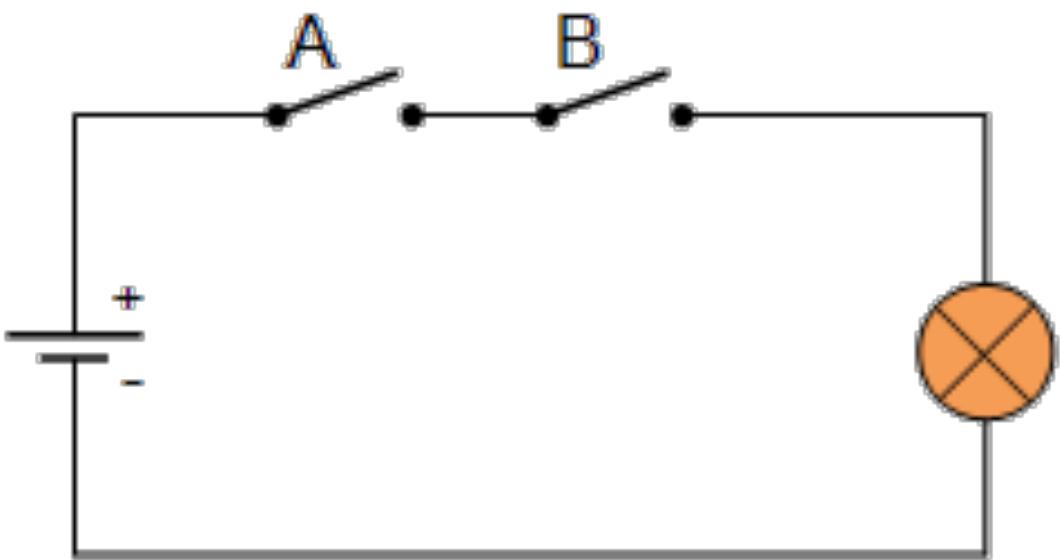
Logické obvody

Ing. Gabriel Valky, PhD.

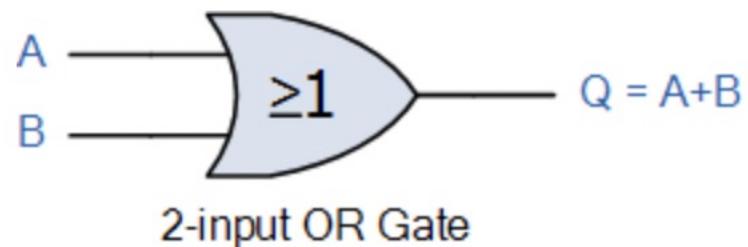




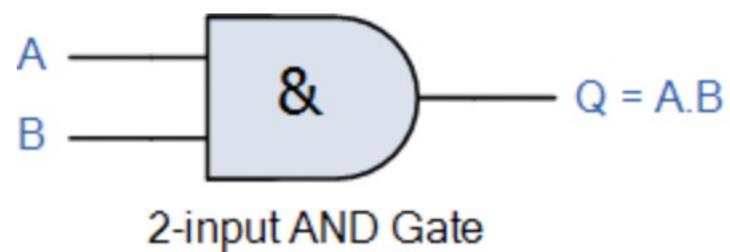


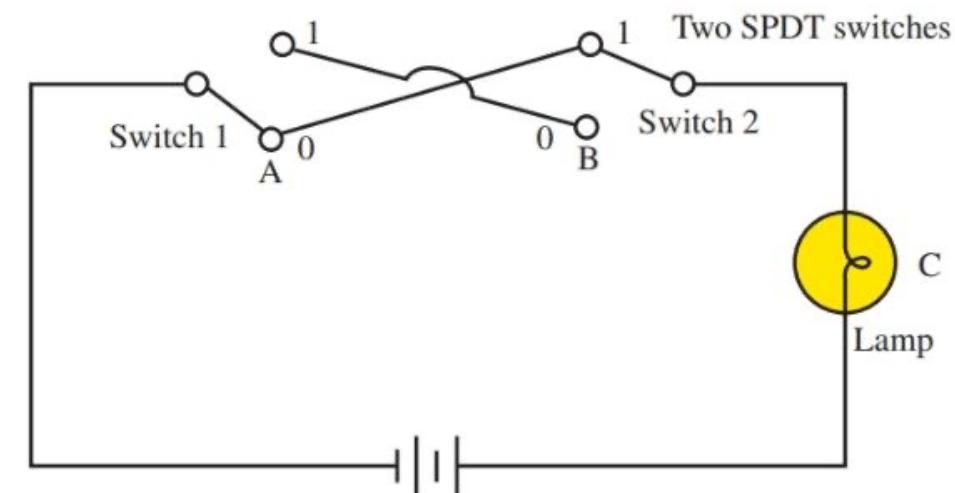
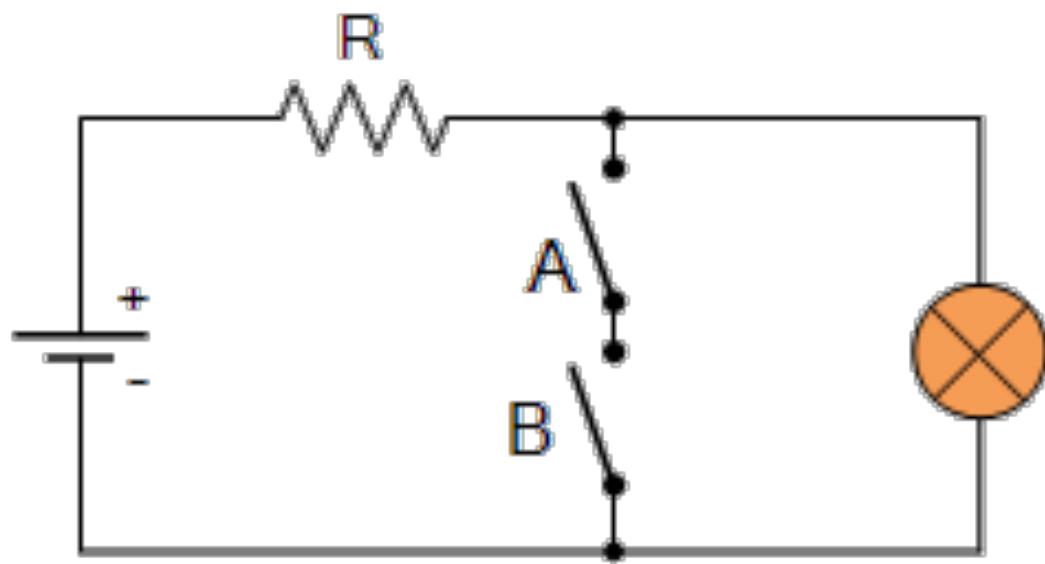
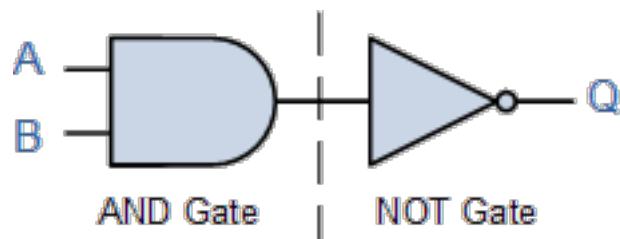


Switch A	Switch B	Output	Description
0	0	0	A and B are both open, lamp OFF
0	1	1	A is open and B is closed, lamp ON
1	0	1	A is closed and B is open, lamp ON
1	1	1	A is closed and B is closed, lamp ON
Boolean Expression (A OR B)		$A + B$	

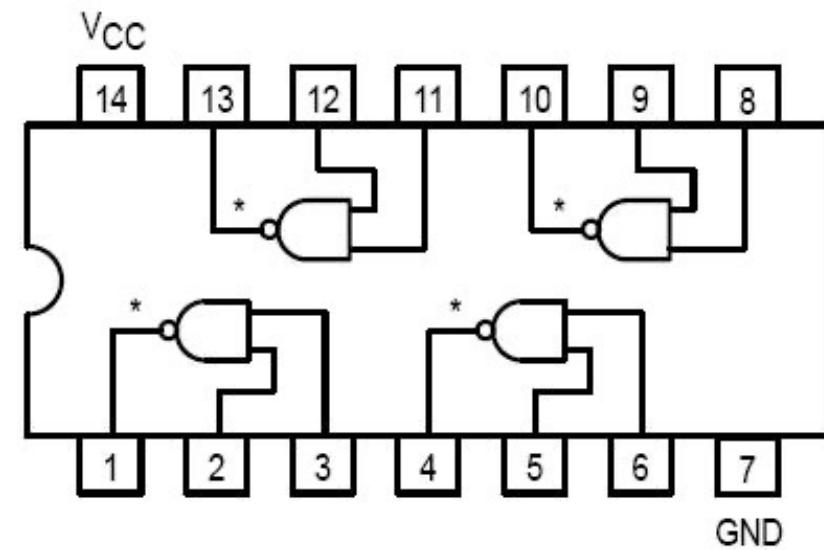
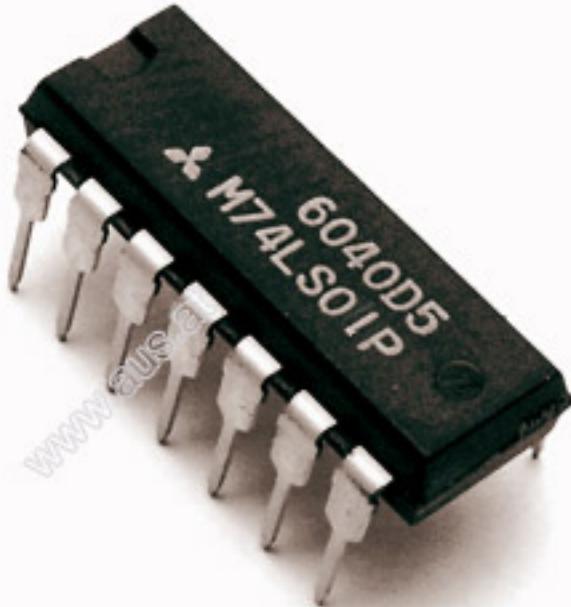


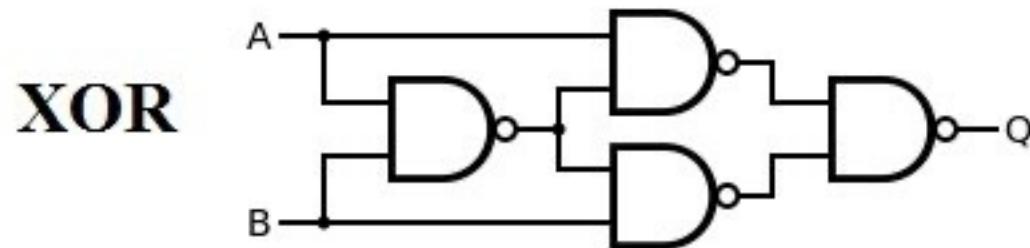
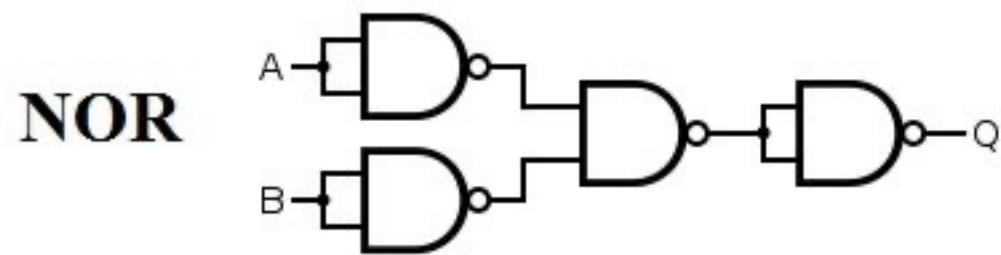
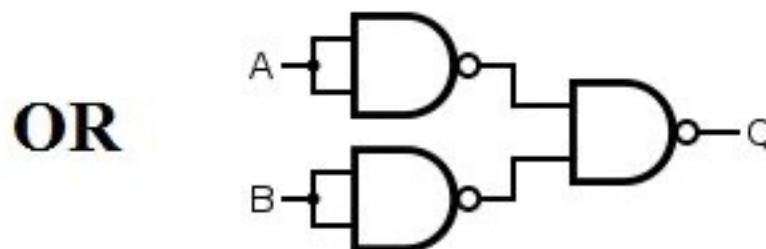
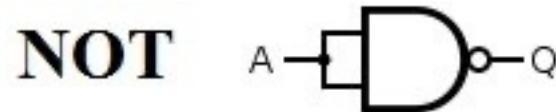
Switch A	Switch B	Output	Description
0	0	0	A and B are both open, lamp OFF
0	1	0	A is open and B is closed, lamp OFF
1	0	0	A is closed and B is open, lamp OFF
1	1	1	A is closed and B is closed, lamp ON
Boolean Expression (A AND B)		$A \cdot B$	





74LS01 Quad 2-input NAND Gate

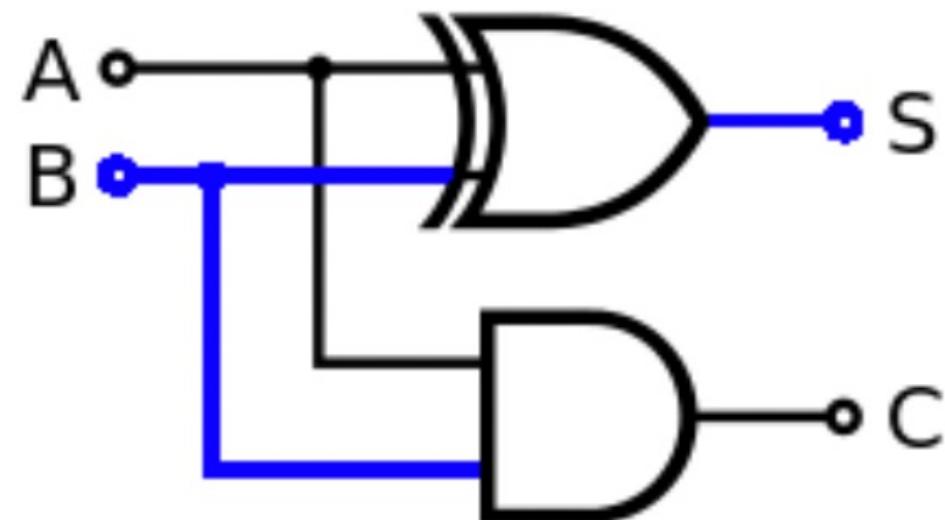




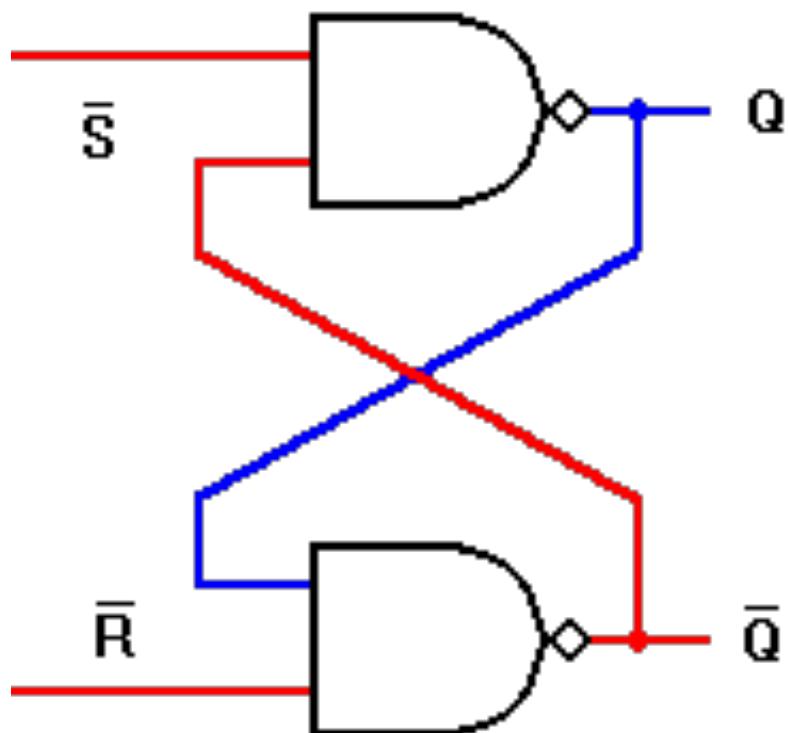
Kombinacne obvody: Half adder

A B Sum Carry

0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

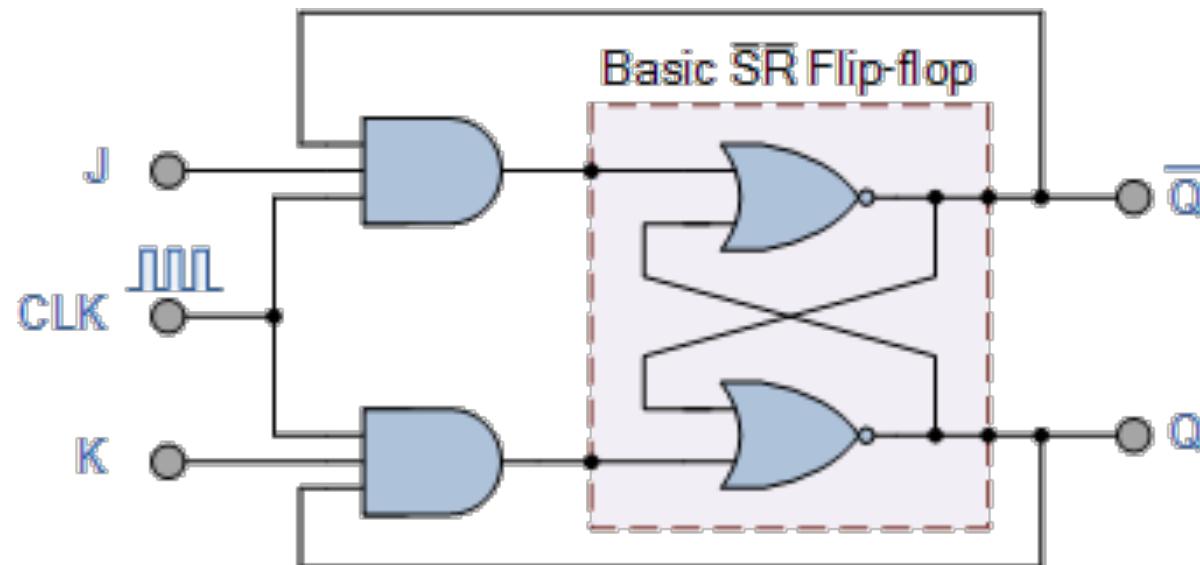


Klopne obvody: RS

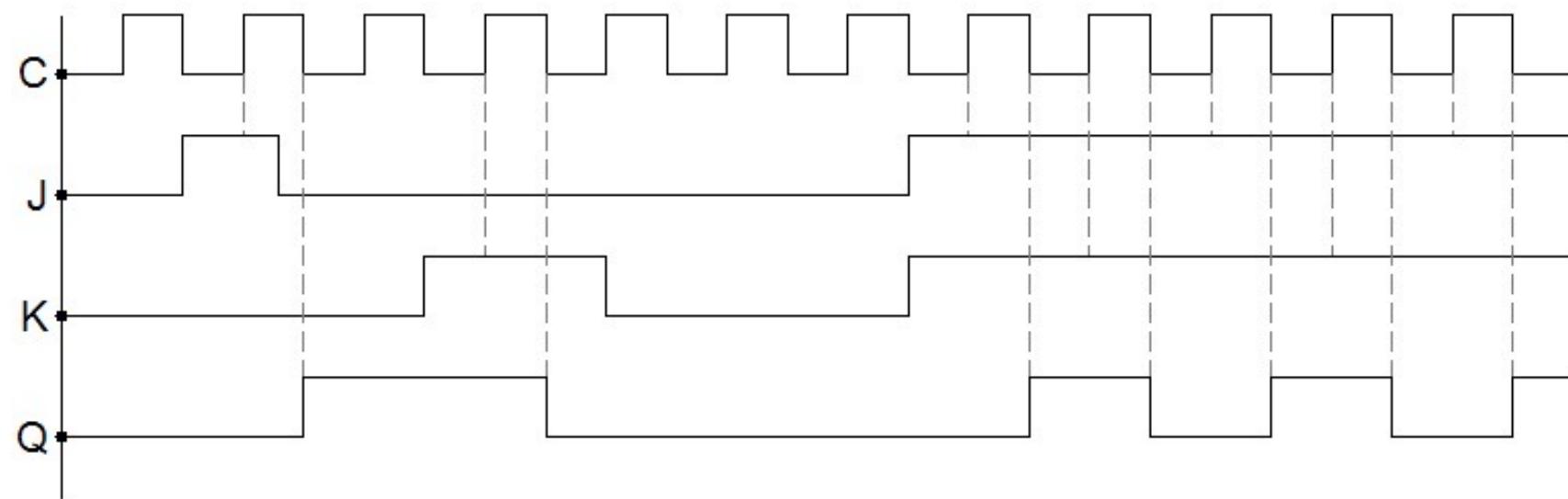


Sno	S	R	Q	Q'	State
1	1	0	1	0	Q is set to 1
2	1	1	1	0	No change
3	0	1	0	1	Q' is set to 1
4	1	1	0	1	No change
5	0	0	1	1	Invalid

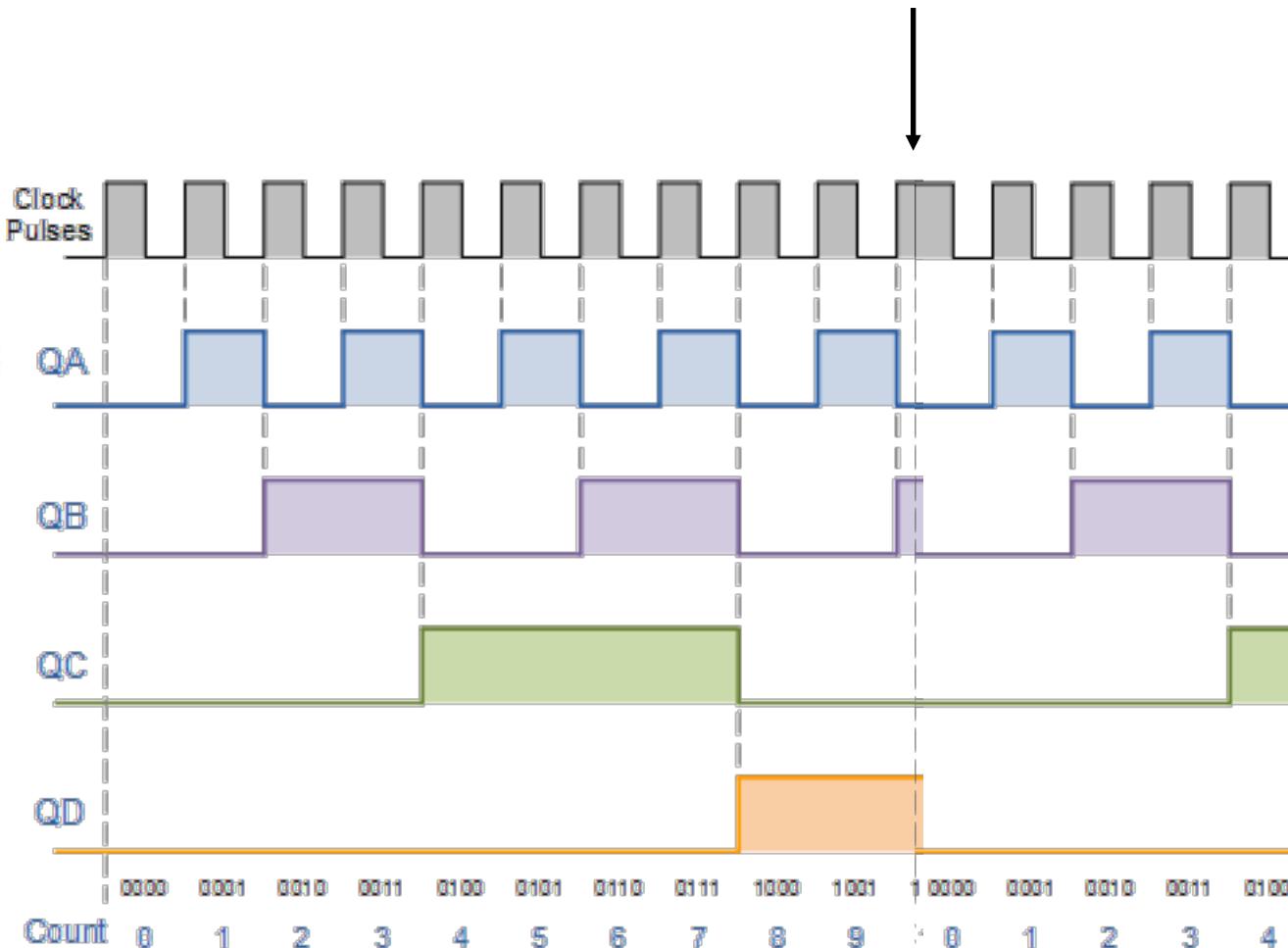
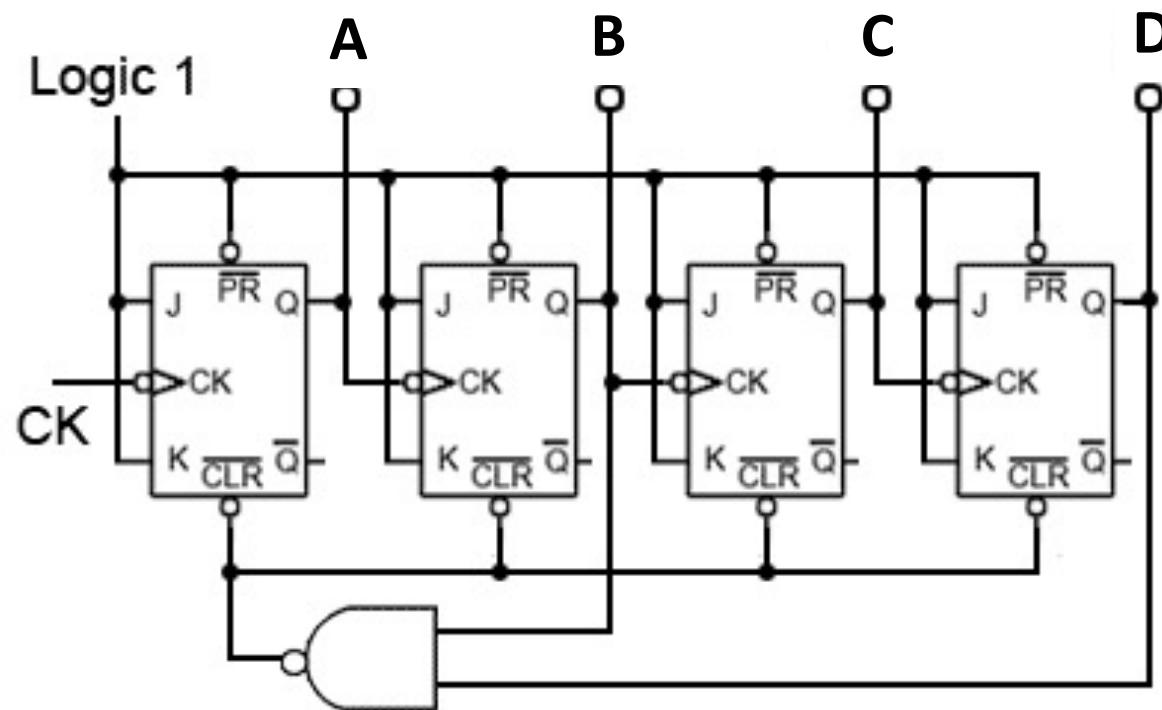
Klopy obvod: JK



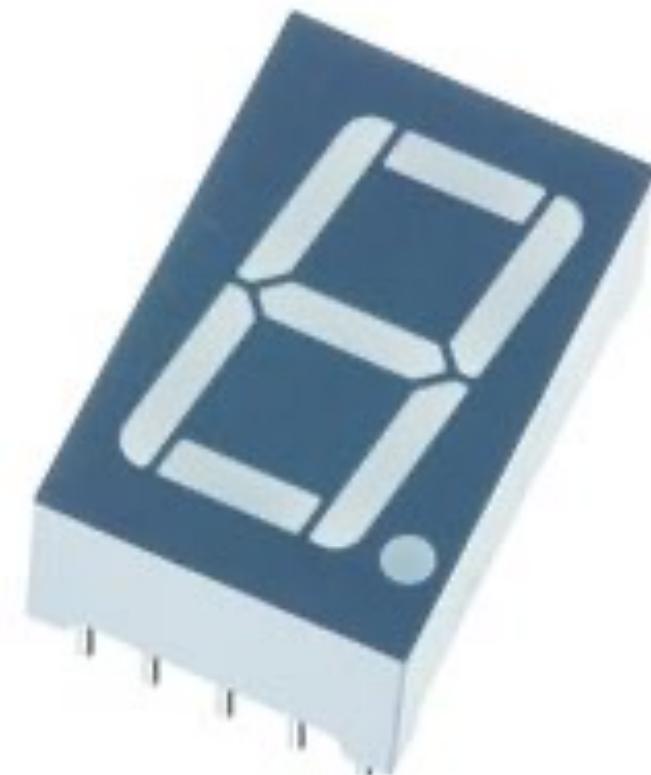
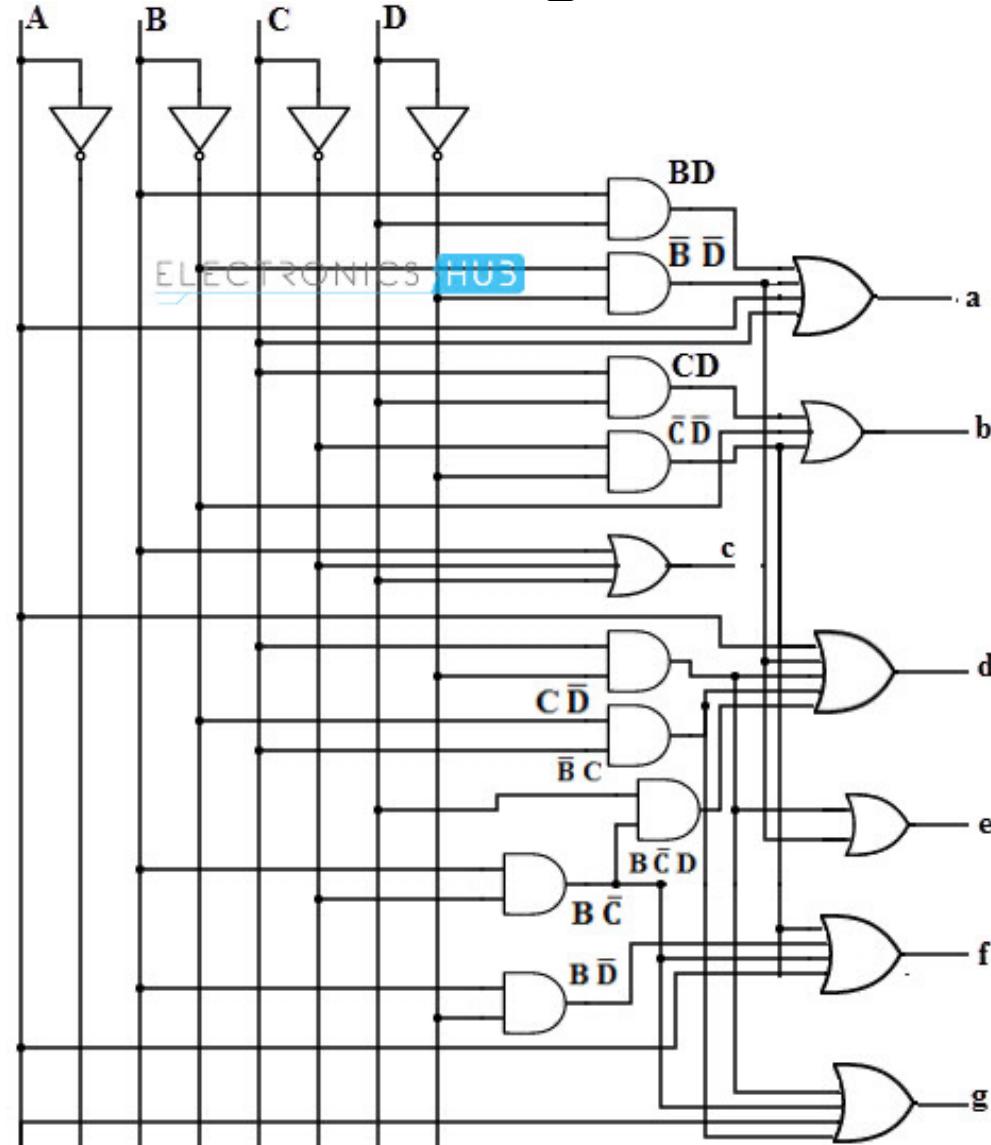
J	K	Q	\bar{Q}
0	0	Latch	Latch
0	1	0	1
1	0	1	0
1	1	Toggle	Toggle

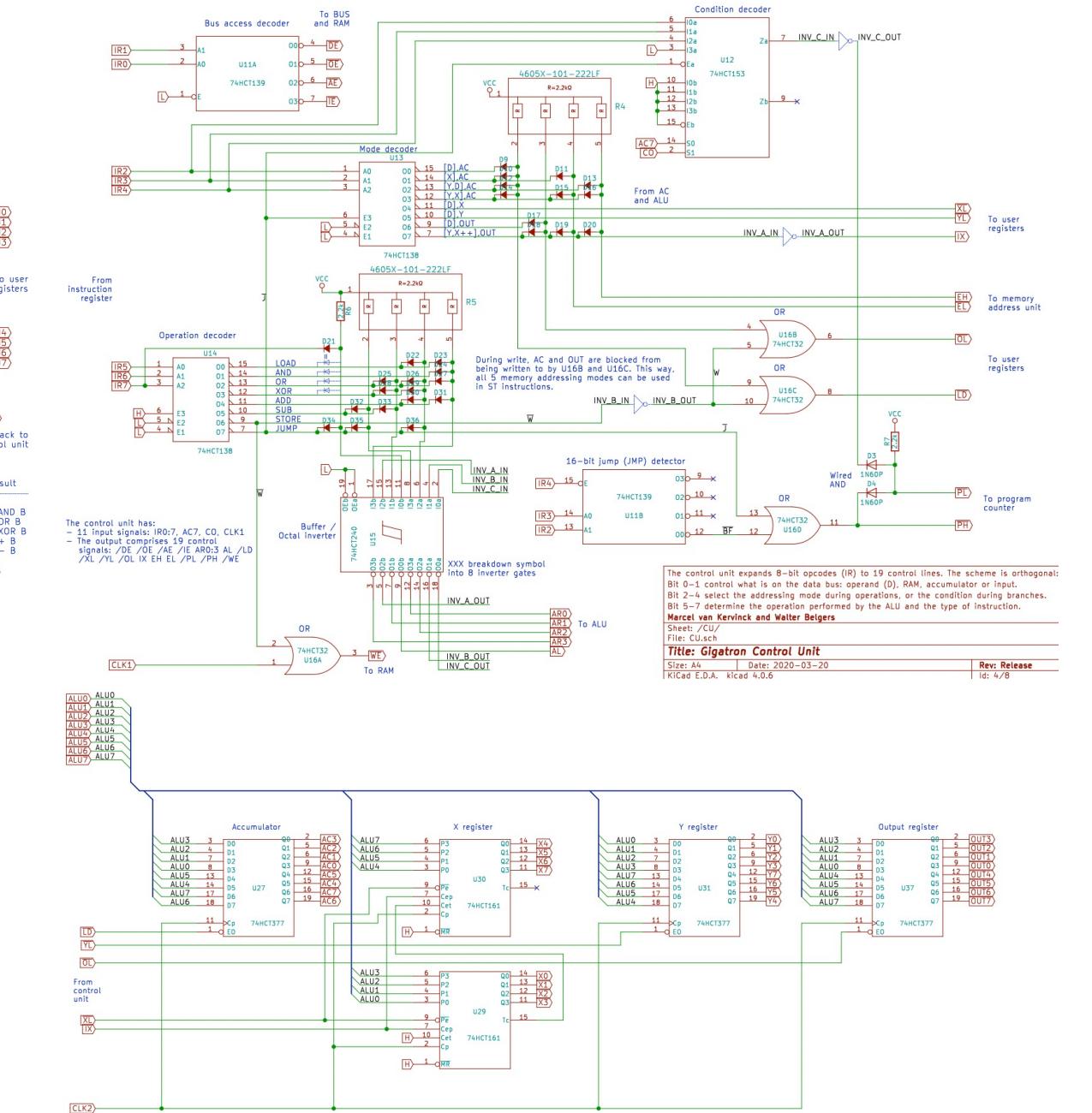
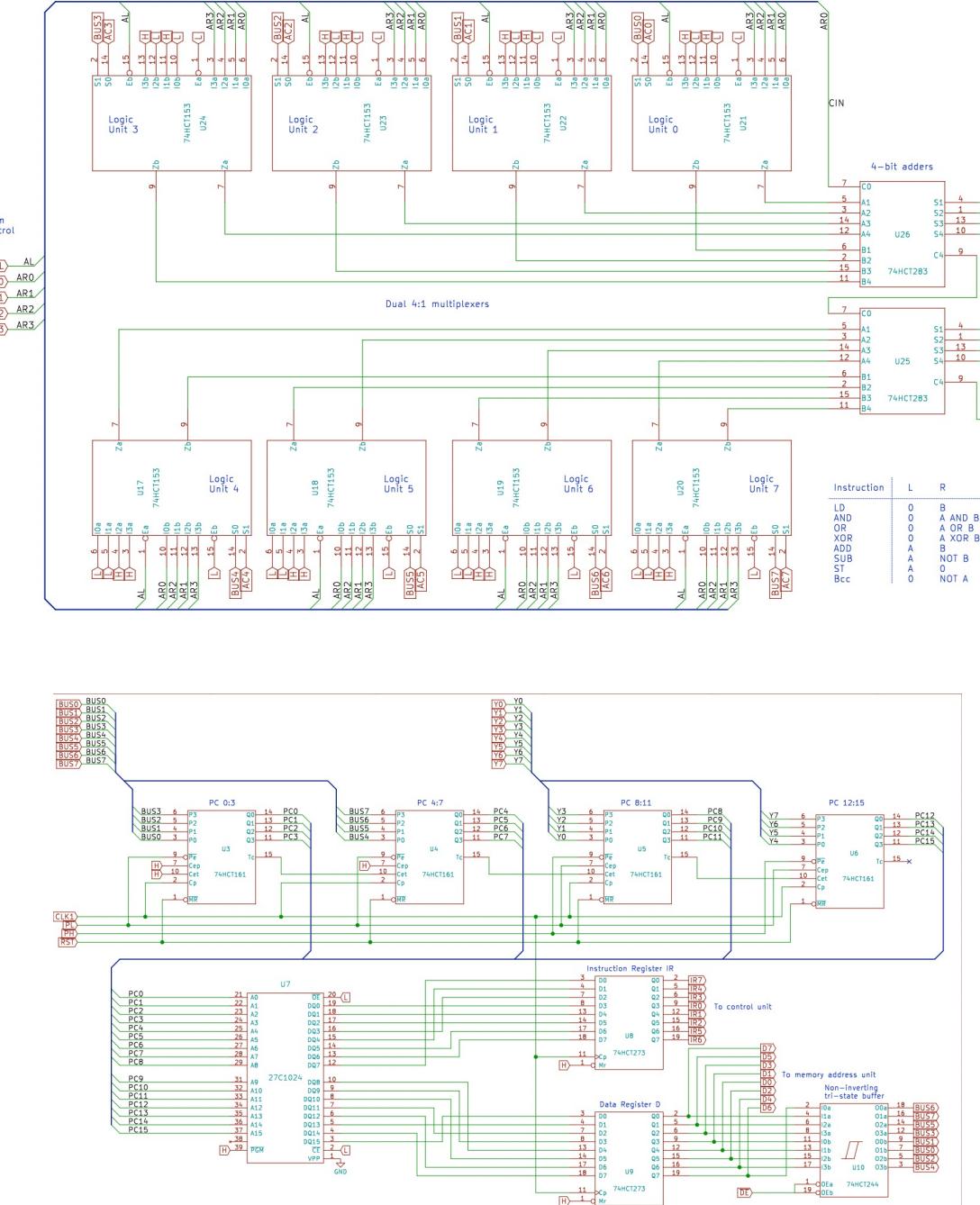


BCD Counter

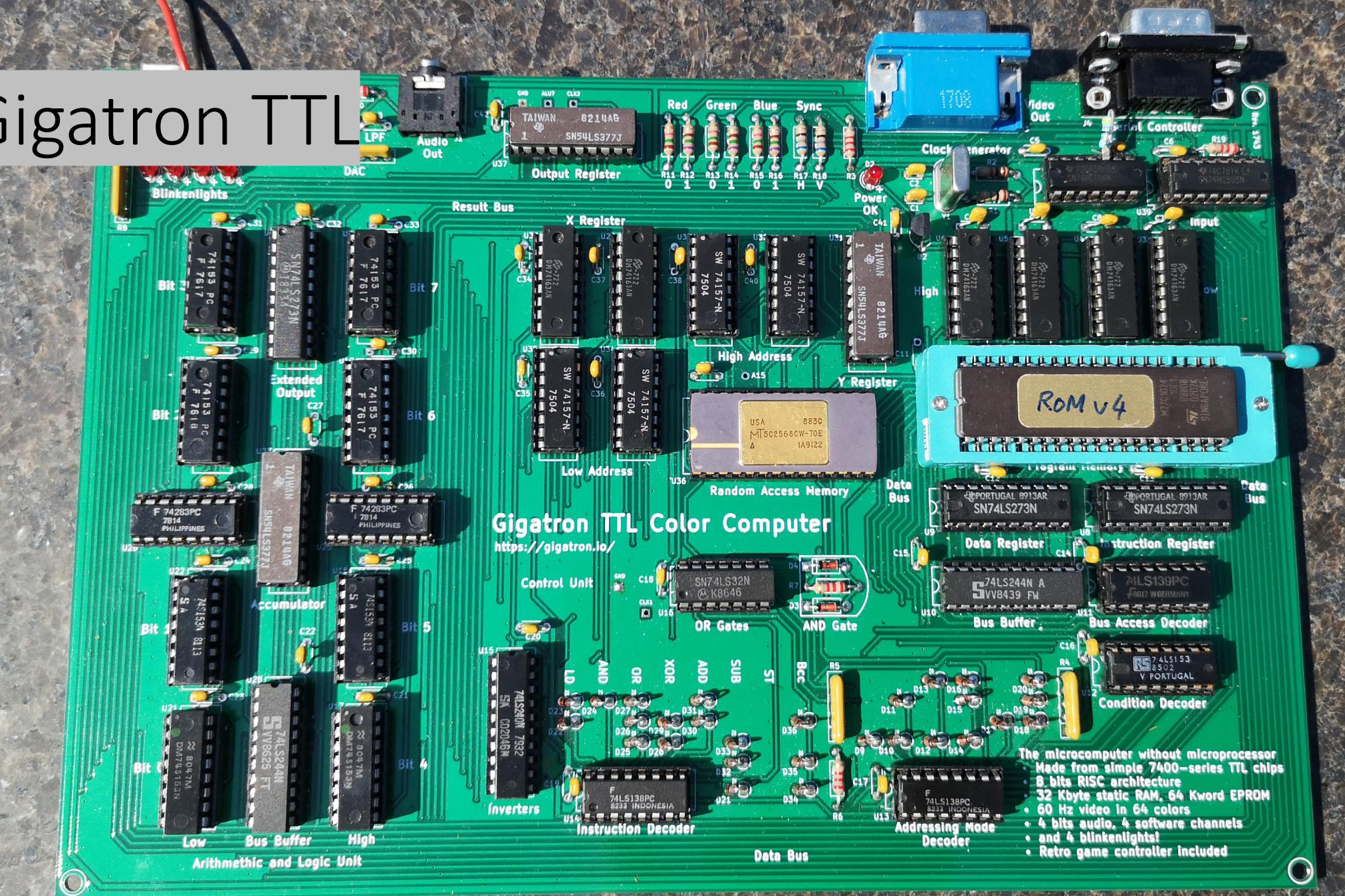


BDC to 7 segment decoder

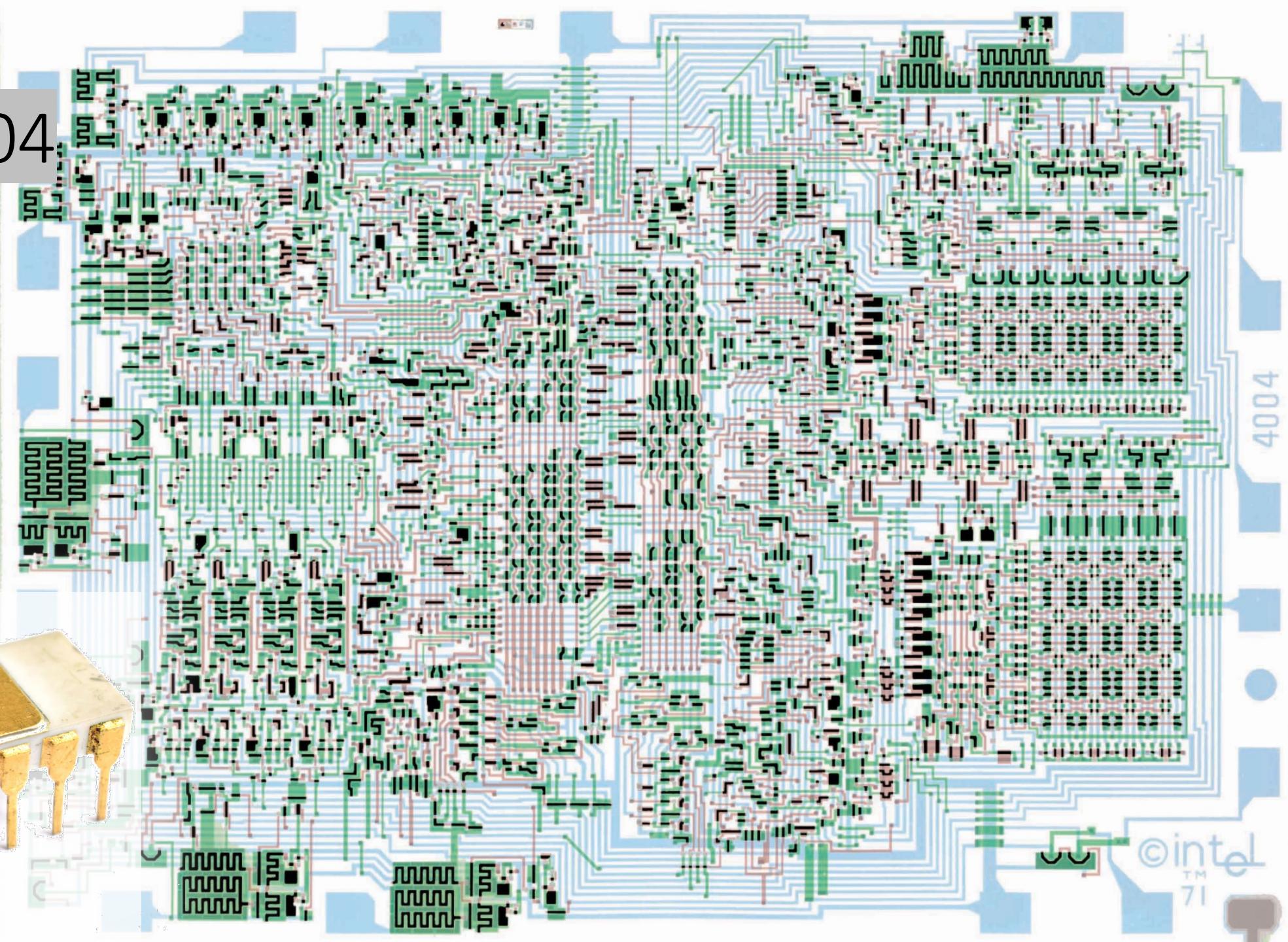
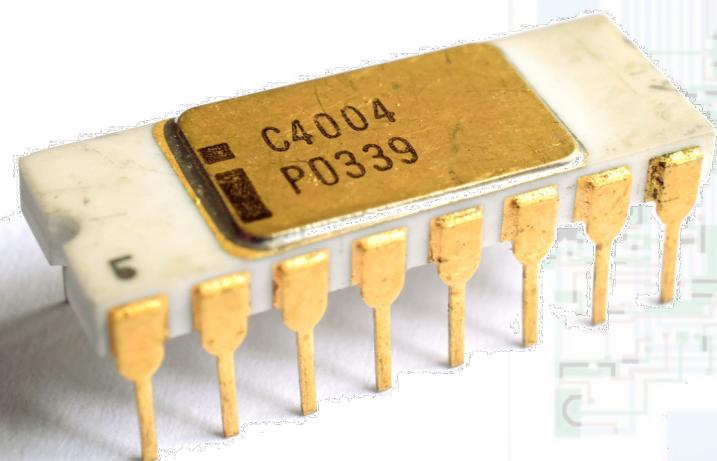


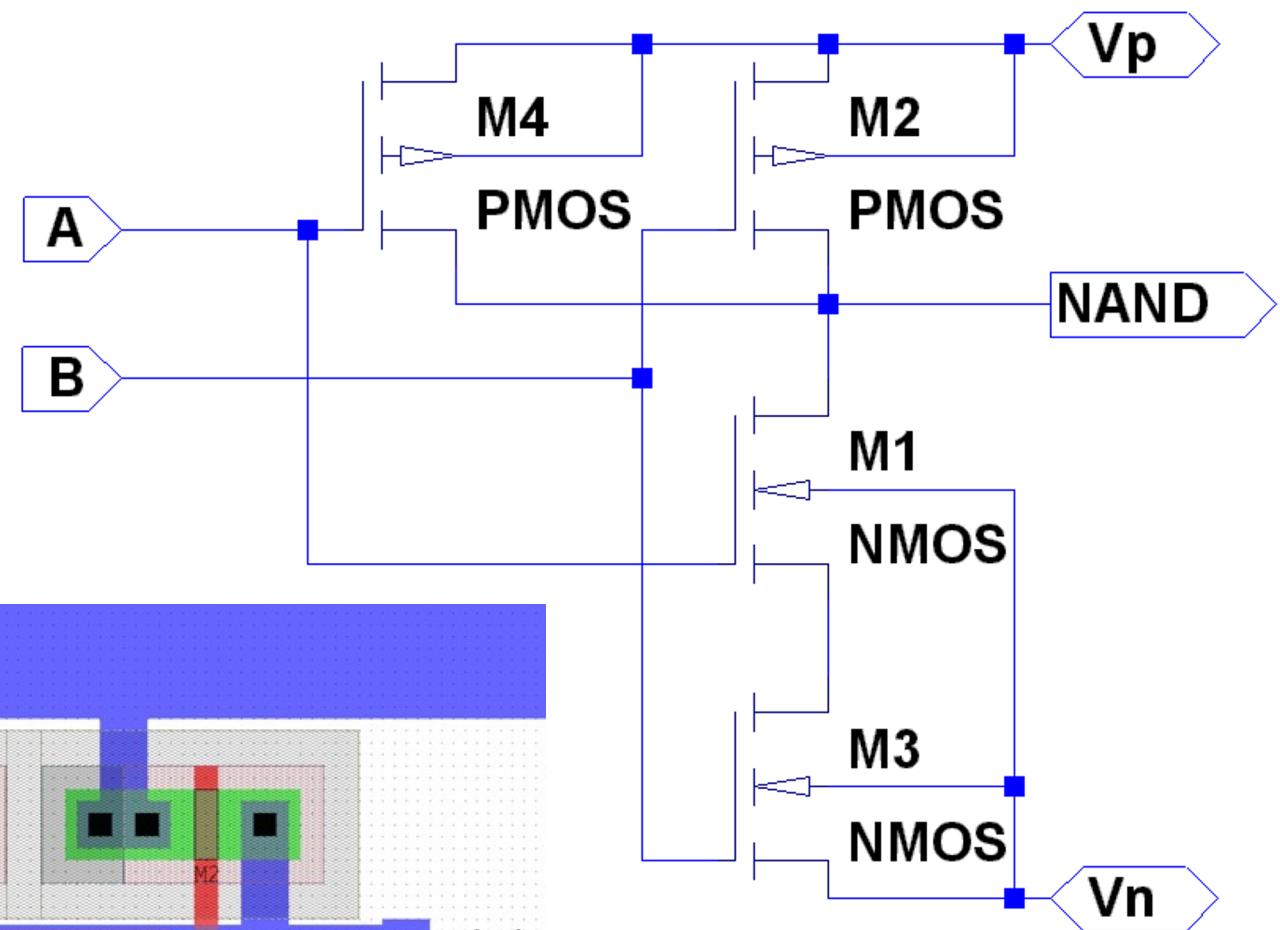
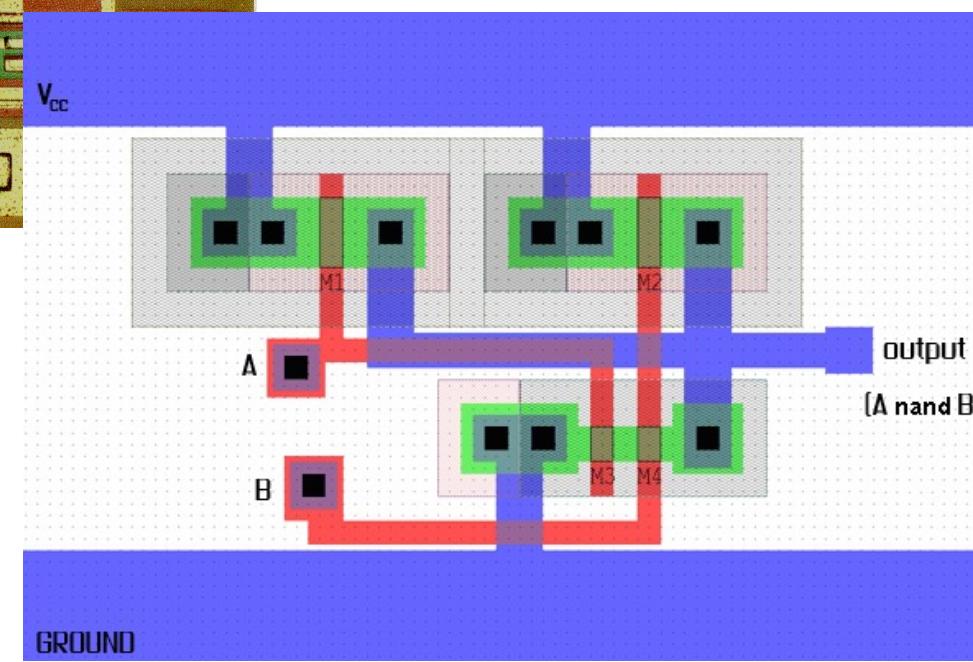
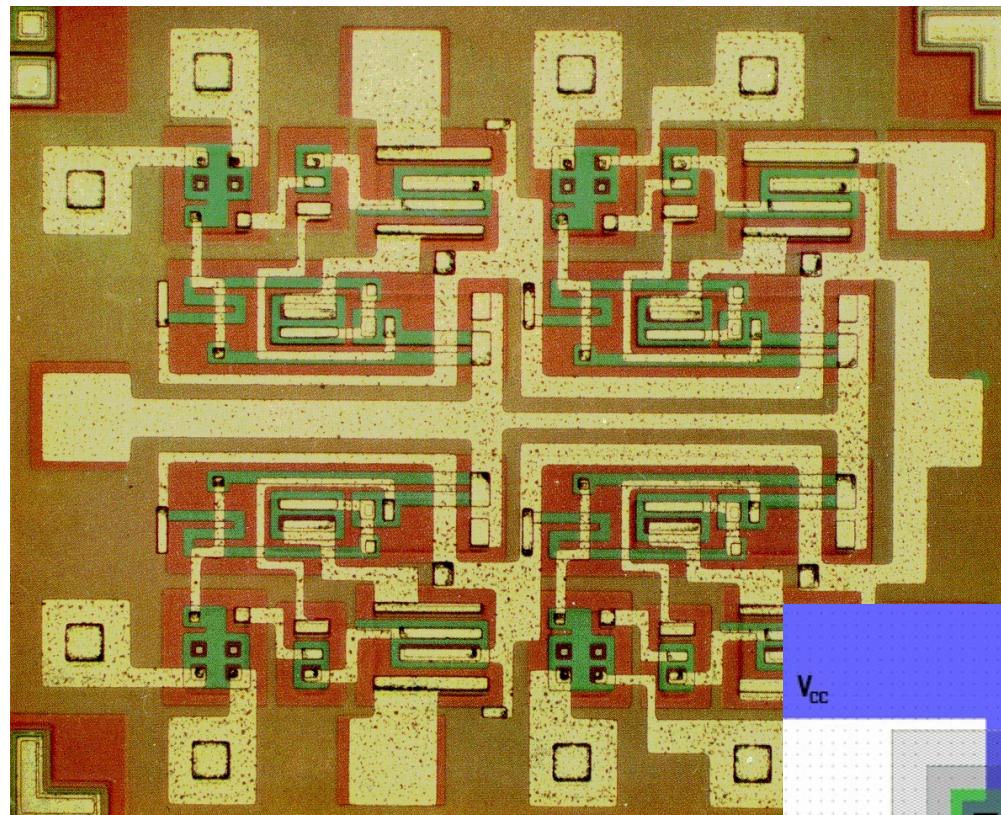


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