

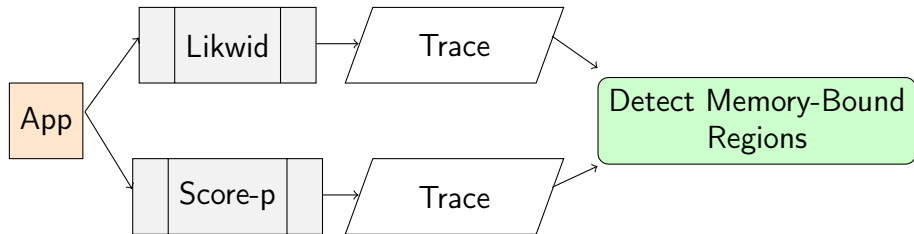
# Retrospective: September to December

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Porto Alegre, December 2016

# Methodology



# Use of the Likwid

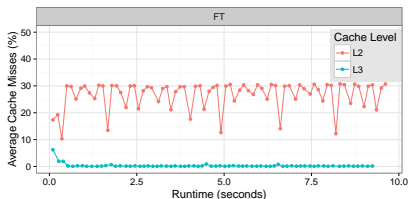


Figure: Sampling interval - 100 milliseconds<sup>[1]</sup>.

## Beagle1:

- 2 x Intel (R) Xeon (R) E5-2650 CPU 2.00 GHz
- 8 physical cores
- Hyper-Threading technology

[1] Moro, Gabriel and Schnorr, Lucas. *Measuring Hardware Counters for HPC Application Phase Detection*. XIV

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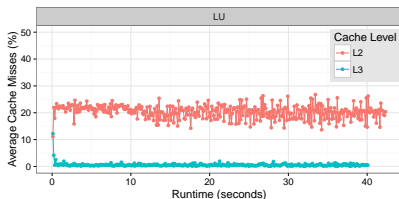


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