Gabriel de la parra

Electrical Engineer · Computer Scientist
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Education

M.Sc. in Computer Science

Universidad de Chile, Chile

2015 - Present

M.Sc. in Electrical Engineering

Automation, Measurement and Control

Technische Universität Dresden, Germany

2010-2011

M.Sc. in Electrical Engineering

Process Automation

Universidad de Santiago de Chile, Chile

2007-2009

B.Sc. in Electrical Engineering

Universidad de Santiago de Chile, Chile

2004-2007

Professional Experience

Research and Development Engineer

ABB S.A. • 2013 - Present • Chile

Power System Substation Automation, Protection and Control Division

Substation Electrical Drawings Automation Tools

Project Documentation Automation Tools

IEDs Configuration Automation Tools

SCADA Configuration Automation Tools

Team IT Infraestructure Productivity Tools

Project Engineer

ABB S.A. • 2011 - Present • Chile

Power System Substation Automation, Protection and Control Division

Control and Protection Schemes design and development

Substation's Electrical Drawings Design and Draft

Project Documentation design and development

Control and Protection Series IEDs configuration

Technical meetings with customers

Research and development Intern

Parker Hannifin GmbH • 2010 - 2011 • Germany

Europe's SensoControl Division: USB - IOLink interface prototype

Circuit/PCB design, construction and testing.

System Design based on the IOLink Standard.

Hardware and software design, construction, programming, testing.

COM Wrappers for unmanaged C libraries.

LABView interface design and programming for device's production chain.

Prototypes HMI simulators design and develpment for analysis and testing.

Project documentation.

Computer Skills

Office   Word, Excel, PowerPoint, VisualStudio, Visio, Project

Languages   C#, Java, LaTeX, HTML/XML, Matlab, VBS, Assembler