Gabriel de la Parra

ELECTRICAL ENGINEER · COMPUTER SCIENTIST

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Education

MSc. Computer Science

Universidad de Chile Chile 2015 - Present

MSc. Electrical Engineering

Automation and Control Technische Universität Dresden Germany 2010-2011

MSc. Electrical Engineering

Process Automation Universidad de Santiago de Chile Chile 2007-2009

BSc Electrical Engineering

Universidad de Santiago de Chile Chile 2004-2007

Computer Skills

Programming

C#, Java, IATEX, HTML/XML, Matlab, VBS, Assembler, SQL,

Office

Access, VisualStudio, Visio, Project

Languages

Spanish English

German

Professional Experience

Research and Development Engineer

ABB S.A. • 2013 - Present • Chile

Power System Substation Automation, Protection and Control Division

Automation Tools for Productivity Improvement

Substation Electrical Drawings
Project Documentation
IEDs Configuration
SCADA Configuration
Team Infraestructure Productivity

Project Engineer

ABB S.A. • 2011 - Present • Chile

Power System Substation Automation, Protection and Control Division

Projects Responsabilities

Control and Protection Schemes design and development Substation's Electrical Drawings Design and Draft Project Documentation design and development Control and Protection Series IEDs configuration Technical meetings with customers

Research and Development Intern

Parker Hannifin GmbH • 2010 - 2011 • Germany Europe's Senso Control R & D Division

USB - IOLink interface prototype

Circuit/PCB design, construction and testing.
System Design based on the IOLink Standard.
Hardware and software design, construction, programming, testing.
COM Wrappers for unmanaged C libraries.
LABView interface design and programming for device's production chain.
Prototypes HMI simulators design and development for analysis and testing.
Project documentation.

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November 2015