# Gabriel de la parra

ELECTRICAL ENGINEER · COMPUTER SCIENTIST

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# Professional Experience

# Research and Development Engineer

ABB S.A. • 2013 - Present • Chile

Power System Substation Automation, Protection and Control Division

#### Software Tools for Quality & Productivity Improvement

Substation Electrical Drawings
Project Documentation
IEDs Configuration
SCADA Configuration
Team IT Infraestructure Productivity

#### Hardware Devices for Testing Productivity Increase

Binary Input Interface SwitchGear Primary Equipment Simulators

#### Global Products and Technologies Key User

Zuken's E<sup>3</sup> Series ABB Internal CyberSecurity

# Project Engineer

ABB S.A. • 2011 - Present • Chile

Power System Substation Automation, Protection and Control Division

#### Projects Responsabilities

Control and Protection Schemes design and development Substation's Electrical Drawings Design and Draft Project Documentation design and development Control and Protection Series IEDs configuration Technical meetings with customers

### Research and Development Intern

Parker Hannifin GmbH • 2010 - 2011 • Germany Europe's Senso Control  $R \ensuremath{\mathfrak{C}} D$  Division

#### USB - IOLink interface prototype

Circuit/PCB design, construction and testing System Design based on the IOLink Standard Hardware and software design, construction, programming, testing LABView interface design and programming for device's production chain Prototypes HMI simulators design and development for analysis and testing

#### References

#### Ronald Valdés

 $\begin{array}{c} Product\ Group\ Manager\ Chile\\ ronald.valdes@cl.abb.com \end{array}$ 

#### Rafael Pedrucci

PSAC Hub Manager Latam rafael.pedrucci@cl.abb.com

## Education

#### MSc. Computer Science

Universidad de Chile Chile 2015 - Present

### MSc. Electrical Engineering

Automation and Control Technische Universität Dresden Germany 2010-2011

#### MSc. Electrical Engineering

Process Automation Universidad de Santiago de Chile Chile 2007-2009

### **BSc Electrical Engineering**

Universidad de Santiago de Chile Chile 2004-2007

# Computer Skills

#### Programming

C#, Java, IATEX, HTML/XML, XAML/WPF, MVVM, Trello, Github, Sharepoint, SQL, EPPlus, Office Interop, OpenXML, AForge.Net, Matlab, VBScript, Assembler,

#### Office

Word, Excel, PowerPoint, Visio, VisualStudio, Access, Project

# Languages

Spanish English

German