Gabriel de la parra

ELECTRICAL ENGINEER · COMPUTER SCIENTIST

ignacio.delaparra@gmail.com
+56 9 622 611 03
Github / Pluralsight: gabrieldelaparra

Education

MSc. Computer Science

Universidad de Chile
Chile
2015 - Dec. 2017

MSc. Electrical Engineering

Automation and Control
Technische Universität Dresden
Germany
2010 - 2011

MSc. Electrical Engineering

Process Automation
Universidad de Santiago de Chile
Chile
2007 - 2009

BSc Electrical Engineering

Universidad de Santiago de Chile
Chile
2004 - 2007

Computer Skills

Programming

C#, Java, LaTeX, HTML/XML, XAML/WPF, MVVM (Caliburn Micro), Trello, Git, Sharepoint (CSOM), SQL, EPPlus, Office Interop, OpenXML, AForge.Net, Matlab, VBScript, Assembler

Office

Word, Excel, PowerPoint, Visio, VisualStudio, Sharepoint, Project

Languages

Spanish

English

German

Professional Experience

Research and Development Engineer

ABB S.A. • 2013 - Present • Chile
Power System Substation Automation, Protection and Control Division
Development with C#, WPF, XML, SQL, OpenXML, EPPlus,
Third Party APIs, Sharepoint, Office Plugins

Software Tools for Quality & Productivity Improvement
Responsible for 8 Electrical Engineers and their productivity
Developed several tools for Document Automation,
Third Party Tools Automation, Configuration Files Generation
Currently leading a team of 3 ElectricalEngineers+Devs#

Hardware Devices for Testing Productivity Increase
Electronic PCB devices.
Remote Terminal Units Interface

Global Products and Technologies Key User
Zuken's E^3 Series
ABB's Local CyberSecurity

Project Engineer

ABB S.A. • 2011 - Present • Chile
Power System Substation Automation, Protection and Control Division

Projects Responsabilities
Electrical Schemes design and development,
Project Documentation, IEDs configuration
Technical meetings with customers

Research and Development Intern

Parker Hannifin GmbH • 2010 - 2011 • Germany
Europe's SensoControl R&D Division

USB - IOLink Interface Prototype
Circuit/PCB design, construction and testing
System Design based on the IOLink Standard
Hardware and software design, construction, programming, testing
LABView interface design and programming for device's production chain
Prototypes HMI simulators design and develpment for analysis and testing

References

Ronald Valdés
Product Group Manager Chile
ronald.valdes@cl.abb.com

Rafael Pedrucci
PSAC Hub Manager Latam
rafael.pedrucci@cl.abb.com