									ı	l I		 	1 1	
05	234	225	209	193	177	155	133	16	38	60	76	92	108	117
.Out	LC7L9B1	LC6L9B1	LC5L9B1	LC4L9B1	LC3L9B1	LC2L9B1	LC1L9B1	RC1L9B1	RC2L9B1	RC3L9B1	RC4L9B1	RC5L9B1	RC6L9B1	RC7L9B1
	233 LC7L8B1	224	208	192	176	154	132	15	37	59	75	91	107	
06		LC6L8B2	LC5L8B2	LC4L8B2	LC3L8B2	LC2L8B2	LC1L8B2	RC1L8B2	RC2L8B2	RC3L8B2	RC4L8B2	RC5L8B2	RC6L8B2	
.Out		223	207	191	175	153	131	14	36	58	74	90	106	RC7L8B1
		LC6L8B1	LC5L8B1	LC4L8B1	LC3L8B1	LC2L8B1	LC1L8B1	RC1L8B1	RC2L8B1	RC3L8B1	RC4L8B1	RC5L8B1	RC6L8B1	
	232	222		190	174	152	130	13	35	57	73	89	105	115
07	LC7L7B1 i	1	LC5L7B2		LC3L7B2				i RC2L7B2 	I RC3L7B2	RC4L7B2	RC5L7B2 	ı RC6L7B2 ı <u> </u>	RC7L7B1
7.Out		221			173			12	34	56	72	88	104	
		LU6L/B1 I	LU5L/B1	LC4L7B1					ı	I	I HC4L/B1	I HUSL/B1	RC6L7B1 	
08	231 LC7L6B1	220	204	188 LC4L6B2	172 LC3L6B4 171	150 LC2L6B4 149	128 LC1L6B3 127	11 RC1L6B3 10	33 RC2L6B4		71	87 BC51 6B2	103 RC6L6B2	114
00		 			LC3L6B3	LC2L6B3	LC1L6B2 126	RC1L6B2	_RC2L6B3_ - 31	RC3L6B3_			<u> </u>	RC7L6B1
Out		1		187 LC4L6B1	LC3L6B2	LC2L6B2	LC1L6B1	RC1L6B1	RC2L6B2_	RC3L6B2		l	102 RC6L6B1	
		I		l	168	LC2L6B1			RC2L6B1		l		1 101	
09	230	218 LC6L5B2	202 LC5L5B2	186 LC4L5B2	LC3L5B4 167				RC2L5B4 28	RC3L5B4_		85 RC5L5B2	101 RC6L5B2	113
	LC7L5B1	 	217 201 C6L5B1 LC5L5B1		LC3L5B3	144				RC3L5B3 49	-; ;	0.4	100	RC7L5B1
		217 LC6L5B1		185 LC4L5B1	LC3L5B2	LC2L5B2 1 143				+ -48 - RC4L5B1			84 100 5L5B1 RC6L5B1	
		<u>'</u>			LC3L5B1	142			RC2L5B1				i i	
10	229 LC7L4B1	216 LC6L4B2	200 	184	⁻ 163	141	125	8	24	RC3L4B4_		1 83 1 BC51 4B2	99 	112
10					162	1/10	- 1 24 -	-	RC2L4B3 23	RC3L4B3			<u> </u>	RC7L4B1
Out		LC6L4B1	LC5L4B1	LC4L4B1	161	LC2L4B2 139 LC2L4B1	$\bar{1}2\bar{3}^{-}$	6	· - <u>2</u> 2 -	44	RC4L4B1	RC5L4B1	98 RC6L4B1	
	228 LC7L3B1	214	198	182				5	21	43	65	81	97	32 111
11		LC6L3B2	LC5L3B2	LC4L3B2	LC3L3B2	LC2L3B2	LC1L3B2	RC1L3B2	RC2L3B2	RC3L3B2	RC4L3B2	RC5L3B2	RC6L3B2	
Out		213		181				4	20	42	64	80	96	RC7L3B1
		LC6L3B1	LC5L3B1	LC4L3B1	LC3L3B1	LC2L3B1	LC1L3B1	RC1L3B1	RC2L3B1	RC3L3B1	RC4L3B1	RC5L3B1	RC6L3B1	
12	227 LC7L2B1	212	196	180	158	136	120	3	19	41	63	79	95	110
		LC6L2B2	LC5L2B2	LC4L2B2	LC3L2B2	LC2L2B2	LC1L2B2	RC1L2B2	RC2L2B2	RC3L2B2	RC4L2B2	RC5L2B2	RC6L2B2	RC7L2B1
Out		211		179				2	18	40	62	78	94	
		LC6L2B1	LC5L2B1	LC4L2B1	LC3L2B1	LC2L2B1	LC1L2B1	RC1L2B1	RC2L2B1	RC3L2B1	RC4L2B1	RC5L2B1	RC6L2B1	
	226	210	194	178	156	134	110	1	17	39	61	! 77	93	109
		410	174	1/0	150	134	110	1	1/	22	01	, <i>,</i> ,	73	107
13		L C6L 1R1	LC5L1R1	 C4 1R1	LC3L1R1	C2L1R1	LC1L1R1	BC1L1R1	I I BC2I 1B1	I RC3I 1R1	BC4I 1R1	RC5I 1R1	RC6L1B1	BC7l 1B1

= RPC

 $\begin{bmatrix} \cdot \\ \cdot \end{bmatrix} = Board$