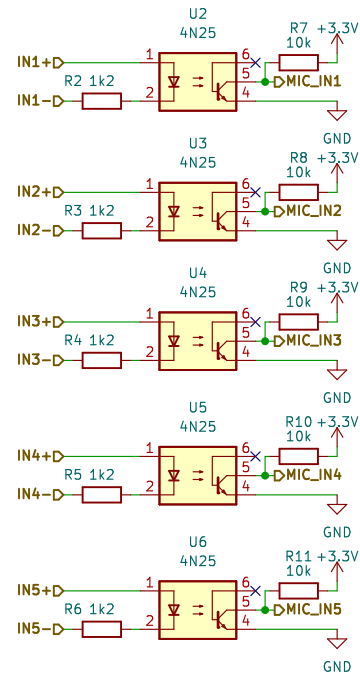


## ENTRADAS



**PARA ENTRADAS NPN**  
(conecte o negativo ao  
GND de sua fonte).  
**PARA ENTRADAS PNP**  
(conecte o positivo ao  
VCC de sua fonte).

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**DMV Automação & Tecnologia**

Sheet:

File: INPUTS.kicad\_sch

**Title: DMV PLC 32**

Size: A4

Date: 2022-07-17

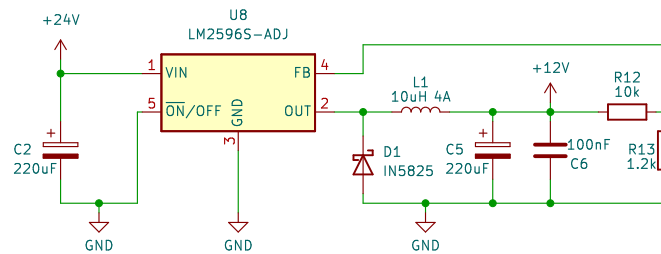
**Rev: 1**

KiCad E.D.A. kicad (7.0.0)

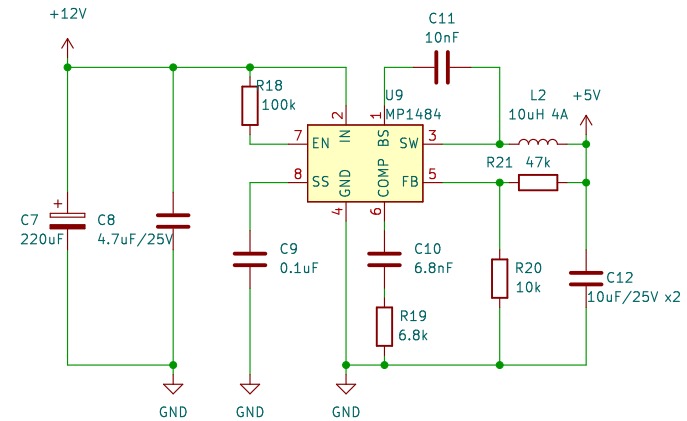
Id: 2/6



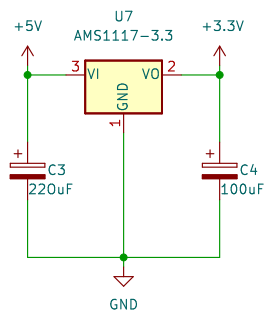
## FONTE 24V – 12V



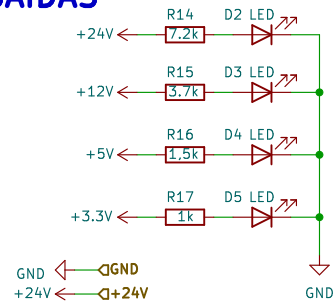
## FONTE 12V – 5V



## FONTE 5V – 3,3V



## SAÍDAS



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Sheet:

File: POWER.kicad\_sch

**Title: DMV PLC 32**

Size: A4

Date: 2022-07-17

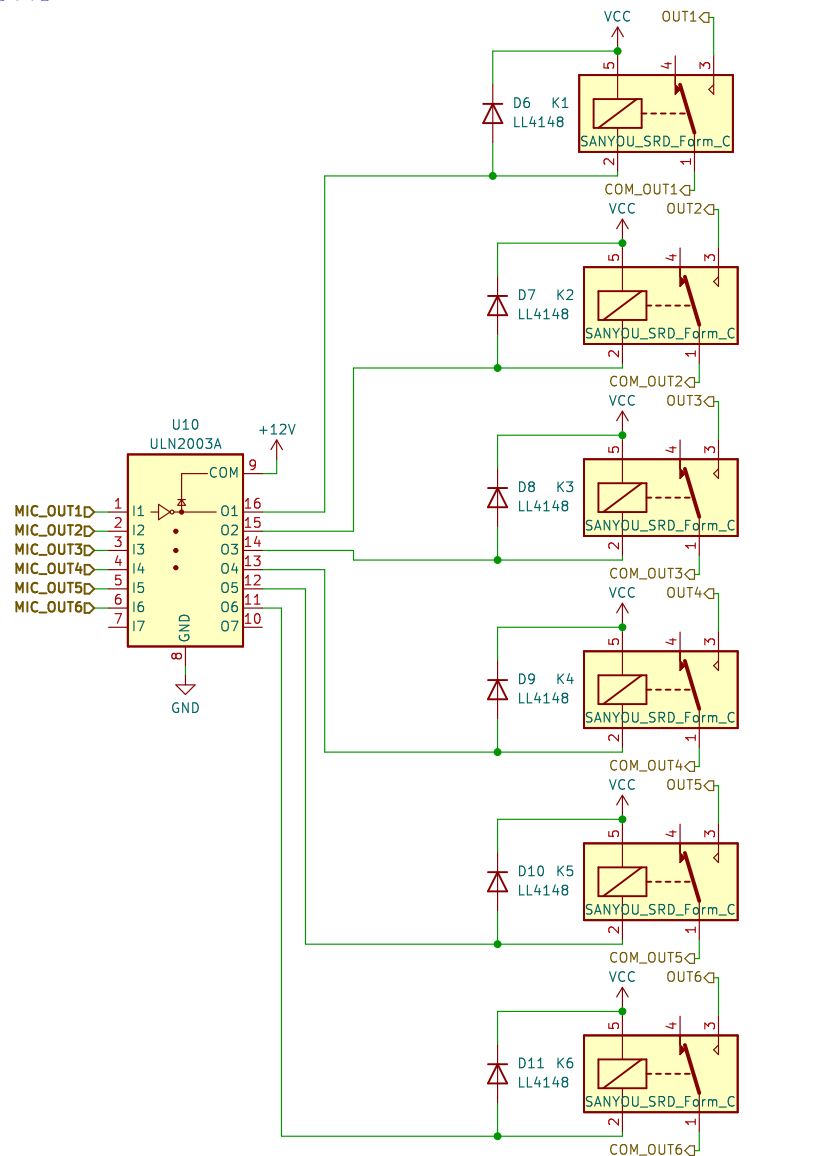
KiCad E.D.A. kicad (7.0.0)

**Rev: 1**

Id: 3/6



## SAÍDAS



**TODAS AS SAÍDAS SÃO  
(normalmente abertas) a  
relê 12vdc suportando até  
10A por saída**

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Sheet:

File: OUTPUTS.kicad\_sch

**Title: DMV PLC 32**

Size: A4

Date: 2022-07-17

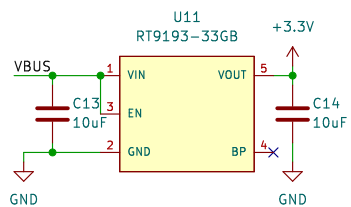
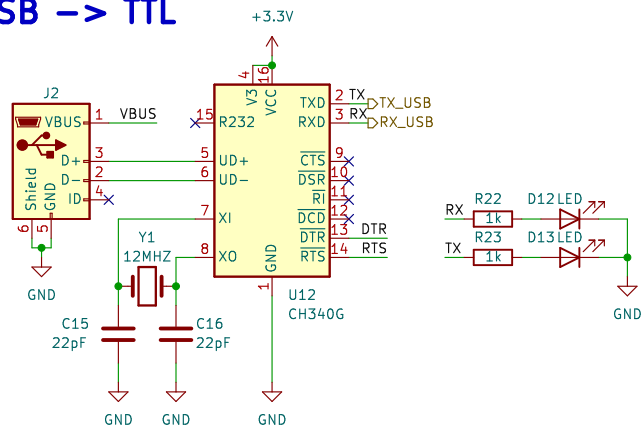
Rev: 1

KiCad E.D.A. kicad (7.0.0)

Id: 4/6

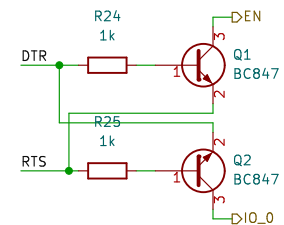


# FONTE USB

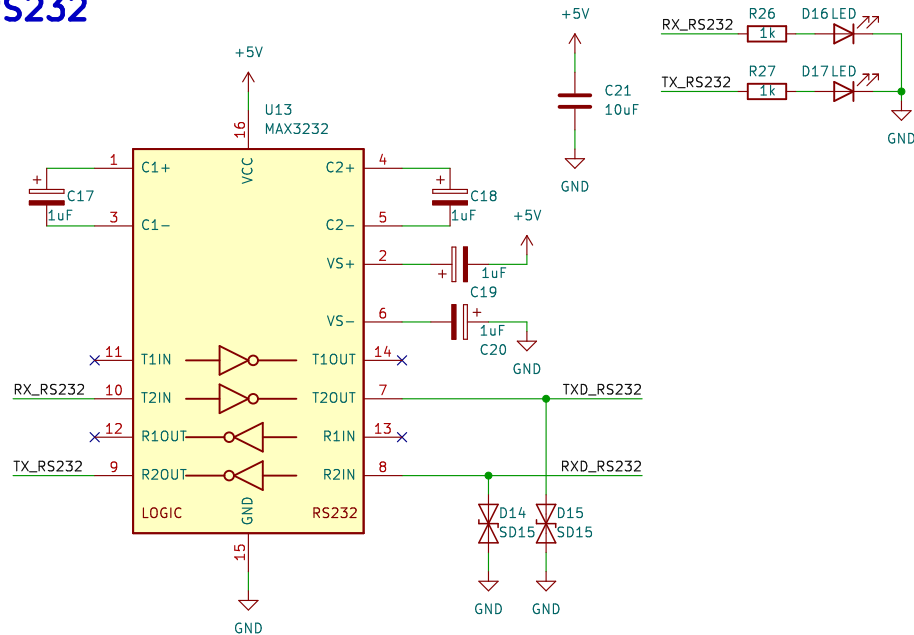
[illegible]

# AUTO BOOT

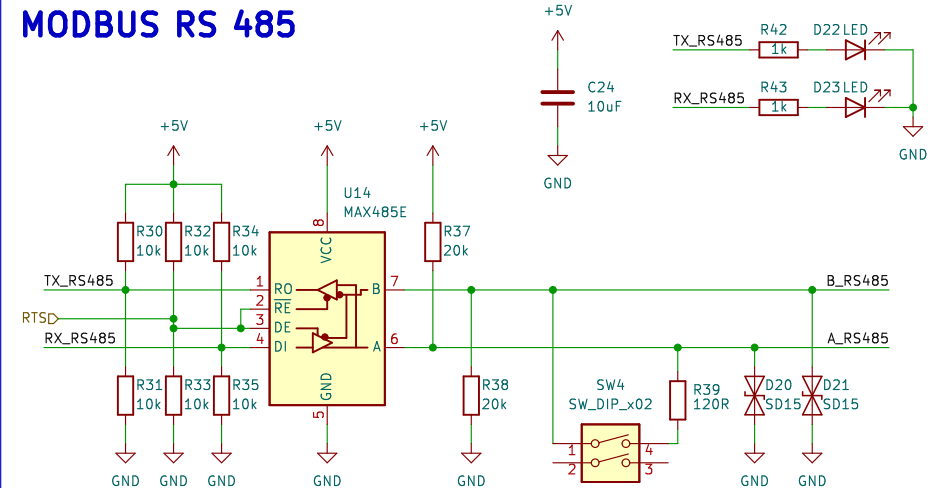
The diagram illustrates the AUTO BOOT circuit. It features two BC847 transistors, Q1 and Q2. The base of Q1 is connected to the DTR signal line through a 1kΩ resistor (R24). The base of Q2 is connected to the RTS signal line through a 1kΩ resistor (R25). The emitter of Q1 is connected to the EN pin (pin 3) of the device. The emitter of Q2 is connected to the IO\_0 pin (pin 3) of the device. The collector of Q1 is connected to the base of Q2 (pin 2), and the collector of Q2 is connected to the base of Q1 (pin 1), forming a cross-coupled configuration. The device pins are labeled: EN (pin 3), IO\_0 (pin 3), and pins 1 and 2.



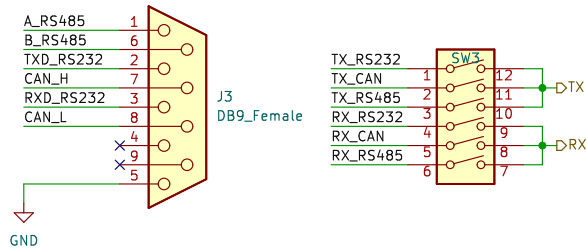
## RS232



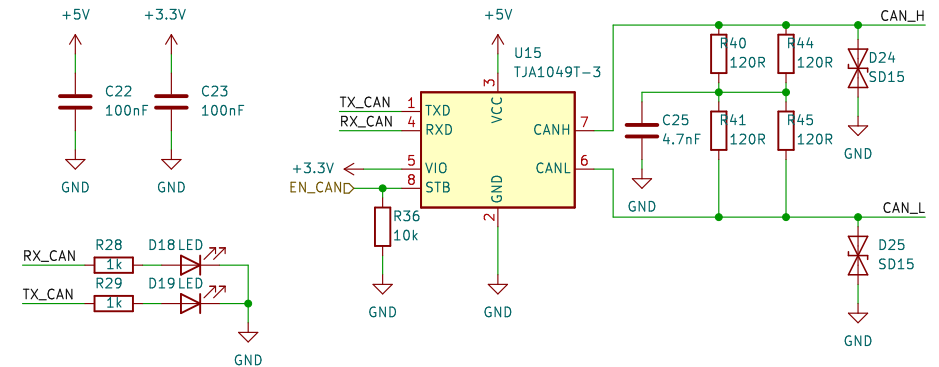
## MODBUS RS 485



## PINOUT



## CAN



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Sheet:  
File: RS232\_RS485.kicad\_sch

**Title: DMV PLC 32**

Size: A4 Date: 2022-07-17  
KiCad E.D.A. kicad (7.0.0)

Rev: 1  
Id: 6/6

