

Digital Clock using the Arduino Framework

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Abstract—In this paper the design and implementation of a feature-rich digital clock is demonstrated. The system uses multiplexing to drive six seven-segment displays efficiently, minimizing I/O utilization. Key functionalities include timekeeping, digit-by-digit editing, and pause/play control. Boolean-based increment and decrement logic ensures more accurate cascading of seconds, minutes, and hours within standard constraints. The hardware setup, complemented by software debouncing and display refreshing, demonstrates a reliable, compact, and user-interactive digital clock suitable for both educational and practical applications.

I. INTRODUCTION

Digital timekeeping has long been a critical component of electronic system design, with classical digital design principles thoroughly discussed in foundational works such as [1]–[3]. The advent of microcontroller platforms, particularly Arduino, has enabled the development of compact, programmable clocks with enhanced user interactivity [4]. Techniques such as BCD-to-seven-segment interfacing and display multiplexing allow efficient utilization of limited I/O resources while maintaining accurate visual representation [5]. Inspired by these principles, a simple state machine representing a decade counter is implemented in [6]. Based on this, we design an Arduino-based digital clock featuring six-digit multiplexed displays, pause/play functionality, and digit-by-digit editing with Boolean logic-driven increment and decrement operations.

II. CLOCK FUNCTIONALITY AND HARDWARE SETUP

Fig. 1 shows the various hardware connections and the corresponding components are listed in Table II. The clock follows the 23:59:59 format using 6 displays. The first 2 represent hours, next 2 denote minutes and the last 2 count the seconds. The six displays are connected to the arduino through the pin connections listed in Table I. The number count for each display is also listed therein. Pin connections from the

Display	Arduino Pin	Description
1	D4	Counts 0-2
2	D5	Counts $\begin{cases} 0-3 & display1 = 2, \\ 0-9 & otherwise \end{cases}$
3	D6	Counts 0-5
4	D7	Counts 0-9
5	D8	Counts 0-5
6	D9	Counts 0-9

TABLE I: Display to Arduino Connections

Arduino to the four push buttons are available in Table III.

Table IV gives the connections between the Arduino and the IC 7447 display decoder. Further, the pin connections for the IC 7447 to all the displays are available in Table V.

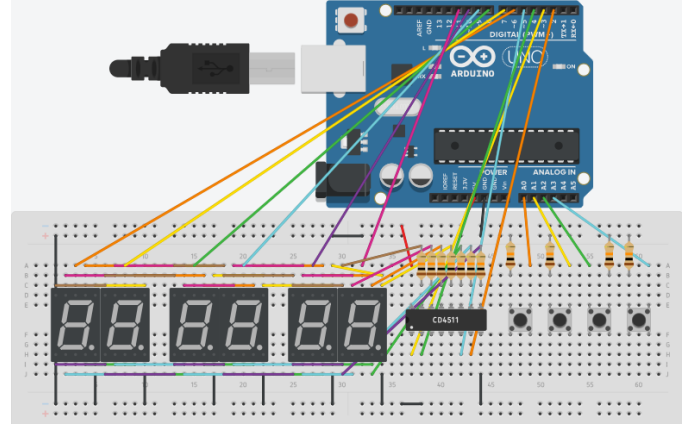


Fig. 1: Tinkercad Simulation of the Digital Clock

Component	Value	Quantity
Arduino Uno		1
USB Cable	Type B	1
Seven Segment Display	Common Cathode	6
Push Buttons		4
IC 7447		1
Jumper Wires	M-M	16
Breadboard		1
Resistors	220 Ω	7
Resistors	10k Ω (pull-down)	4

TABLE II: Components List

Button	Arduino Pin	Description
1	D10	Edit Mode Toggle
2	D11	Next Digit Selection
3	D12	Increment Digit
4	D13	Decrement Digit

TABLE III: Button to Arduino Connections

IC 7447 PIN	Arduino Pin	Description
7	D0	BCD Bit 0 (A)
1	D1	BCD Bit 1 (B)
2	D2	BCD Bit 2 (C)
6	D3	BCD Bit 3 (D)

TABLE IV: IC 7447 to Arduino Connections

IC 7447	Seven Segment (All)
13	a
12	b
11	c
10	d
9	e
15	f
14	g
8	Ground
16	5V

TABLE V: BCD to 7-Segment Connections

III. CLOCK LOGIC

Here, we show how to design counters for the various displays in the clock.

A. Displays 2 (Display1 \neq 2), 4 and 6

These displays count from 0-9. Display 2 will do this only when Display 1 is less than 2. The truth table and the corresponding K-maps using dont cares for the output variables are given in Fig. 2 yielding the following logic equations.

$$A = W'_1 \quad (1)$$

$$B = (W_1 X'_1 Z'_1) + (W'_1 X_1) \quad (2)$$

$$C = (X'_1 Y_1) + (W'_1 Y_1) + (W_1 X_1 Y'_1) \quad (3)$$

$$D = (W'_1 Z_1) + (W_1 X_1 Y_1) \quad (4)$$

B. Displays 3 and 5

These displays count from 0-5. The truth table and the corresponding K-maps using dont cares for the output variables are given in Fig. 3, yielding the following logic equations.

$$A = W'_2 \quad (5)$$

$$B = (W_2 X'_2 Y'_2) + (W'_2 X_2) \quad (6)$$

$$C = (W_2 X_2) + (W'_2 X'_2 Y_2) \quad (7)$$

$$D = 0 \quad (8)$$

$$(9)$$

C. Display 2 (Display1 = 2)

This display counts from 0-3, when the first display shows 2. The truth table and the corresponding K-maps using dont cares for the output variables are given in Fig. 4, yielding the following logic equations.

$$A = W'_5 \quad (10)$$

$$B = (W_5 X'_5) + (W'_5 X_5) \quad (11)$$

$$C = 0 \quad (12)$$

$$D = 0 \quad (13)$$

D. Display 1

This display counts from 0-2, representing the first digit of the hour. The truth table and the corresponding K-maps using dont cares for the output variables are given in Fig. 5, yielding the following logic equations.

$$A = W'_6 X'_6 \quad (14)$$

$$B = W_6 X'_6 \quad (15)$$

$$C = 0 \quad (16)$$

$$D = 0 \quad (17)$$

IV. DECREMENT LOGIC

This logic is primarily used to decrease the count on a particular display using button 4. This is helpful in setting the time for the clock. The relevant truth tables are available in Table VI

$$A = W'_1 \quad (18)$$

$$B = (X'_1 W'_1 ((Z'_1 Y_1) + (Z_1 Y'_1))) + (Z'_1 W_1 X_1) \quad (19)$$

$$C = (Z'_1 Y_1 (X_1 + W_1)) + (Z_1 X'_1 W'_1 Y'_1) \quad (20)$$

$$D = X'_1 Y'_1 ((Z_1 W_1) + (Z'_1 W'_1)) \quad (21)$$

$$A = W'_2 \quad (22)$$

$$B = (Y'_2 X'_2 W'_2) + (Y'_2 X_2 W_2) \quad (23)$$

$$C = X'_2 ((Y_2 W_2) + (Y'_2 W'_2)) \quad (24)$$

$$D = 0 \quad (25)$$

$$A = W'_5 \quad (26)$$

$$B = (X_5 W_5) + (X'_5 W'_5) \quad (27)$$

$$C = 0 \quad (28)$$

$$D = 0 \quad (29)$$

$$A = X_6 W'_6 \quad (30)$$

$$B = X'_6 W'_6 \quad (31)$$

$$C = 0 \quad (32)$$

$$D = 0 \quad (33)$$

V. MULTIPLEXING TECHNIQUE

All BCD inputs (A-D) are shared among six seven-segment displays. Displays are enabled one at a time using EN[0..5] = D4-D9. Each digit is displayed for 1ms, creating a fast alternating effect that appears continuous. This saves I/O pins and allows full six-digit display.

Z	Y	X	W	D	C	B	A
0	0	0	0	0	0	0	1
0	0	0	1	0	0	1	0
0	0	1	0	0	0	1	1
0	0	1	1	0	1	0	0
0	1	0	0	0	1	0	1
0	1	0	1	0	1	1	0
0	1	1	0	0	1	1	1
0	1	1	1	1	0	0	0
1	0	0	0	1	0	0	1
1	0	0	1	0	0	0	0

ZY \ XW	00	01	11	10
00	1	0	0	1
01	1	0	0	1
11	-	-	-	-
10	1	0	-	-

(A)

ZY \ XW	00	01	11	10
00	0	1	0	1
01	0	1	0	1
11	-	-	-	-
10	0	0	-	-

(B)

ZY \ XW	00	01	11	10
00	0	0	1	0
01	1	1	0	1
11	-	-	-	-
10	0	0	-	-

(C)

ZY \ XW	00	01	11	10
00	0	0	0	0
01	0	0	1	0
11	-	-	-	-
10	1	0	-	-

(D)

Fig. 2: Counting 0-9

Z	Y	X	W	D	C	B	A
0	0	0	0	0	0	0	1
0	0	0	1	0	0	1	0
0	0	1	0	0	0	1	1
0	0	1	1	0	1	0	0
0	1	0	0	0	1	0	1
0	1	0	1	0	0	0	0

ZY	XW			
	00	01	11	10
00	1	0	0	1
01	1	0	-	-
11	-	-	-	-
10	-	-	-	-

(A)

ZY	XW			
	00	01	11	10
00	0	1	0	1
01	0	0	-	-
11	-	-	-	-
10	-	-	-	-

(B)

ZY	XW			
	00	01	11	10
00	0	0	1	0
01	1	0	-	-
11	-	-	-	-
10	-	-	-	-

(C)

Fig. 3: Counting 0-5

X	W	D	C	B	A
0	0	0	0	0	1
0	1	0	0	1	0
1	0	0	0	1	1
1	1	0	0	0	0

X	W	
	0	1
0	1	0
1	1	0

(a) A

X	W	
	0	1
0	0	1
1	1	0

(b) B

Fig. 4: Counting 0-3

X	W	D	C	B	A
0	0	0	0	0	1
0	1	0	0	1	0
1	0	0	0	0	0

		W	
		0	1
X	0	1	0
	1	0	-

(a) A

		W	
		0	1
X	0	0	1
	1	0	-

(b) B

Fig. 5: Counting 0-2

Z	Y	X	W	D	C	B	A
0	0	0	0	1	0	0	1
0	0	0	1	0	0	0	0
0	0	1	0	0	0	0	1
0	0	1	1	0	0	1	0
0	1	0	0	0	0	1	1
0	1	0	1	0	1	0	0
0	1	1	0	0	1	0	1
0	1	1	1	0	1	1	0
1	0	0	0	0	1	1	1
1	0	0	1	1	0	0	0

Z	Y	X	W	D	C	B	A
0	0	0	0	0	1	0	1
0	0	0	1	0	0	0	0
0	0	1	0	0	0	0	1
0	0	1	1	0	0	1	0
0	1	0	0	0	0	1	1
0	1	0	1	0	1	0	0

X	W	D	C	B	A
0	0	0	0	1	1
0	1	0	0	0	0
1	0	0	0	0	1
1	1	0	0	1	0

X	W	D	C	B	A
0	0	0	0	1	0
0	1	0	0	0	0
1	0	0	0	0	1

TABLE VI

VI. DIGIT EDITING LOGIC

The clock allows pausing and digit-by-digit editing:

- 1) Press PAUSE (D10) to toggle run/edit mode. In edit mode, the clock stops.
- 2) Press NEXT (D11) to select the digit to edit (cycles 0-5: sec1, sec10, min1, min10, hr1, hr10).
- 3) Press INC (D12) to increment the selected digit with rollovers.
- 4) Press DEC (D13) to decrement the selected digit with rollunders.
- 5) Selected digit blinks every 500ms to indicate focus.

VII. CONTROL IMPLEMENTATION

- 1) Pressing Button 1 toggles between run mode and edit mode. In edit mode, the clock pauses.
- 2) In edit mode, pressing Button 2 selects the next digit for editing (cycles through all six digits).
- 3) In edit mode, pressing Button 3 increments the currently selected digit using the increment logic tables.

- 4) In edit mode, pressing Button 4 decrements the currently selected digit using the decrement logic tables.
- 5) The selected digit blinks at 5Hz (200ms on, 200ms off) for visual feedback.

VIII. SOFTWARE IMPLEMENTATION

The Arduino code implements:

- Timer interrupt for clock ticking (10Hz interrupt rate)
- Button debouncing with software delays
- Multiplexed display refresh
- Editing mode with digit selection and value modification using the Boolean logic from the tables
- Proper constraints on time values (hours 0-23, minutes 0-59, seconds 0-59)

IX. EXECUTION

A. Upload Code to Arduino

- 1) Connect Arduino to computer via USB
- 2) Upload the following code to the Arduino using PlatformIO.

`https://github.com/gadepall/clock/blob/main/codes/code.cpp`
- 3) Open PlatformIO, select New Project and then fill in the details (name, board & framework).
- 4) Then replace contents in src/main.cpp with the above code, now run & upload that code to Arduino Uno.

B. Hardware Build

- 1) Connect the seven-segment displays to the breadboard
- 2) Connect all segment outputs together (through resistors)
- 3) Make connections to the IC7447 according to Table 3.0
- 4) Connect the IC7447 and the buttons to the Arduino according to Table 2.0
- 5) Add appropriate current-limiting resistors for LEDs and pull-down resistors for buttons

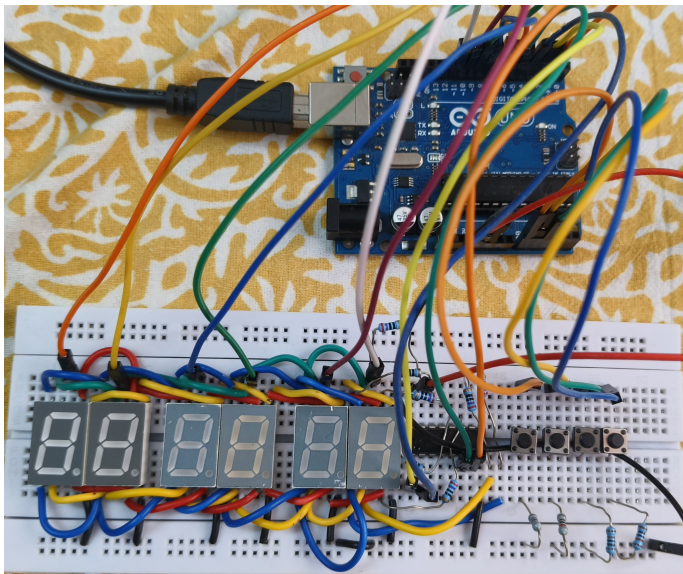


Fig. 6: Final Arduino-based Clock Implementation

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FUTURE SCOPE

- Integration with wireless modules (Bluetooth/Wi-Fi) for remote time setting and synchronization.
- Addition of alarms, timers, and countdown features with user-defined events.
- Implementation of a real-time clock (RTC) module for improved accuracy and power efficiency.
- Expansion to a multi-language or multi-format (12/24-hour) display interface.
- Incorporation of IoT functionality for smart home or wearable applications.