c) 280

a) 270

b) 279

(GATE GA 2020)

d) 299

Q.9 Given a semicircle with O as the centre, as shown in the figure, the ratio $\frac{\overline{AC} + \overline{CB}}{\overline{AB}}$ is _____ where \overline{AC} , \overline{CB} and \overline{AB} are chords.

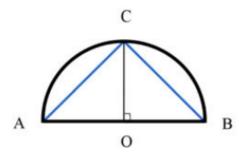


Fig. 1.

a) $\sqrt{2}$

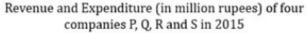
b) $\sqrt{3}$

c) 2

d) 3

(GATE GA 2020)

Q.10 The revenue and expenditure of four different companies P, Q, R and S in 2015 are shown in the figure. If the revenue of company Q in 2015 was 20% more than that in 2014, and company Q had earned a profit of 10% on expenditure in 2014, then its expenditure (in million rupees) in 2014 was



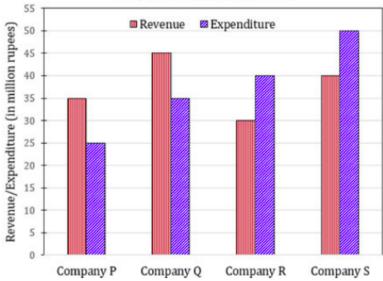


Fig. 2.

a) 32.7

c) 34.1

b) 33.7

d) 35.1

(GATE GA 2020)

Q.11 $ax^3 + bx^2 + cx + d$ is a polynomial on real x over real coefficients a, b, c, d wherein $a \ne 0$. Which of the following statements is true?

- a) d can be chosen to ensure that x = 0 is a root for any given set a, b, c.
- b) No choice of coefficients can make all roots identical.
- c) a, b, c, d can be chosen to ensure that all roots are complex.
- d) c alone cannot ensure that all roots are real.

- Q.12 Which of the following is true for all possible non-zero choices of integers $m, n; m \neq n$, or all possible non-zero choices of real numbers $p, q; p \neq q$, as applicable?

 - a) $\frac{1}{\pi} \int_0^{\pi} \sin m\theta \sin n\theta d\theta = 0$ b) $\frac{1}{2\pi} \int_{-\pi/2}^{\pi/2} \sin p\theta \sin q\theta d\theta = 0$ c) $\frac{1}{2\pi} \int_{-\pi}^{\pi} \sin p\theta \cos q\theta d\theta = 0$

 - d) $\lim_{\alpha \to \infty} \frac{1}{2\alpha} \int_{-\alpha}^{\alpha} \sin p\theta \sin q\theta d\theta = 0$

(GATE EE 2020)

- Q.13 Which of the following statements is true about the two sided Laplace transform?
 - a) It exists for every signal that may or may not have a Fourier transform.
 - b) It has no poles for any bounded signal that is non-zero only inside a finite time interval.
 - c) The number of finite poles and finite zeroes must be equal.
 - d) If a signal can be expressed as a weighted sum of shifted one sided exponentials, then its Laplace Transform will have no poles.

(GATE EE 2020)

- Q.14 Consider a signal $x[n] = \left(\frac{1}{2}\right)^n 1[n]$, where 1[n] = 0 if n < 0 and 1[n] = 1 if $n \ge 0$. The z-transform of x[n-k], k > 0 is $\frac{z^{-k}}{1-\frac{1}{2}z^{-1}}$ with region of convergence being ______.
 - a) |z| < 2

c) |z| < 1/2

b) |z| > 2

d) |z| > 1/2

(GATE EE 2020)

Q.15 The value of the following complex integral, with C representing the unit circle centered at origin in the counterclockwise sense, is:

$$\int_{C} \frac{z^2 + 1}{z^2 - 2z} dz$$

a) $8\pi i$

c) $-\pi i$

b) $-8\pi i$

d) πi

(GATE EE 2020)

Q.16 χ_R and x_A are, respectively, the rms and average values of x(t) = x(t-T), and similarly, y_R and y_A are, respectively, the rms and average values of y(t) = kx(t). k, T are independent of t. Which of the following is true?

a) $y_A = kx_A; y_R = kx_R$

c) $y_A \neq kx_A$; $y_R = kx_R$

b) $y_A = kx_A; y_R \neq kx_R$

d) $y_A \neq kx_A$; $y_R \neq kx_R$

(GATE EE 2020)

- Q.17 A three-phase cylindrical rotor synchronous generator has a synchronous reactance X_s and a negligible armature resistance. The magnitude of per phase terminal voltage is V_A and the magnitude of per phase induced emf is E_A . Considering the following two statements, P and Q,
 - P: For any three-phase balanced leading load connected across the terminals of this synchronous generator, V_A is always more than E_A .
 - Q: For any three-phase balanced lagging load connected across the terminals of this synchronous generator, V_A is always less than E_A .

which of the following options is correct?

- a) P is false and Q is true.
- b) P is true and Q is false.
- c) P is false and Q is false.
- d) P is true and Q is true.

(GATE EE 2020)

Q.18 A lossless transmission line with 0.2 pu reactance per phase uniformly distributed along the length of the line, connecting a generator bus to a load bus, is protected up to 80% of its length by a distance relay placed at the generator bus. The generator terminal voltage is 1 pu. There is no generation at the load bus. The threshold pu current for operation of the distance relay for a solid three phase-to-ground fault on the transmission line is closest to:

a) 1.00

c) 5.00

b) 3.61

d) 6.25

(GATE EE 2020)

- Q.19 Out of the following options, the most relevant information needed to specify the real power (P) at the PV buses in a load flow analysis is
 - a) solution of economic load dispatch
 - b) rated power output of the generator
 - c) rated voltage of the generator
 - d) base power of the generator

(GATE EE 2020)

Q.20 Consider a linear time-invariant system whose input r(t) and output y(t) are related by the following differential equation:

$$\frac{d^2y(t)}{dt^2} + 4y(t) = 6r(t)$$

The poles of this system are at

a) +2j, -2j

b) +2, -2

c) +4, -4d) +4j, -4j

(GATE EE 2020)

- Q.21 A single-phase, full-bridge diode rectifier fed from a 230 V, 50 Hz sinusoidal source supplies a series combination of finite resistance, R, and a very large inductance, L. The two most dominant frequency components in the source current are:
 - a) 50 Hz, 0 Hz

c) 50 Hz, 150 Hz

b) 50 Hz, 100 Hz

d) 150 Hz, 250 Hz

(GATE EE 2020)

Q.22 Thyristor T_1 is triggered at an angle α (in degree), and T_2 at angle $180^{\circ} + \alpha$, in each cycle of the sinusoidal input voltage. Assume both thyristors to be ideal. To control the load power over the range 0 to 2 kW, the minimum range of variation in α is:

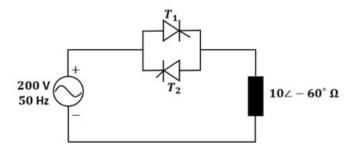


Fig. 3.

- a) 0° to 60°
- b) 0° to 120°

- c) 60° to 120° d) 60° to 180°

Q.23 Which of the options is an equivalent representation of the signal flow graph shown here?

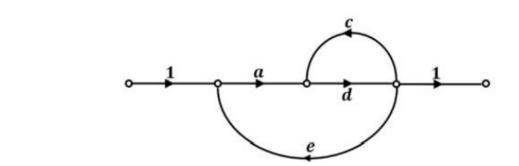
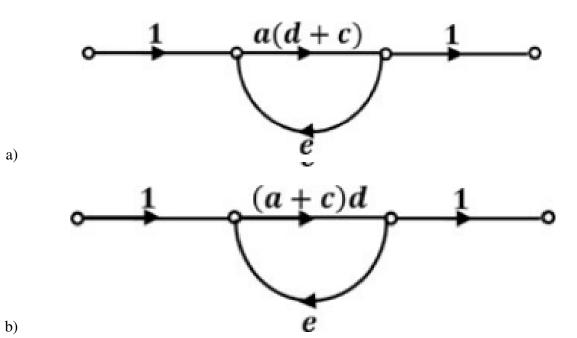
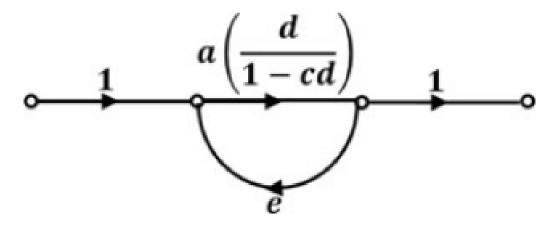
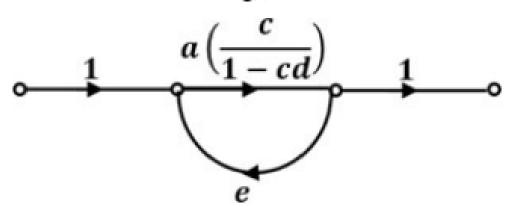


Fig. 4.





c)



d)

(GATE EE 2020)

- Q.24 A common-source amplifier with a drain resistance, $R_D = 4.7 \text{ k}\Omega$, is powered using a 10 V power supply. Assuming that the transconductance, g_m , is $520\mu\text{A/V}$, the voltage gain of the amplifier is closest to:
 - a) -2.44

c) 1.22

b) -1.22

d) 2.44

(GATE EE 2020)

- Q.25 A sequence detector is designed to detect precisely 3 digital inputs, with overlapping sequences detectable. For the sequence (1,0,1) and input data (1,1,0,1,0,0,1,1,0,1,0,1,0), what is the output of this detector?
 - a) 1, 1, 0, 0, 0, 0, 1, 1, 0, 1, 0, 0

c) 0, 1, 0, 0, 0, 0, 0, 1, 0, 1, 1, 0

b) 0, 1, 0, 0, 0, 0, 0, 1, 0, 1, 0, 0

d) 0, 1, 0, 0, 0, 0, 0, 0, 1, 0, 0, 0

(GATE EE 2020)

Q.26 Consider the initial value problem below. The value of y at $x = \ln 2$, (roundedof fto3decimalplaces) is

$$\frac{dy}{dx} = 2x - y, \quad y(0) = 1$$

(GATE EE 2020)

Q.27 A three-phase, 50 Hz, 4-pole induction motor runs at no-load with a slip of 1 %. With full load, the slip increases to 5 %. The % speed regulation of the motor (roundedoffto2decimalplaces) is

____. (GATE EE 2020) Q.28 Currents through ammeters A2 and A3 in the figure are $1\angle 10^{\circ}$ and $1\angle 70^{\circ}$, respectively. The reading of the ammeter A1rounded off to 3 decimal places is ______ A.

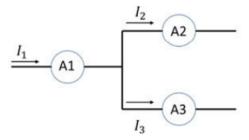


Fig. 5.

(GATE EE 2020)

Q.29 The Thevenin equivalent voltage, V_{TH} , in V (roundedof fto 2 decimal places) of the network shown below, is ______.

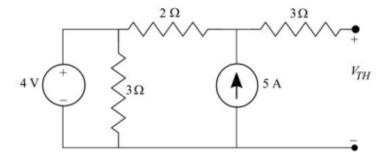


Fig. 6.

(GATE EE 2020)

Q.30 A double pulse measurement for an inductively loaded circuit controlled by the IGBT switch is carried out to evaluate the reverse recovery characteristics of the diode, D, represented approximately as a piecewise linear plot of current vs time at diode turn-off. L_{par} is a parasitic inductance due to the wiring of the circuit, and is in series with the diode. The point on the plot (indicateyourchoicebyentering1, 2, 3or4) at which the IGBT experiences the highest current stress is ______.

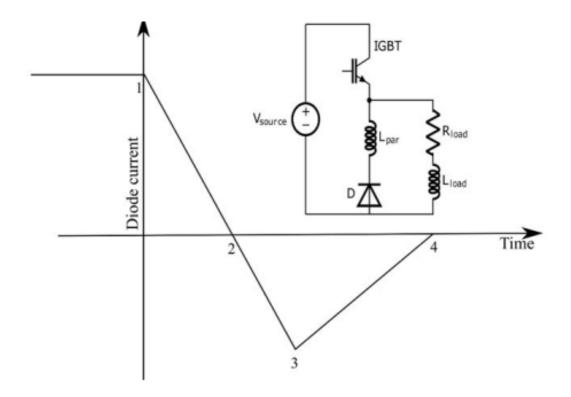


Fig. 7.

Q.31 A single-phase, 4 kVA, 200 V/100 V, 50 Hz transformer with laminated CRGO steel core has rated no-load loss of 450 W. When the high-voltage winding is excited with 160 V, 40 Hz sinusoidal ac supply, the no-load losses are found to be 320 W. When the high-voltage winding of the same transformer is supplied from a 100 V, 25 Hz sinusoidal ac source, the no-load losses will be _____ W (roundedof fto2decimalplaces).

(GATE EE 2020)

Q.32 A single-phase inverter is fed from a 100 V dc source and is controlled using a quasi-square wave modulation scheme to produce an output waveform, v(t), as shown. The angle σ is adjusted to entirely eliminate the 3^{rd} harmonic component from the output voltage. Under this condition, for v(t), the magnitude of the 5^{th} harmonic component as a percentage of the magnitude of the fundamental component is ______ (rounded of fto 2 decimal places).

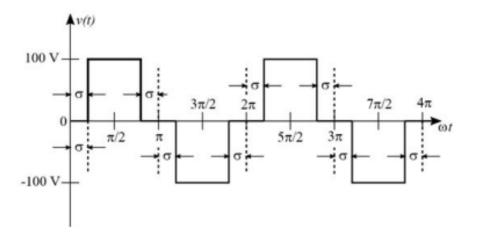


Fig. 8.

(GATE EE 2020)

Q.33 A single 50 Hz synchronous generator on droop control was delivering 100 MW power to a system. Due to increase in load, generator power had to be increased by 10 MW, as a result of which, system frequency dropped to 49.75 Hz. Further increase in load in the system resulted in a frequency of 49.25 Hz. At this condition, the power in MW supplied by the generator is ______ (rounded of fto 2 decimal places).

(GATE EE 2020)

Q.34 Consider a negative unity feedback system with forward path transfer function

$$G(s) = \frac{K}{(s+a)(s-b)(s+c)},$$

where K, a, b, c are positive real numbers. For a Nyquist path enclosing the entire imaginary axis and right half of the s-plane in the clockwise direction, the Nyquist plot of (1 + G(s)), encircles the origin of (1 + G(s))-plane once in the clockwise direction and never passes through this origin for a certain value of K. Then, the number of poles of $\frac{G(s)}{1 + G(s)}$ lying in the open right half of the s-plane is ______. (GATE EE 2020)

Q.35 The cross-section of a metal-oxide-semiconductor structure is shown schematically. Starting from an uncharged condition, a bias of +3 V is applied to the gate contact with respect to the body contact. The charge inside the silicon dioxide layer is then measured to be +Q. The total charge contained within the dashed box shown, upon application of bias, expressed as a multiple of Q (absolutevalueinCoulombs, rounded of ftothenearestinteger) is

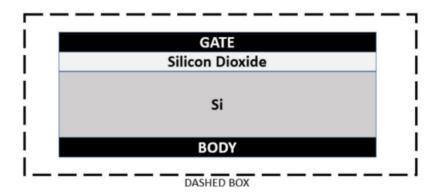


Fig. 9.

- Q.36 For real numbers, x and y, with $y = 3x^2 + 3x + 1$, the maximum and minimum value of y for $x \in [-2, 0]$ are respectively.
 - a) 7 and 1/4

c) -2 and -1/2

b) 7 and 1

d) 1 and 1/4

(GATE EE 2020)

- Q.37 The vector function expressed by $F = a_x (5y k_1 z) + a_y (3z + k_2 x) + a_z (k_3 y 4x)$ represents a conservative field, where a_x , a_y , a_z are unit vectors along x, y and z directions, respectively. The values of constants k_1 , k_2 , k_3 are given by:
 - a) $k_1 = 3, k_2 = 3, k_3 = 7$

c) $k_1 = 4, k_2 = 5, k_3 = 3$

b) $k_1 = 3, k_2 = 8, k_3 = 5$

d) $k_1 = 0, k_2 = 0, k_3 = 0$

(GATE EE 2020)

- Q.38 A 250 V dc shunt motor has an armature resistance of 0.2 Ω and a field resistance of 100 Ω . When the motor is operated on no-load at rated voltage, it draws an armature current of 5 A and runs at 1200 rpm. When a load is coupled to the motor, it draws total line current of 50 A at rated voltage, with a 5% reduction in the air-gap flux due to armature reaction. Voltage drop across the brushes can be taken as 1 V per brush under all operating conditions. The speed of the motor, in rpm, under this loaded condition, is closest to:
 - a) 1200

b) 1000

c) 1220

d) 900

(GATE EE 2020)

Q.39 Two buses, i and j, are connected with a transmission line of admittance Y, at the two ends of which there are ideal transformers with turns ratios as shown. Bus admittance matrix for the system is:

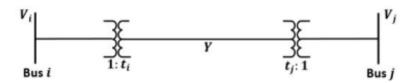


Fig. 10.

a)
$$\begin{pmatrix} -t_i t_j Y & t_j^2 Y \\ t_i^2 Y & -t_i t_j Y \end{pmatrix}$$

b)
$$\begin{pmatrix} t_i t_j Y & -t_j^2 Y \\ -t_i^2 Y & t_i t_j Y \end{pmatrix}$$
c)
$$\begin{pmatrix} t_i^2 Y & -t_i t_j Y \\ -t_i t_j Y & t_j^2 Y \end{pmatrix}$$
d)
$$\begin{pmatrix} -t_i^2 Y & t_i t_j Y \\ t_i t_j Y & -t_j^2 Y \end{pmatrix}$$

- Q.40 Consider the diode circuit shown below. The diode, D, obeys the current-voltage characteristic $I_D = I_S \left(\exp\left(\frac{V_D}{nV_T}\right) 1 \right)$, where n > 1, $V_T > 0$, V_D is the voltage across the diode and I_D is the current through it. The circuit is biased so that voltage, V > 0 and current, I < 0. If you had to design this circuit to transfer maximum power from the current source I_1 to a resistive load (not shown) at the output, what values of R_1 and R_2 would you choose(?)
 - a) Large R_1 and large R_2 .

c) Large R_1 and small R_2 .

b) Small R_1 and small R_2 .

d) Small R_1 and large R_2 .

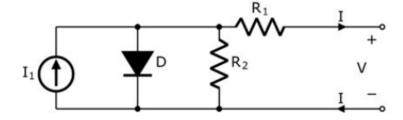


Fig. 11.

(GATE EE 2020)

- Q.41 A non-ideal diode is biased with a voltage of -0.03 V, and a diode current of I_1 is measured. The thermal voltage is $26 \, mV$ and the ideality factor for the diode is 15/13. The voltage, in V, at which the measured current increases to $1.5I_1$, is closest to
 - a) -0.02

c) -1.50

b) -0.09

d) -4.50

(GATE EE 2020)

- Q.42 A benchtop dc power supply acts as an ideal 4A current source as long as its terminal voltage is below 10 V. Beyond this point, it begins to behave as an ideal 10 V voltage source for all load currents going down to 0A. When connected to an ideal rheostat, find the load resistance value at which maximum power is transferred, and the corresponding load voltage and current.
 - a) Short, 0Ω , 10V
 - b) Open, 4*A*, 0*V*
 - c) $2.5 \Omega, 4A, 10 V$
 - d) $2.5 \Omega, 4A, 5 V$

(GATE EE 2020)

Q.43 The static electric field inside a dielectric medium with relative permittivity, $\epsilon_r = 2.25$, expressed in cylindrical coordinate system is given by the following expression

$$\mathbf{E} = a_r \, 2r + a_\varphi \left(\frac{3}{r}\right) + a_z \, 6$$

where a_r, a_{φ}, a_z are unit vectors along r, φ and z directions, respectively. If the above expression represents a valid electrostatic field inside the medium, then the volume charge density associated with this field in terms of free space permittivity, ϵ_0 , in SI units is given by(:)

- a) 3ϵ
- b) 4ϵ
- c) 5ϵ
- d) 9ϵ

Q.44 Consider a permanent magnet dc (PMDC) motor which is initially at rest. At t = 0, a dc voltage of 5 V is applied to the motor. Its speed monotonically increases from 0 rad/s to 6.32 rad/s in 0.5 s and finally settles to 10 rad/s. Assuming that the armature inductance of the motor is negligible, the transfer function for the motor is

a)
$$\frac{10}{0.5s + 1}$$

c)
$$\frac{2}{s+0.5}$$

d) $\frac{10}{s+0.5}$

b)
$$\frac{2}{0.5s+1}$$

d)
$$\frac{10}{s + 0.5}$$

(GATE EE 2020)

Q.45 Which of the following options is correct for the system shown below?

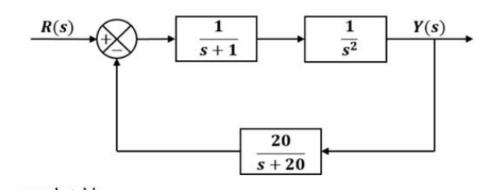


Fig. 12.

- a) 4th order and stable
- b) 3rd order and stable

- c) 4th order and unstable
- d) 3rd order and unstable

(GATE EE 2020)

Q.46 Consider a negative unity feedback system with the forward path transfer function

$$\frac{s^2 + s + 1}{s^3 + 2s^2 + 2s + K},$$

where K is a positive real number. The value of K for which the system will have some of its poles on the imaginary axis is _____.

a) 9

c) 7

b) 8

d) 6

(GATE EE 2020)

- Q.47 Suppose for input x(t) a linear time-invariant system with impulse response h(t) produces output y(t), so that x(t) * h(t) = y(t). Further, if |x(t)| * |h(t)| = z(t), which of the following statements is true?
 - a) For all $t \in (-\infty, \infty)$, $z(t) \le y(t)$
 - b) For some but not all $t \in (-\infty, \infty)$, $z(t) \le y(t)$
 - c) For all $t \in (-\infty, \infty)$, $z(t) \ge y(t)$

 $\begin{pmatrix} 2 & 3 & 2 \\ 3 & 2 & 1 \\ 3 & 1 & 7 \end{pmatrix} = \begin{pmatrix} a_{11} & 0 & 0 \\ a_{12} & a_{22} & 0 \\ a_{13} & a_{23} & a_{33} \end{pmatrix} \begin{pmatrix} a_{11} & a_{12} & a_{13} \\ 0 & a_{22} & a_{23} \\ 0 & 0 & a_{33} \end{pmatrix}^{T}$

a) 5

b) 6

c) 8

d) 9

Q.53 The figure below shows the per-phase Open Circuit Characteristics (measured in V) and Short Circuit Characteristics (measured in A) of a 14 kVA, 400 V, 50 Hz, 4-pole, 3-phase, delta connected alternator, driven at 1500 rpm. The field current, I_f is measured in A. Readings taken are marked as respective (x, y) coordinates in the figure. Ratio of the unsaturated and saturated synchronous impedances $(Z_s(unsat)/Z_s(sat))$ the alternator is closest to:

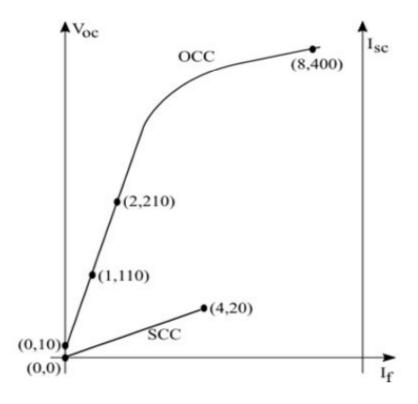


Fig. 13.

- a) 2.100
- b) 2.025
- c) 2.000
- d) 1.000

(GATE EE 2020)

Q.54 Let \hat{a}_x and \hat{a}_y be unit vectors along x and y directions, respectively. A vector function is given by

$$\mathbf{F} = \hat{a}_y y - \hat{a}_x x$$

The line integral of the above function

$$\int_C \mathbf{F} \cdot d\mathbf{l}$$

along the curve C, which follows the parabola $y = x^2$ as shown below is _____ (roundedoffto2decimalplacetimes)

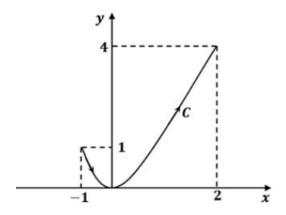


Fig. 14.

Q.55 A resistor and a capacitor are connected in series to a 10 V dc supply through a switch. The switch is closed at t = 0, and the capacitor voltage is found to cross 0 V at $t = 0.4\tau$, where τ is the circuit time constant. The absolute value of percentage change required in the initial capacitor voltage if the zero crossing has to happen at $t = 0.2\tau$ is ______ (roundedof fto 2 decimal places).

(GATE EE 2020)

Q.56 The figure below shows the per-phase Open Circuit Characteristics (measured in V) and Short Circuit Characteristics (measured in A) of a 14 kVA, 400 V, 50 Hz, 4-pole, 3-phase, delta connected alternator, driven at 1500 rpm. The field current, I_f is measured in A. Readings taken are marked as respective (x, y) coordinates in the figure. Ratio of the unsaturated and saturated synchronous impedances $(Z_s(unsat)/Z_s(sat))$ of the alternator is closest to:

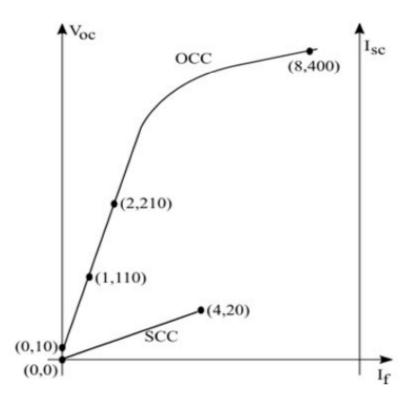


Fig. 15.

- a) 2.100
- b) 2.025
- c) 2.000
- d) 1.000

Q.57 Let \hat{a}_x and \hat{a}_y be unit vectors along x and y directions, respectively. A vector function is given by

$$\mathbf{F} = \hat{a}_y y - \hat{a}_x x$$

The line integral of the above function

$$\int_C \mathbf{F} \cdot d\mathbf{l}$$

along the curve C, which follows the parabola $y = x^2$ as shown below is _____ (rounded of fto 2 decimal place)

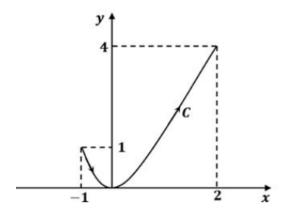


Fig. 16.

(GATE EE 2020)

Q.58 A resistor and a capacitor are connected in series to a 10 V dc supply through a switch. The switch is closed at t = 0, and the capacitor voltage is found to cross 0 V at $t = 0.4\tau$, where τ is the circuit time constant. The absolute value of percentage change required in the initial capacitor voltage if the zero crossing has to happen at $t = 0.2\tau$ is ______ (roundedof fto2decimal places).

(GATE EE 2020)

Q.59 A cylindrical rotor synchronous generator with constant real power output and constant terminal voltage is supplying 100 A current to a 0.9 lagging power factor load. An ideal reactor is now connected in parallel with the load, as a result of which the total lagging reactive power requirement of the load is twice the previous value while the real power remains unchanged. The armature current is now ______ A (roundedof fto2decimalplaces).

(GATE EE 2020)

Q.60 Bus 1 with voltage magnitude $V_1 = 1.1$ pu is sending reactive power Q_{12} towards bus 2 with voltage magnitude $V_2 = 1$ pu through a lossless transmission line of reactance X. Keeping the voltage at bus 2 fixed at 1 pu, magnitude of voltage at bus 1 is changed, so that the reactive power Q_{12} sent from bus 1 is increased by 20%. Real power flow through the line under both the conditions is zero. The new value of the voltage magnitude, V_1 , in pu (roundedof fto2decimalplaces), at bus 1 is ______.



Fig. 17.

(GATE EE 2020)

Q.61 Windings A, B and C have 20 turns each and are wound on the same iron core as shown, along with winding X which has 2 turns. The figure shows the sense (clockwise/anti-clockwise) of each of the windings only and does not reflect the exact number of turns. If windings A, B and C are supplied with balanced 3-phase voltages at 50 Hz and there is no core saturation, the no-load RMS voltage (in V, rounded off to 2 decimal places) across winding X is ______.

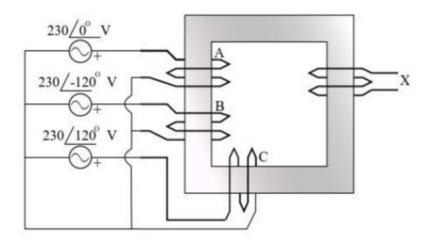


Fig. 18.

- Q.62 A cylindrical rotor synchronous generator has steady state synchronous reactance of 0.7 pu and subtransient reactance of 0.2 pu. It is operating at (1 + j0) pu terminal voltage with an internal emf of (1 + j0.7) pu. Following a three-phase solid short circuit fault at the terminals of the generator, the magnitude of the subtransient internal emf (rounded off to 2 decimal places) is _____ pu. (GATE EE 2020)
- Q.63 In the dc-dc converter circuit shown, switch Q is switched at a frequency of 10 kHz with a duty ratio of 0.6. All components of the circuit are ideal, and the initial current in the inductor is zero. Energy stored in the inductor in mJ (roundedof fto2decimalplaces) at the end of 10 complete switching cycles is

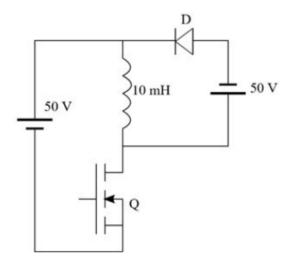


Fig. 19.

(GATE EE 2020)

Q.64 A single-phase, full-bridge, fully controlled thyristor rectifier feeds a load comprising a 10 Ω resistance in series with a very large inductance. The rectifier is fed from an ideal 230 V, 50 Hz sinusoidal source through cables which have negligible internal impedance and a total inductance of 2.28 mH. If the thyristors are triggered at an angle $\alpha = 45^{\circ}$, the commutation overlap angle in degree (roundedof fto2decimalplaces) is ______.

Q.65 A non-ideal Si-based pn junction diode is tested by sweeping the bias applied across its terminals from -5 V to +5 V. The effective thermal voltage, V_T , for the diode is measured to be (29 ± 2) mV. The resolution of the voltage source in the measurement range is 1 mV. The percentage uncertainty (rounded off to 2 decimal places) in the measured current at a bias voltage of 0.02 V is ______.

(GATE EE 2020)

Q.66 The temperature of the coolant oil bath for a transformer is monitored using the circuit shown. It contains a thermistor with a temperature-dependent resistance, $R_{\text{thermistor}} = 2(1 + \alpha T) \text{ k}\Omega$, where T is the temperature in °C. The temperature coefficient, α , is $-(4 \pm 0.25) \%/$ °C. Circuit parameters: $R_1 = 1 \text{ k}\Omega$, $R_2 = 1.3 \text{ k}\Omega$, $R_3 = 2.6 \text{ k}\Omega$. The error in the output signal (in V, rounded off to 2 decimal places) at 150°C is ______.

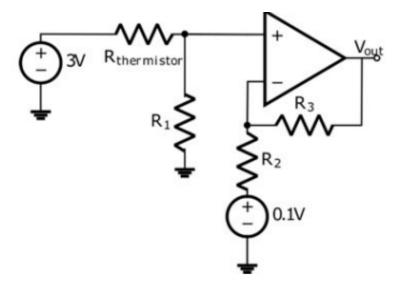


Fig. 20.

(GATE EE 2020)

Q.67 An 8085 microprocessor accesses two memory locations (2001H) and (2002H), that contain 8-bit numbers 98H and B1H, respectively. The following program is executed:

LXI H,2001H

MVI A, 21H

INX H

ADD M

INX H

MOV M, A

HLT

At the end of this program, the memory location 2003H contains the number in decimal (base 10) form _____.

(GATE EE 2020)

Q.68 A conducting square loop of side length 1 m is placed at a distance of 1 m from a long straight wire carrying a current I = 2 A as shown below. The mutual inductance, in nH (rounded off to 2 decimal places), between the conducting loop and the long wire is ______.

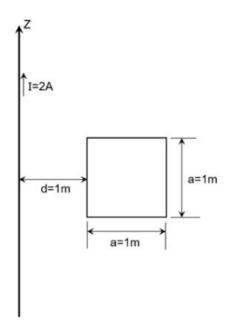


Fig. 21.