```
`timescale 1ns / 1ps
// Company:
// Engineer:
//
module m2 1x8( //m2 1x8(.in0 [7:0], .in1
[7:0], .sel, out [7:0])
   input [7:0] in0,
   input [7:0] in1,
  input sel,
  output [7:0] out
  );
  assign out = sel ? in1 : in0; //if
sel=1, then out = in1, else in0
```

endmodule