

# All three RV32 Store Instructions

Encodes data size of store operation

			funct3	opcode		
imm[11:5]	rs2	rs1	000	imm[4:0]	0100011	sb
imm[11:5]	rs2	rs1	001	imm[4:0]	0100011	sh
imm[11:5]	rs2	rs1	010	imm[4:0]	0100011	sw

No sign/zero extending for store!  
We only write to memory  
the data width specified.

Same data width fields as  
load instructions.