All nine RV32 I-Format Arithmetic Instructions

Same funct3 fields as corresponding R-format operation (remember, no `subi`)

			funct3		opcode	
imm[11:0]		rs1	000	rd	0010011	addi
imm[11:0]		rs1	010	rd	0010011	slti
imm[11:0]		rs1	011	rd	0010011	sltiu
imm[11:0]		rs1	100	rd	0010011	xori
imm[11:0]		rs1	110	rd	0010011	ori
imm[11:0]		rs1	111	rd	0010011	andi
0000000	shamt	rs1	001	rd	0010011	slli
0000000	shamt	rs1	101	rd	0010011	srli
0100000	shamt	rs1	101	rd	0010011	srai

"Shift by Immediate" instructions encode the <u>**shift amount**</u> in the lower-order 5 bits.

▫ We can only (meaningfully) shift 32-b word by 0-31 positions.

▫ One higher-order immediate bit used for sign extend (`srli` vs. `srai`). Same bit position as in R-Format!

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