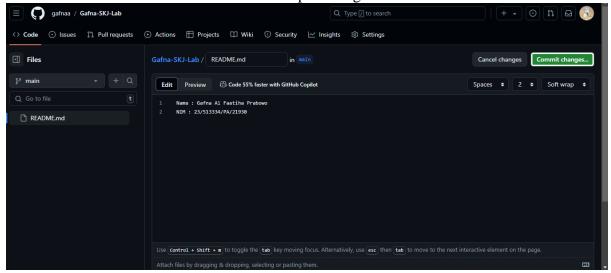
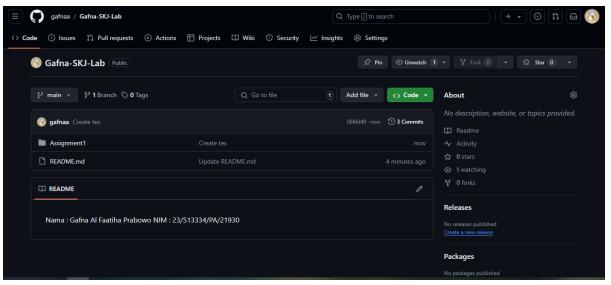
# Tugas 1 Prak-SKJ

1. Membuat akun Github dan melakukan beberapa settingan.



Membuat repositori, lalu membuat file readme.md yang berisi nama dan nim saya.



Membuat folder Assignment1 pada repositori.

Link github : <a href="https://github.com/gafnaa/Gafna-SKJ-Lab/">https://github.com/gafnaa/Gafna-SKJ-Lab/</a>

- 2. Tugas C++ ke Assembly
  - Membuat Program C++ sederhana

```
#include <iostream>
using namespace std;
int main()

int a,b;
cin >> a >> b;
int c = a+b;
cout << c;
return 0;
}</pre>
```

Contoh output:

```
1 2
3
...Program finished with exit code 0
Press ENTER to exit console.
```

• Melakukan compiling code dengan menuliskan syntax berikut pada terminal

```
4.cpp
                    Microsoft Windows [Version 10.0.14393]
■ 4.exe
                     (c) 2016 Microsoft Corporation. All rights reserved.
📆 ika.cpp
                    H:\1DATA>g++ -o tescpp tescpp.cpp
ika.exe
ikiaq.cpp
                    H:\1DATA>
ikiaq.exe
tes.cpp
tes.exe
tescpp.cpp
Untitled1.cpp
Untitled1.exe
tescpp.exe
```

• Melakukan diassemble code, dengan menuliskan syntax berikut

```
\1DATA>obidump -d tescpp.exe
                           file format pei-x86-64
sassembly of section .text:
     000140001000 <_mingw_invalidParameterHandler>:
40001000: 3 ret
40001001: 0f 1f 44 00 00 nopl 0x0(%rax,%rax,1)
40001001: 06 2e 0f 1f 84 00 00 cs nopw 0x0(%rax,%rax,1)
40001001: 00 00 00 00
         $0v28, Krsp
0v33d5(Krip), Krax # 1:
Krex, Keck
$0v1, (Krax)
0v33d6(Krip), Krax # 1:
$0v1, (Krax)
$0v1, (Krax)
$0v3, (Krax)
$0v33d6(Krip), Krax # 1:
$0v1, (Krax)
$0v33d6(Krip), Krax # 1:
$0v33d6(Krip), Krax # 1:
$0v33d4(, Krax)
$1:
$0v33d4(, Krax)
$0v33d6(Krip), Krax
$1:
$0v33d4(, Krax)
$0v33d6(Krip), Krax
$1:
$0v33d6(Krip), Krax
$1:
$0v33d6(Krip), Krax
$1:
$0v33d6(Krip), Krax
                                                                                                                                                              # 1400043f0 <.refptr. mingw initltsdrot force>
                                                                                                                                                              # 140004400 <.refptr.__mingw_initltsdyn_force>
                                                                                                                                                             # 140004390 <.refptr.__image_base__>
                                                                                                    eax
0b0 <pre_c_init+0xa0>
                                                                                                     # 140004320 <.refptr._MINGW_INSTALL_DEBUG_MATHERR
                                                                                                                        f1 
      1400010aa:
                                               48 83 c4 28
                                                                                                                                add
                                                                                                                                                       $0x28,%rsp
      1400010ae:
                                             90
b9 02 00 00 00
e8 7e 17 00 00
eb bb
0f 1f 40 00
66 81 fa 0b 01
74 39
66 81 fa 0b 02
75 88
83 b8 84 00 00 00 0e
0f 86 7b ff ff ff
8b 90 f8 00 00 00
                                                                                                                               nop
mov
      1400010af:
                                               90
      1400010b0:
                                                                                                                                                       $0x2,%ecx
                                                                                                                                call
                                                                                                                                                       140002838 <__set_app_type>
140001077 <pre_c_init+0x67>
      1400010b5:
                                                                                                                                jmp
nopl
movzwl
                                                                                                                                                       0x0(%rax)
0x18(%rax),%edx
$0x10b,%dx
140001104 <pre_c_init+0xf4>
      1400010bc:
       1400010c0:
      1400010c4:
                                                                                                                                cmp
                                                                                                                                                       cmp
jne
      1400010ch:
       1400010d0:
                                                                                                                               cmpl
jbe
      1400010d2:
      1400010df:
                                                                                                                                mov
                                              85 d2

96 95 c1

99 69 ff ff ff

48 8d 0d d8 06 00 00

88 13 0e 00 00
                                                                                                                                                       %ecx,%ecx
%edx,%edx
       1400010e5:
                                                                                                                                test
      1400010e7:
       1400010e9:
                                                                                                                                                       jmp
lea
call
      1400010ec:
     1400010f1:
1400010f8:
      1400010fd:
                                               31 c0
48 83 c4 28
                                                                                                                               xor
add
                                                                                                                                                       %eax,%eax
$0x28,%rsp
      1400010ff:
                                              63 C4 28
C3
83 78 74 0e
0f 86 4c ff ff ff
44 8b 80 e8 00 00 00
     140001103:
140001104:
                                                                                                                                                     $0xe,0x74(%rax)
14000105a <pre_c_init+0x4a>
0xe8(%rax),%r8d
                                                                                                                               cmp1
     140001108:
14000110e:
                                                                                                                                jbe
                                                                                                                                mov
     140001115:
140001117:
                                               31 c9
45 85 c0
                                                                                                                                xor
test
                                                                                                                                                       %ecx,%ecx
%r8d,%r8d
                                              45 85 C0

0f 95 c1

e9 38 ff ff ff

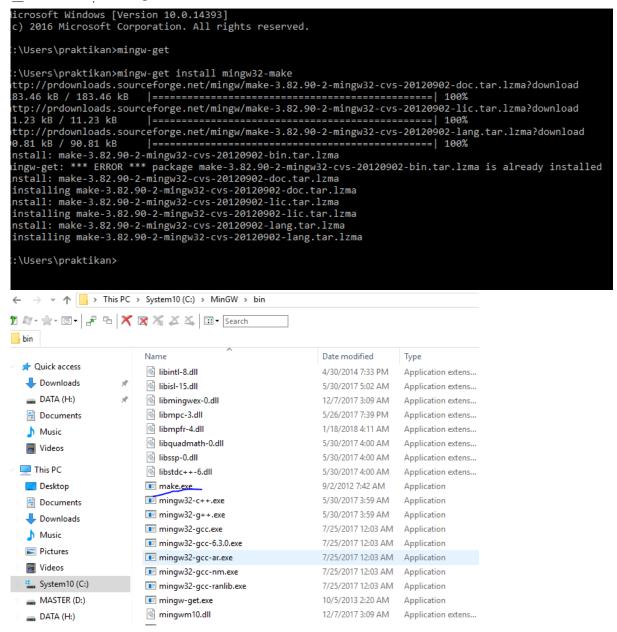
0f 1f 40 00

66 2e 0f 1f 84 00 00

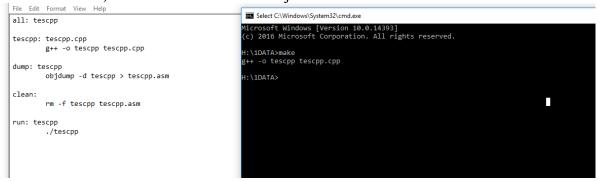
00 00 00
     14000111a:
14000111d:
                                                                                                                                                      %cl
14000105a <pre_c_init+0x4a>
                                                                                                                               jmp 14000105a <pre_c_i
nopl 0x0(%rax)
cs nopw 0x0(%rax,%rax,1)</pre>
     140001122:
140001126:
      14000112d:
000000140001130 cpre_cpp_init>:
140001130: 48 83 ec 38
140001134: 48 80 05 a5 33 00 00
140001142: 48 8d 15 d7 5e 00 00
140001149: 48 8d 0d d8 5e 00 00
140001150: 8b 00
140001152: 89 05 ac 5e 00 00
140001158: 48 8d 05 a5 5e 00 00
140001158: 48 8d 05 a5 5e 00 00
140001158: 48 8d 05 a5 5e 00 00
140001156: 48 80 95 a5 5e 00 00
140001166: 48 8b 05 35 33 00 00
140001166: 44 8b 08
140001166: 68 b5 16 00 00
                                                                                                                                                       $0x38,%rsp
                                                                                                                                                       $\text{9}\text{9}\text{9}\text{8}\text{,}\text{rsp}\text{9}\text{9}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\text{7}\te
                                                                                                                               mov
lea
                                                                                                                                                                                                                                     # 1400044e0 <.refptr._newmode>
# 140007018 <envp>
                                                                                                                                lea
lea
                                                                                                                                                                                                                                        # 140007020 <argv> # 140007028 <argc>
                                                                                                                               mov
mov
                                                                                                                                                       (%rax),%eax
%eax,0x5eac(%rip)
                                                                                                                                                                                                                                        # 140007004 <startinfo>
# 140007004 <startinfo>
                                                                                                                                                      %eax,0x5ed(\ar1F)
0x5ea5(%rip),%rax #
%rax,0x20(%rsp)
0x3335(%rip),%rax #
(%rax),%r9d
140002828 <__getmainargs>
                                                                                                                                lea
                                                                                                                                                                                                                                         # 1400044a0 <.refptr._dowildcard>
                                                                                                                                mov
                                                                                                                               mov
call
                                               e8 b5 16 00 00
      14000116e:
                                              90
48 83 c4 38
       140001173:
                                                                                                                               nop
add
                                                                                                                                                       $0x38,%rsp
     140001174:
                                               c3
0f 1f 80 00 00 00 00
                                                                                                                                                       0x0(%rax)
                                                                                                                                nopl
  00000140001180 <__tmainCRTStartup>:
140001180: 41 55
140001182: 41 54
                                                                                                                                                       %r13
%r12
                                                                                                                                push
     140001184:
140001185:
                                                                                                                                                       %rbp
%rdi
                                                                                                                                push
                                                                                                                                                       %rsi
%rbx
      140001186:
     140001187:
                                                                                                                                push
     140001188:
14000118f:
                                               48 81 ec 98 00 00 00
31 c0
                                                                                                                                                       $0x98,%rsp
%eax,%eax
                                                                                                                                 sub
```

### Membuat Makefile

1) Menginstall 'make' command



# 2) Membuat makefile lalu dijalankan



### 3. Assembly ke C++

• Analisis kode assembly

```
section .data
num1 dw 5
num2 dw 10
result dw 0

section .text
global _start

_start:
mov ax, [num1]
imul ax, [num2]
mov [result], ax

; Exit the program
mov eax, 1
xor ebx, ebx
int 0x80
```

# Deskripsi dan penjelasan tiap line

- **section .data** = sebagai tanda bahwa bagian tersebut akan digunakan untuk mendeklarasikan variabel
- **num1 dw 5** = menginisialisasi nilai 5 ke variabel *num1* dan mengalokasikannya ke memori. (dw = define word, menisialisasi data 2-byte)
- **num2 dw 10** = menginisialisasi nilai 10 ke variabel *num2* dan mengalokasikannya ke memori. (dw = define word, menisialisasi data 2-byte)
- **result dw 0** = menginisialisasi nilai 0 ke variabel *result* dan mengalokasikannya ke memori. (dw = define word, menisialisasi data 2-byte)
- **section .text** = sebagai tanda bahwa bagian tersebut akan digunakan untuk menyimpan instruksi program yang akan dieksekusi CPU
- **global\_start** = mendeklarasikan label \_start
- **\_start** = titik awal eksekusi program
- **mov ax. [num1]** = memindahkan nilai di variabel num1(5) ke register ax
- **imul ax, [num2]** = mengalikan nilai di register ax(5) dengan nilai di variabel num2(10)
- **mov** [result], ax = memidahkan nilai di register ax ke variabel result
- **;Exit the program** = tanda bahwa bagian tersebut berisi instruksi-instruksi yang digunakan untuk menghentikan eksekusi program
- mov eax, 1 = memindahkan nilai 1 ke register eax
- **xor ebx, ebx** = melakukan operasi xor antara ebx dengan ebx juga
- int 0x80 = memanggil interrupt 0x80 untuk mengeseksui perintah exit

Menulis kode C++ yang ekuivalen

```
#include <iostream>
int main() {
    int16_t num1 = 5;
    int16_t num2 = 10;
    int16_t result = 0;

    result = num1 * num2;

    // Exit the program
    return 0;
}
```

Di sini saya menggunakan tipe data int16\_t yang artinya hanya mendifiniskan data integer yang bernilai 16-bit(2-byte)

• Buat makefile

```
This PC > Tugas (E:) > Praktikum SKJ > Minggu 1
                                                                           Microsoft Windows [Version 10.0.19045.4780]
(c) Microsoft Corporation. All rights reserved.
                                                                           C
E:\Praktikum SKJ\Minggu 1>make
Ag++ -o to_asm to_asm.cpp
Fi
          main.cpp
          makefile to_asm.cpp
                                                    30/08/2024 07.24
                                                                           CE:\Praktikum SKJ\Minggu 1>
                                                    30/08/2024 07.25
                           makefile - Notepad
                                                                                                                                         - □ ×
                           File Edit Format View Help
                           all: to_asm
                           to_asm: to_asm.cpp
g++ -o to_asm to_asm.cpp
                           dump: to_asm
objdump -d to_asm > to_asm.cpp
                           clean:
                                    rm -f to_asm to_asm.asm
                           run: to_asm
                                     ./to_asm
```