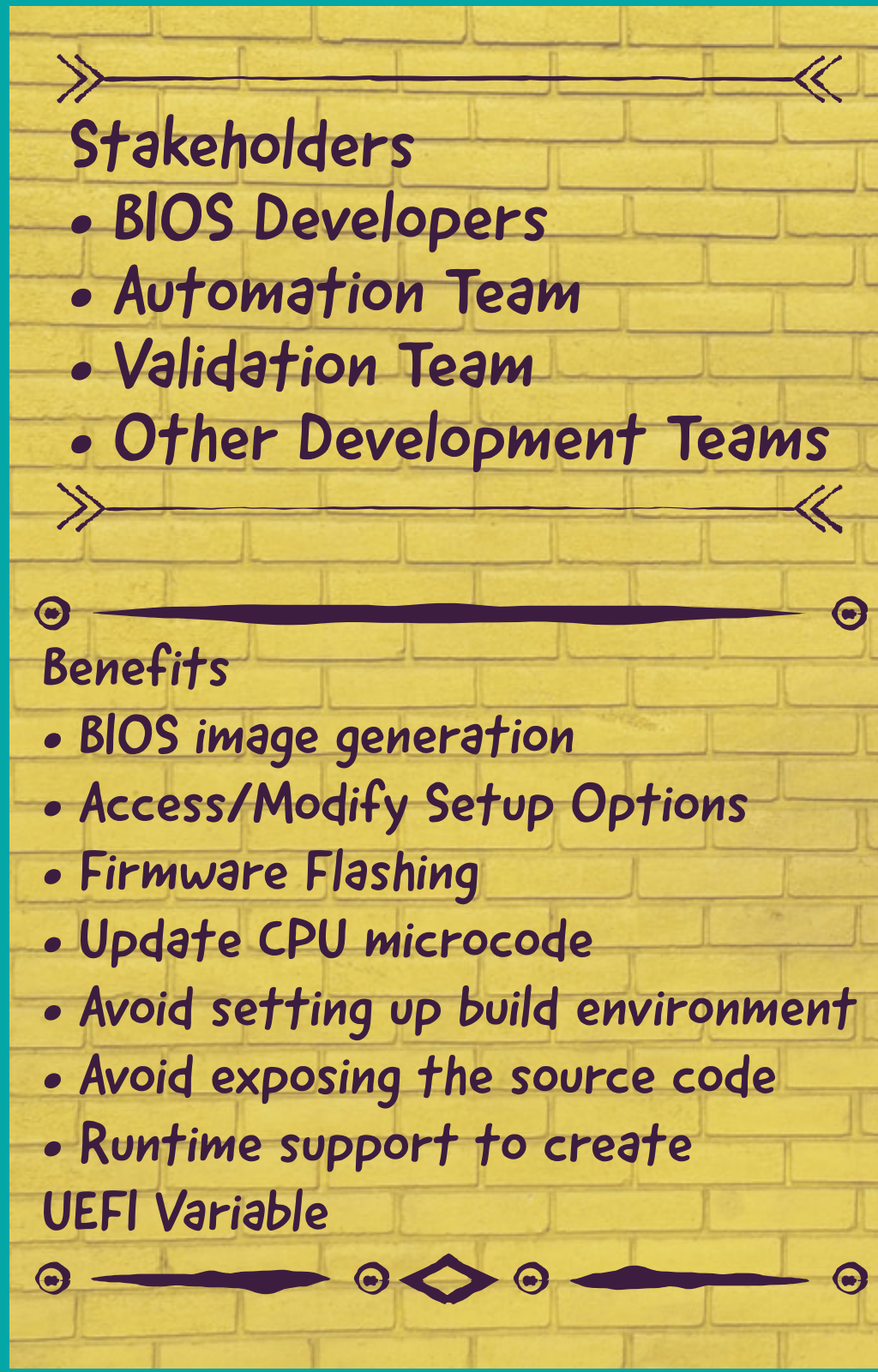


# IP Independent Generic Framework to accelerate Development Process Iteration

Gahan M. Saraiya (18MCEC10)  
M. Tech, Computer Science and Engineering,  
Institute of Technology, Nirma University  
Internal Guide: Prof. Dvijesh Bhatt  
External Guide: Amol Shinde

## “ Setup Knobs Modification ”

- Information parsing
- Information Simulation
- Realtime sync
- Toggle DEPEX
- Works on SUT
- Patch offline Image



## “ Runtime UEFI Variable Creation ”

- Avoid providing native BIOS Support
- No initial setup overhead
- Interactive Web GUI
- Quick Modification of Variable

## “ Parsing of BIOS image ”

- Human Readable interpretation
- GUID Lookup
- Verification of the existence of a module by GUID
- Storing the image file system content by GUID
- Summarizing BIOS Changes
- Works on SUT