



National Institute of Technology, Rourkela

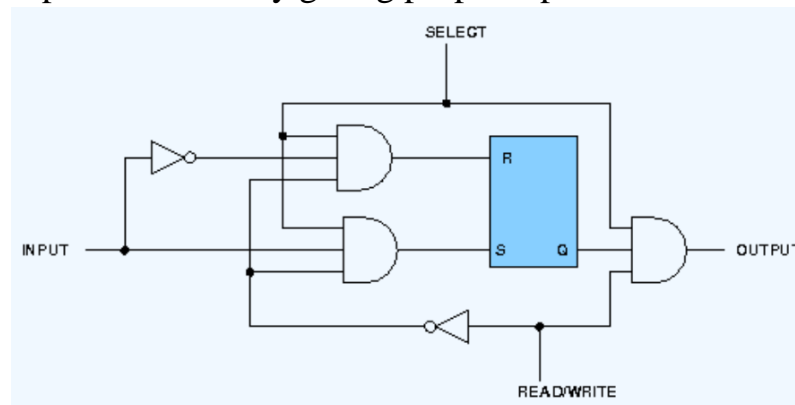
CS2074: Computer Organization Laboratory

SPRING 2021-2022

Assignment 8: Memory Design

Due: March 25, 2022, 16:15 hours

1. Design a binary RAM cell using a S-R flip-flop, AND gates, NOT gates having select, read/write, input, output and test it by giving proper input.



2. Design a 4X4 RAM memory which will have 4 words using binary RAM cells designed in question 1, decoder with enable, OR gates and test it by giving proper input.

