Nano-Power, CMOS Input, RRIO, Push-Pull Output Comparator

FEATURES

- **Low supply current 400nA (TYP) at Vs = 1.4V**
- **Low input offset voltage: Vos(max) = ±3mV**
- **Rail-to-Rail Input and output**
- **SUPPLY RANGE: +1.4V to +5.5V**
- **SPECIFIED UP TO +125°C**
- *Micro* **SIZE PACKAGES: SOIC-8(SOP8), MSOP-8**

APPLICATIONS

- **OVERVOLTAGE AND UNDERVOLTAGE DETECTION**
- **MULTIVIBRATORS**
- **OVERCURRENT DETECTION**
- **SYSTEM MONITORING**
- **BATTERY POWERED SYSTEM**

DESCRIPTION

The RS8905 offers a wide supply range, It is a dual low power comparator with a typical power supply current of 400nA (per channel), and rail-to-rail inputs. All of these features come in industry-standard and extremely small packages, making this device an excellent choice for low-voltage and low-power applications for portable electronics and industrial systems.

Featuring a push-pull output stage, the RS8905 allows for operation with absolute minimum power consumption when driving any capacitive or resistive load.
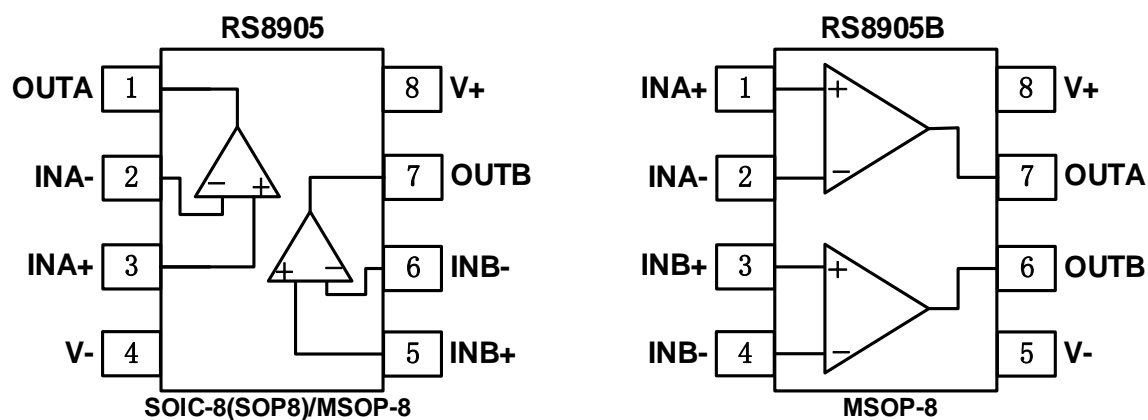
The devices are ideal for system monitoring, include tablets, portable medical, smart phones. The RS8905 is specified at the full temperature range of −40°C to +125°C under single power supplies of 1.4V to 5.5V.

Device Information [1]

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RS8905	SOIC-8	4.90mm×3.90mm
	MSOP-8	3.00mm×3.00mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

Pin Configuration and Functions (Top View)

RS8905

OUTA	1		8	V+
INA-	2		7	OUTB
INA+	3		6	INB-
V-	4		5	INB+

SOIC-8(SOP8)/MSOP-8

RS8905B

INA+	1		8	V+
INA-	2		7	OUTA
INB+	3		6	OUTB
INB-	4		5	V-

MSOP-8

Pin Description

NAME	PIN		I/O (1)	DESCRIPTION
	RS8905B	RS8905		
	MSOP-8	SOIC-8(SOP8)/MSOP-8		
INA+	1	3	I	Noninverting input A
INA-	2	2	I	Inverting input A
INB+	3	5	I	Noninverting input B
INB-	4	6	I	Inverting input B
V-	5	4	P	Negative (lowest) power supply
OUTB	6	7	O	Output B
OUTA	7	1	O	Output A
V+	8	8	P	Positive (highest) power supply

(1)I=Input, O=Output, P=Power

SPECIFICATIONS

Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) [1]

		MIN	MAX	UNIT
Voltage	Supply, Vs=(V+) - (V-)		7	V
	Input pin (IN+, IN-) [2]	(V-)-0.5	(V+) +0.5	
	Signal output pin [3]	(V-)-0.5	(V+) +0.5	
Current	Signal input pin (IN+, IN-) [2]	-10	10	mA
	Signal output pin [3]	-55	55	mA
	Output short-circuit [4]	Continuous		
Temperature	Operating range, T_A	-40	125	°C
	Junction, T_J		150	
	Storage, T_{stg}	-65	150	

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

(2) Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current-limited to 10mA or less.

(3) Output terminals are diode-clamped to the power-supply rails. Output signals that can swing more than 0.5V beyond the supply rails should be current-limited to ±55mA or less.

(4) Short-circuit to ground, one amplifier per package.

ESD Ratings

			VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	Human-body model (HBM)	±3000	V
		Machine Model (MM)	±400	

Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
Supply voltage，Vs= (V+) - (V-)	Single-supply	1.4		5.5	V
	Dual-supply	±0.7		±2.75	

Thermal Information:

THERMAL METRIC [1]		SOIC-8(SOP-8)	MSOP-8	UNIT
		8PINS		
$R_{\Theta JA}$	Junction-to-ambient thermal resistance	116	165	°C/W
$R_{\Theta JC(top)}$	Junction-to-case(top) thermal resistance	60	53	°C/W
$R_{\Theta JB}$	Junction-to-board thermal resistance	56	87	°C/W
Ψ_{JT}	Junction-to-top characterization parameter	12.8	4.9	°C/W
Ψ_{JB}	Junction-to-board characterization parameter	98.3	85	°C/W
$R_{\Theta JC(bot)}$	Junction-to-case(bottom) thermal resistance	N/A	N/A	°C/W

PACKAGE/ORDERING INFORMATION

Orderable Device	Package Type	Pin	Channel	Op Temp(℃)	Device Marking[1]	Package Qty
RS8905XK	SOIC-8(SOP-8)	8	2	-40℃~125℃	RS8905	Tape and Reel,4000
RS8905XM	MSOP-8	8	2	-40℃~125℃	RS8905	Tape and Reel,4000
RS8905BXM	MSOP-8	8	2	-40℃~125℃	RS8905B	Tape and Reel,4000

NOTE:

(1) There may be additional marking, which relates to the lot trace code information(data code and vendor code)，the logo or the environmental category on the device.
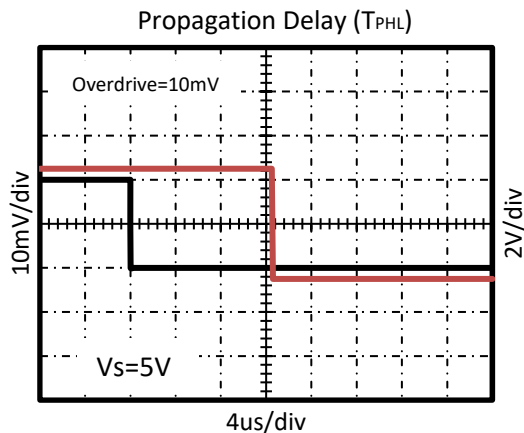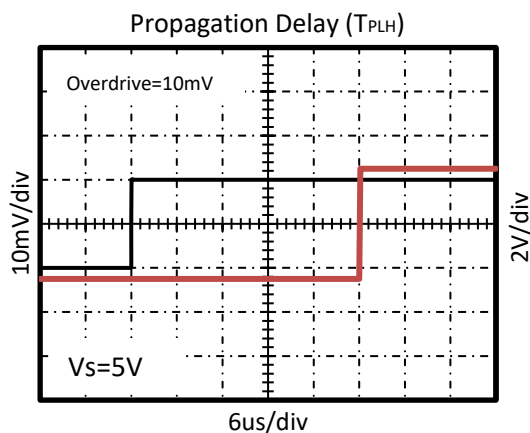
ELECTRICAL CHARACTERISTICS

(At T_A = +25°C, Vs=1.4V to 5.5V, V_{CM}=Vs/2, C_L = 15pF, unless otherwise noted.)

PARAMETER		CONDITIONS		RS8905/RS8905B			UINTS
				MIN	TYP	MAX	
POWER SUPPLY							
Vs	Operating Voltage Range			1.4		5.5	V
I_Q /Amp	Quiescent Current Per Channel				400	1500	nA
PSRR	Power-Supply Rejection Ratio	Vs=1.4V to 5.5V, V_{CM}=(V)+0.5V			70		dB
INPUT							
Vos	Input Offset Voltage	V_{CM} =Vs/2	Vs =1.4V	-6	1	6	mV
			Vs =5.0V	-3	1	3	
ΔVos/ΔT	Input Offset Voltage Drift	V_{CM} =Vs/2, -40°C ≤ T_A ≤125°C			2		µV/°C
IB	Input Bias Current				1	10	pA
V_{CM}	Common-Mode Voltage Range	T_A = −40°C to 125°C		(V-)-0.1		(V+)+0.1	V
CMRR	Common-Mode Rejection Ratio	Vs=5.5V, V_{CM} =-0.1 to 5.6V			70		dB
OUTPUT							
V_{OH}	Output Swing From Upper Rail	Vs=1.4V, Io=0.1mA			70	75	mV
		Vs=5.0V, Io=2.5mA			140	170	mV
V_{OL}	Output Swing From Lower Rail	Vs=1.4V, Io=-0.1mA			35	40	mV
		Vs=5.0V, Io=-2.5mA			85	115	mV
I_{SC}	Short Circuit Sink Current	Vs=5.0V			42		mA
	Short Circuit Source Current	Vs=5.0V			38		mA
SWITCHING							
T_{PHL}	Propagation Delay H To L	Vs = 5.0 V, Overdrive = 10 mV			13		µs
		Vs = 5.0 V, Overdrive = 100 mV			9		
		Vs = 2.5 V, Overdrive = 10 mV			12		
		Vs = 2.5 V, Overdrive = 100 mV			8		
		Vs = 1.4 V, Overdrive = 10 mV			13		
		Vs = 1.4 V, Overdrive = 100 mV			9		
T_{PLH}	Propagation Delay L To H	Vs = 5.0 V, Overdrive = 10 mV			30		
		Vs = 5.0 V, Overdrive = 100 mV			40		
		Vs = 2.5 V, Overdrive = 10 mV			22		
		Vs = 2.5 V, Overdrive = 100 mV			35		
		Vs = 1.4 V, Overdrive = 10 mV			22		
		Vs = 1.4 V, Overdrive = 100 mV			35		
T_R	Rise Time	Overdrive = 100 mV			240		ns
T_F	Fall Time	Overdrive = 100 mV			260		ns

TYPICAL CHARACTERISTICS

At T_A = +25°C, Vs=5V, V_{CM} = V_S/2, C_L=15pF unless otherwise noted.

Propagation Delay (T_{PLH})

Overdrive=10mV

10mV/div

2V/div

Vs=5V

6us/div

Propagation Delay (T_{PHL})

Overdrive=10mV

10mV/div

2V/div

Vs=5V

4us/div

SINK CURRENT vs TEMPERATURE

Sink Current(mA)

Temperature(C)

SOURCE CURRENT vs TEMPERATURE

Source Current(mA)

Temperature(C)
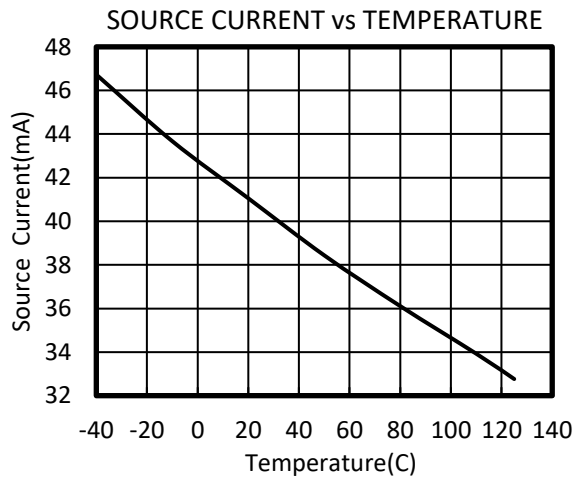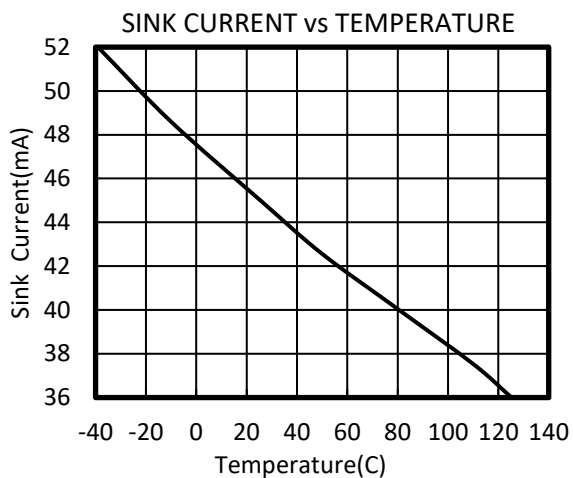
Detailed Description

Overview

The RS8905 devices are double-channel, Nano-power comparators with a push-pull output stage. Operating from 1.4V to 5.5V and consuming only 400nA per channel. The push-pull output of the RS8905 supports rail-to-rail output swing and interfaces with TTL/CMOS logic.

Functional Block Diagram
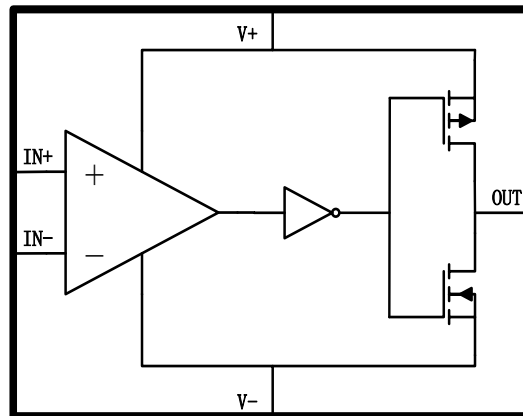
Feature Description

The RS8905 devices are Nano-Power comparators that can operate at low voltages. The RS8905 feature a rail-to-rail input stage capable of operating up to 100 mV beyond the VCC power supply rail.

Input Stage

The RS8905 has rail-to-rail input common-mode voltage range. It can operate at any differential input voltage within this limit as long as the differential voltage is greater than zero. A differential input of zero volts may result in oscillation.

The differential input stage of the comparator is a pair of PMOS and NMOS transistors, therefore, no current flows into the device. The input bias current measured is the leakage current in the MOS transistors and input protection diodes. This low bias current allows the comparator to interface with a variety of circuitry and devices with minimal concern about matching the input resistances.

Output Stage

The RS8905 has a MOS push-pull rail-to-rail output stage. The push-pull transistor configuration of the output keeps the total system power consumption to a minimum. The only current consumed by the RS8905 is the less than 1µA supply current and the current going directly into the load. No power is wasted through the pullup resistor when the output is low. The output stage is specifically designed with dead time between the time when one transistor is turned off and the other is turned on (break-before-make) to minimize shoot through currents. The internal logic controls the break-before-make timing of the output transistors. The break-before-make delay varies with temperature and power condition.

Output Current

Even though the RS8905 uses less than 1µA supply current, the outputs can drive very large currents. The RS8905 can source up to 38mA and can sink up to 42mA when operated at 5V supply. This large current handling capability allows driving heavy loads directly.

Application Information

The RS8905 is an ultra-low-power comparator with a typical power supply current of 400nA per channel. It has the best-in class power supply current versus propagation delay. The propagation delay is as low as 9µs with 100mV overdrive at 1.4V supply.

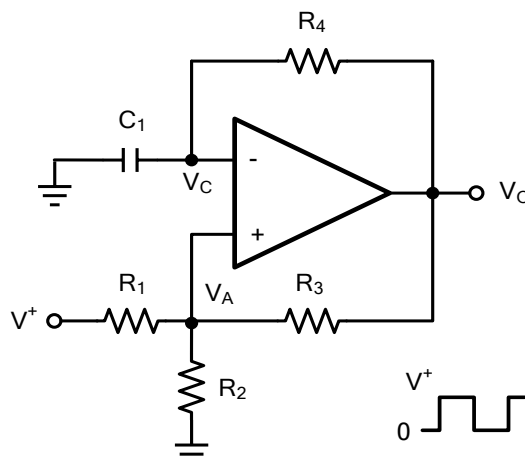## Typical Applications

Square Wave Generator

Figure 1. Square Wave Generator Schematic

Design Requirements

A typical application for a comparator is as a square wave oscillator. The circuit in Figure 1 generates a square wave whose period is set by the RC time constant of the capacitor C1 and resistor R4. The maximum frequency is limited by the large signal propagation delay of the comparator and by the capacitive loading at the output, which limits the output slew rate.
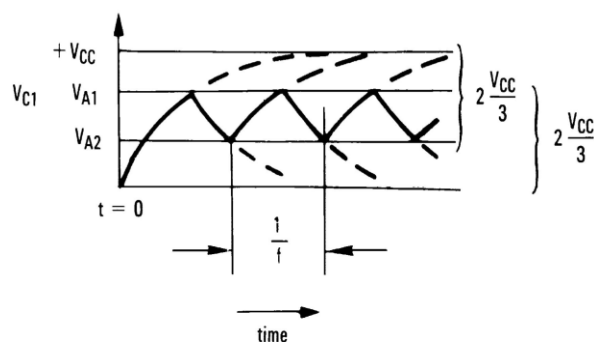
Detailed Design Procedure

Figure 2. Square Wave Oscillator

Typical Applications(continued)

Consider the output of Figure 2 to be high to analyze the circuit. That implies that the inverted input (V_C) is lower than the noninverting input (V_A). This causes the C_1 to be charged through R_4, and the voltage V_C increases until it is equal to the noninverting input. The value of V_A at this point is in Equation 1.

$$V_{A1} = \frac{V_{CC} \times R_2}{R_2 + R_1 \| R_3}$$
....(1)

If $R_1 = R_2 = R_3$ then $V_{A1} = 2\,V_{CC}/3$

At this point the comparator switches pulling down the output to the negative rail. The value of V_A at this point, as shown in Equation 2:

$$V_{A2} = \frac{V_{CC}(R_2 \| R_3)}{R_1 + (R_2 \| R_3)}$$
....(2)

If $R_1 = R_2 = R_3$ then $V_{A2} = V_{CC}/3$ The capacitor C_1 now discharges through R_4, and the voltage V_C decreases until it is equal to V_{A2}, at which point the comparator switches again, bringing it back to the initial stage. The time period is equal to twice the time it takes to discharge C_1 from $2\,V_{CC}/3$ to $V_{CC}/3$, which is given by $R_4 C_1 \times$ ln2. Hence the formula for the frequency is given by Equation 3:

F=1/(2×R_4×C_1×ln2)...(3)

Application Curves

Figure 3 shows the simulated results of an oscillator using the following values:

1. $R_1 = R_2 = R_3 = R_4 = 100\,k\Omega$
2. $C_1 = 100\,pF$, $C_L = 20\,pF$
3. V+ = 5 V, V− = GND
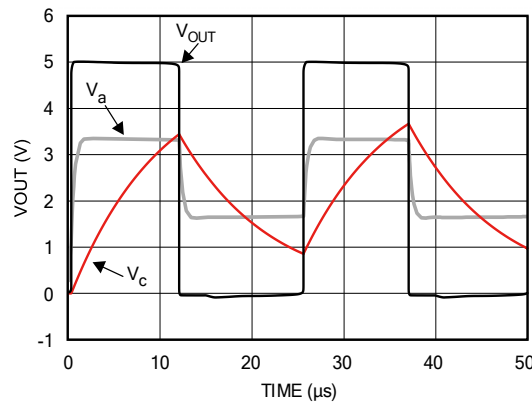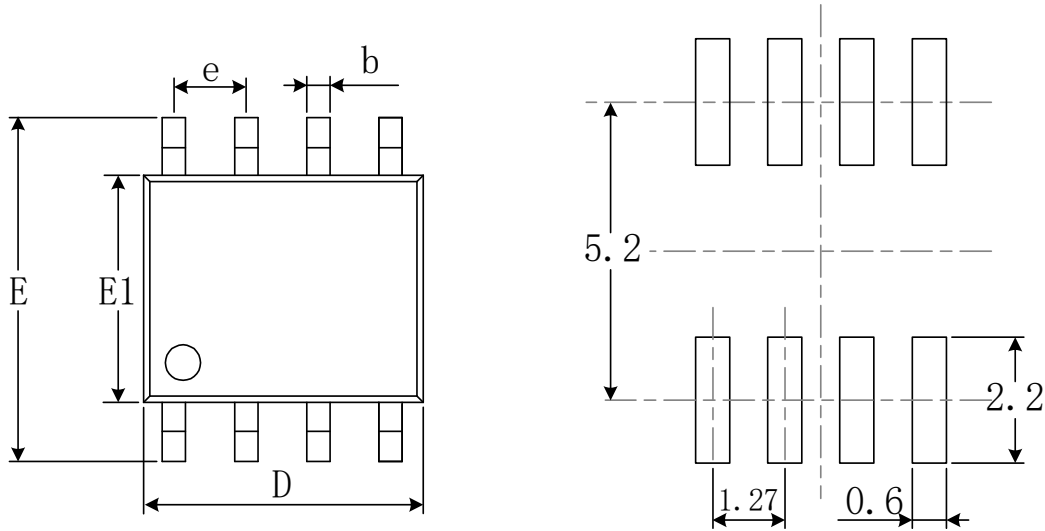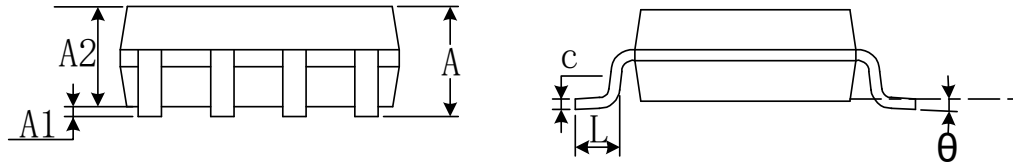4. C_{STRAY} (not shown) from Va to GND = 10 pF

Figure 3. Square Wave Oscillator Output Waveform
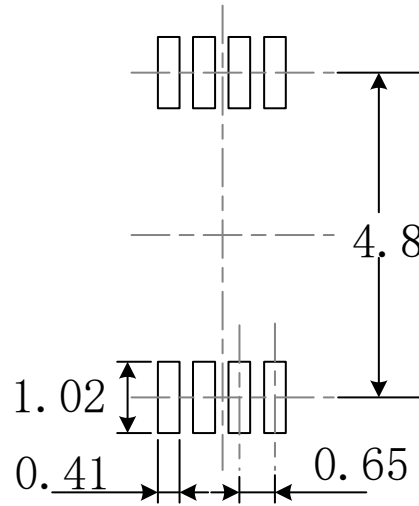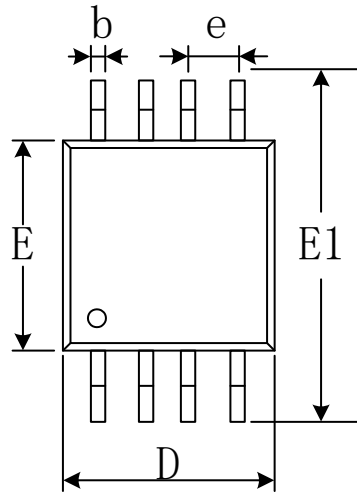
PACKAGE OUTLINE DIMENSIONS
## SOIC-8(SOP8)
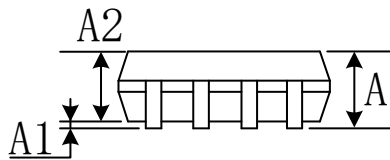

RECOMMENDED LAND PATTERN (Unit: mm)

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	**Max**	**Min**	**Max**
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270(BSC)		0.050(BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

MSOP-8

RECOMMENDED LAND PATTERN (Unit: mm)
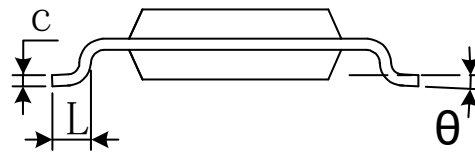
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.820	1.100	0.032	0.043
A1	0.020	0.150	0.001	0.006
A2	0.750	0.950	0.030	0.037
b	0.250	0.380	0.010	0.015
c	0.090	0.230	0.004	0.009
D	2.900	3.100	0.114	0.122
e	0.650(BSC)		0.026(BSC)	
E	2.900	3.100	0.114	0.122
E1	4.750	5.050	0.187	0.199
L	0.400	0.800	0.016	0.031
θ	0°	6°	0°	6°