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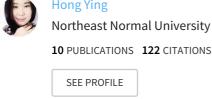
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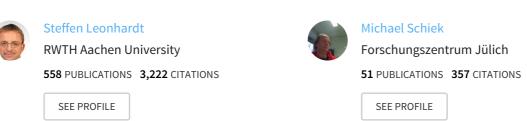
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# **Automatic Step Detection in the Accelerometer Signal**

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Abstract— The automatic step detection is a crucial component for the analysis of vegetative locomotor coordination during monitoring the patients with Parkinson's disease. It is aimed to develop the algorithms for automatic step detection in the accelerometer signal, which will be integrated in sensor networks for neurological rehabilitation research. In this paper, three algorithms (Pan-Tompkins method, template matching method and peak detection based on combined dual-axial signals) are detailed described. Finally, these methods will be discussed by means of dis- and advantages.

Keywords— Step detection, accelerometer, Pan-Tompkins method, template-matching method, neurological rehabilitation research

#### I. Introduction

Parkinson's disease (PD) is associated with reduced coordination between respiration and locomotion [1]. For the neurological rehabilitation research, it requires a long-time monitoring system, which enables the online analysis of the patients' vegetative locomotor coordination. The online analysis allows the later integration of bio-feedback protocol for the rehabilitation purpose. This system will facilitate the identification of the therapeutic effect and measuring the patients' health status. To monitor the phase synchronization between respiration and locomotion, the accurate time of step event has to be determined. The commercial pedometers, which count each step a person makes, can not fulfill this requirement. For this purpose, the online algorithm is developed for the automatic step detection. Since the characteristics of the PD patients' gait are pronounced different from the normal gait [2], the database for algorithm development in our work is acquired only from the PD patients.

The first experiment on the locomotion measuring is carried out using accelerometer. The accelerometer signals show considerable difference in morphology and amplitude among the individuals. We attempt to design a fast and robust algorithm, which should be suitable for individual patients, without any user-specified parameters. So far, three methods have been investigated for this project:

- Pan-Tompkins Method
- Template-Matching Method

 Peak-detection method based on combined dualaxial signals.

These three algorithms will be described in detail below, as well as their preliminary results on the patients' data. Finally, the dis- and advantages of these methods will be discussed.

### II. METHODOLOGY

## A. Subjects and Equipment

Subjects: In total, eight patients suffering from PD were measured in the Clinic Ambrock for Neurology, Centre for Sleep- and Rehabilitation Research, Germany. The patients are at different stage from 1 through 4 according to unified rating scale (UPDRS). To evaluate the algorithms, data in total duration of 115 minutes were collected from eight recordings. They are acquired at a sample rate of 200 Hz. The patients were walking on a treadmill with accelerometers attaching on the lateral side of the left and right feet during the measurement. Meanwhile, the abdominal and thoracic respiration signals were measured and acquired with a sample rate of 20 Hz.

Equipment: The horizontal and vertical acceleration in xand z-axis generated from the locomotion during steps is measured by ADXL322, a low power dual-axis accelerometer, produced by Analog Devices, Inc. The schematic circuit diagram of the accelerometer for activity measuring is provided in Fig.1 below. The accelerometer is supplied by battery voltage through a low-dropout voltage regulator (TPS77027) to maintain constant 2.7 V level. To improve transient response and noise rejection, a 1 µF ceramic capacitor (C<sub>DC</sub>) is connected between V<sub>RIN</sub> and GND. The voltage regulator requires a 4.7 µF capacitor (C<sub>ROUT</sub>) connected between V<sub>ROUT</sub> and GND. The output of regulator is brought into ADXL 322 through pin Vs. The ADXL322 contains two 32  $k\Omega$  resistors (R<sub>X</sub> and R<sub>Y</sub>) in each output inside IC, respectively. The 0.1  $\mu F$  capacitors (C<sub>X</sub> and C<sub>Y</sub>) are added at the pins X<sub>OUT</sub> and Y<sub>OUT</sub> to implement passive low-pass filtering of 50Hz, first order, to eliminate high frequent noise. The output signals:  $V_{AOUTX}$  and  $V_{AOUTY}$  are analog voltages that are proportional to acceleration.