ECEN/CSCI 5593 Advanced Computer Architecture (ACA)
Paper Review #2 (Cache / Dynamic Execution- Superscalar Architectures)

Assignment: write a one page (no cover sheet, but include your name) on the concepts of the paper. Ideally summarize the paper in one paragraph (6-7 sentences), and add a second paragraph to add your unique insight on the paper (relevant today, flaws in the paper, ideas of your own on what the authors might have explored in addition to their work). Papers are available on IEEE and ACM publishing library when logged in from a colorado.edu system or colorado.edu VPN connection.

Suggested papers for review:

• Ravi Iyer, Li Zhao, Fei Guo, Ramesh Illikkal, Srihari Makineni, Don Newell, Yan Solihin, Lisa Hsu, and Steve Reinhardt. 2007. QoS policies and architecture for cache/memory in CMP platforms. SIGMETRICS Performance Evaluation.
• Anoop Gupta, John Hennessy, Kourosh Gharachorloo, todd Mowry, and Wolf-Dietrich Weber, "Comparative Evaluation of Latency Reducing and tolerating Techniques", Proceedings of the International Symposium on Computer Architecture (ISCA), 1991
• Subbarao Palacharla and R.E. Kessler, "Evaluating Stream Buffers as a Secondary Cache Replacement", Proceedings of the International Symposium on Computer Architecture (ISCA-21), 1994.
• Keith Farkas, Paul Chow, Norman Jouppi, and Zvonko Vranesic, "Memory-System Design Considerations for Dynamically-Scheduled Processors", Proceedings of the International Symposium on Computer Architecture (ISCA-24), 1997
• James Smith and Andrew Pleszkun, "Implementation of Precise Interrupts in Pipelined Processors", Proceedings of the International Symposium on Computer Architecture (ISCA) 1985
• R. Tomasulo, "An Efficient Algorithm for Exploiting Multiple Arithmetic units", IBM Journal, January 1967
• Wen-mei Hwu and Yale Patt, "HPSm, a High Performance Restricted Data Flow Architecture Having Minimal Functionality," Proceedings of the International Symposium on Computer Architecture (ISCA) 1986.
• James Smith, "Decoupled Access/Execute Computer Architectures", Proceedings of the International Symposium on Computer Architectures (ISCA), 1982.
• Jack Dennis and David Misunas, "A Preliminary Architecture for a Basic Data-Flow Processor", Proceedings of the International Symposium on Computer Architecture (ISCA) 1975.

Any paper of your own selection related to hardware cache memory or dynamic execution in superscalar architectures.