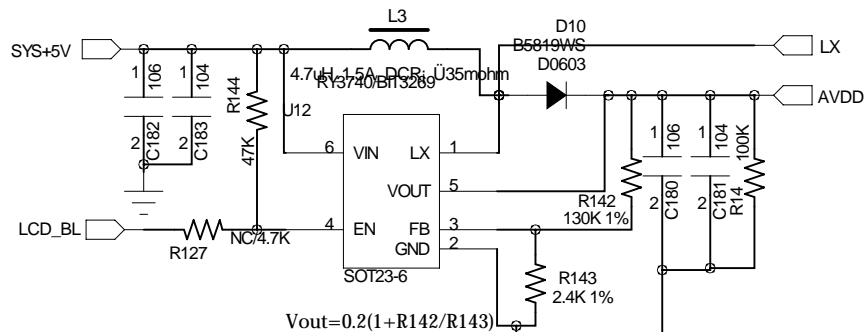
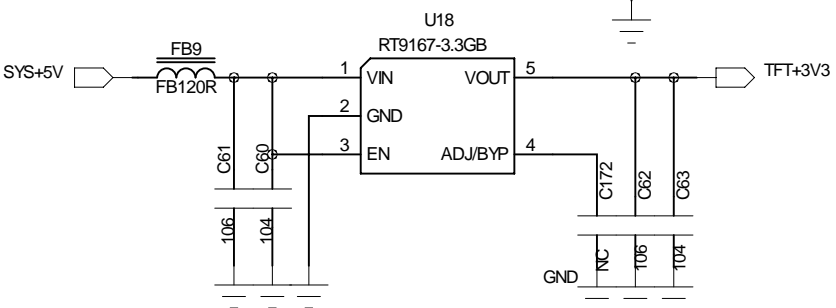
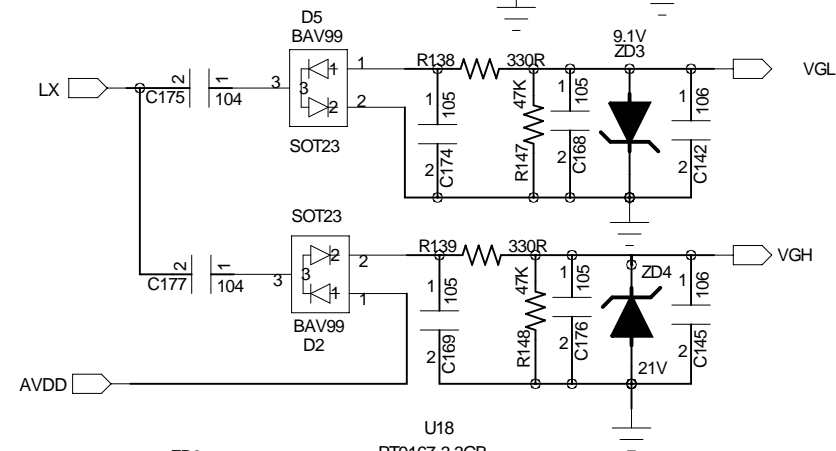




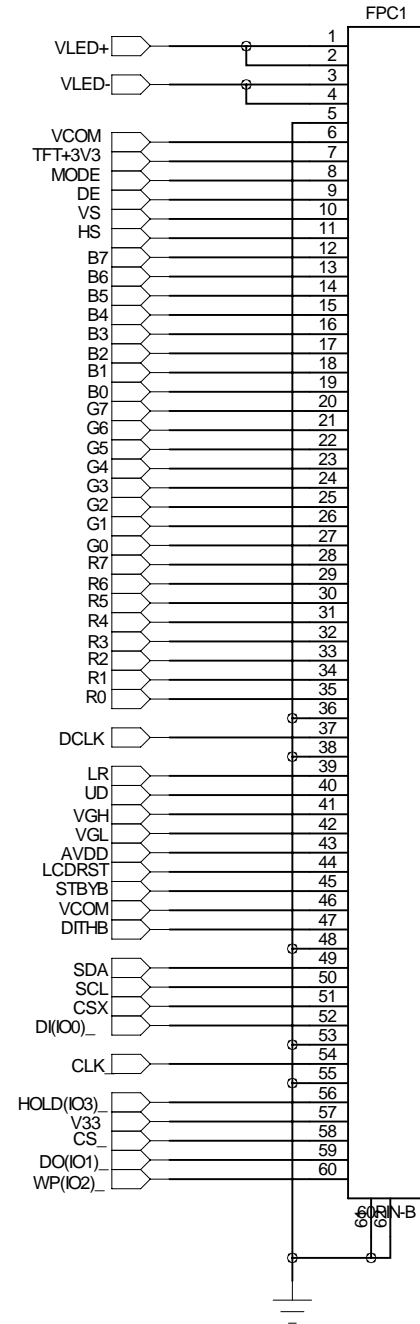
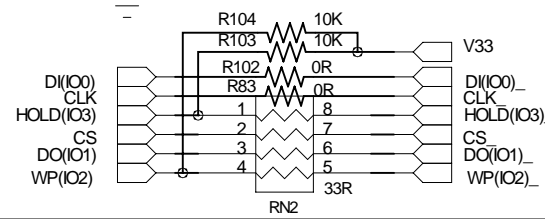
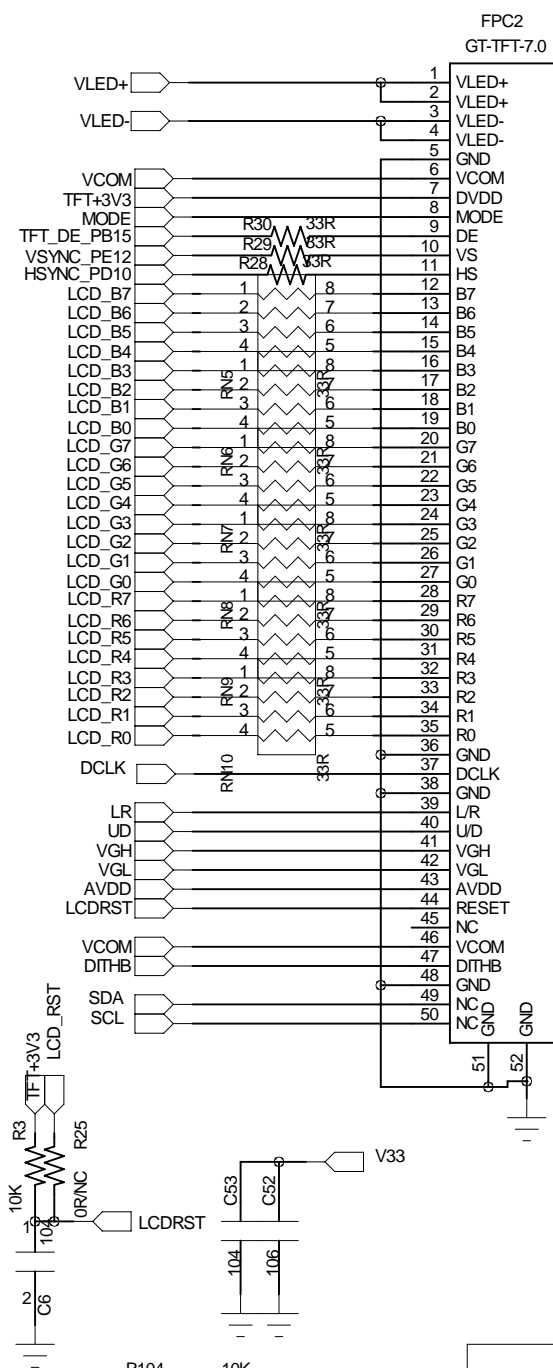
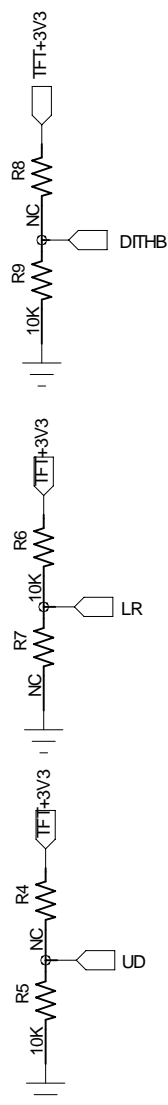
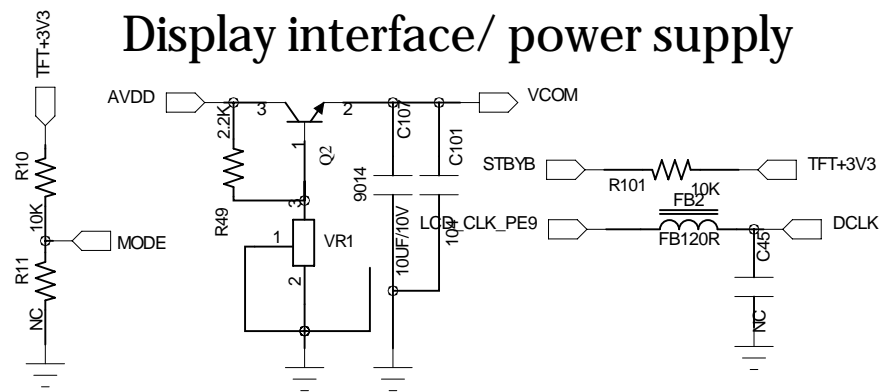
R143=2.7K:AVDD/9.95V;ACC/7.44V;R/200R/12.5MA  
R143=2K:AVDD/13.22V;ACC/10.26V;R/200R/25.5MA



$$V_{out}=0.2(1+R_{142}/R_{143})$$



## Display interface/ power supply



Shenzhen Genitop Semiconductor Co., Ltd

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| <b>Project:</b> | GT-7.0 "serial screen module circuit diagram |        |        |
| <b>File:</b>    |  |        |        |
| Date:           | 2023-03-27                                   | Rev.   | V1.0   |
| Designed by:    | HGL  | Sheet: | 2 of 6 |

**POWER**

VIN

J1

DC-005C-2.0-SU

GND

NC/0R R98

4 3

2 1

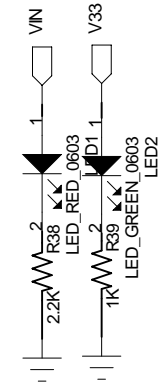
NC/SDCW2012-2-900 L4 R99

F1 JK-nSMD400L-12

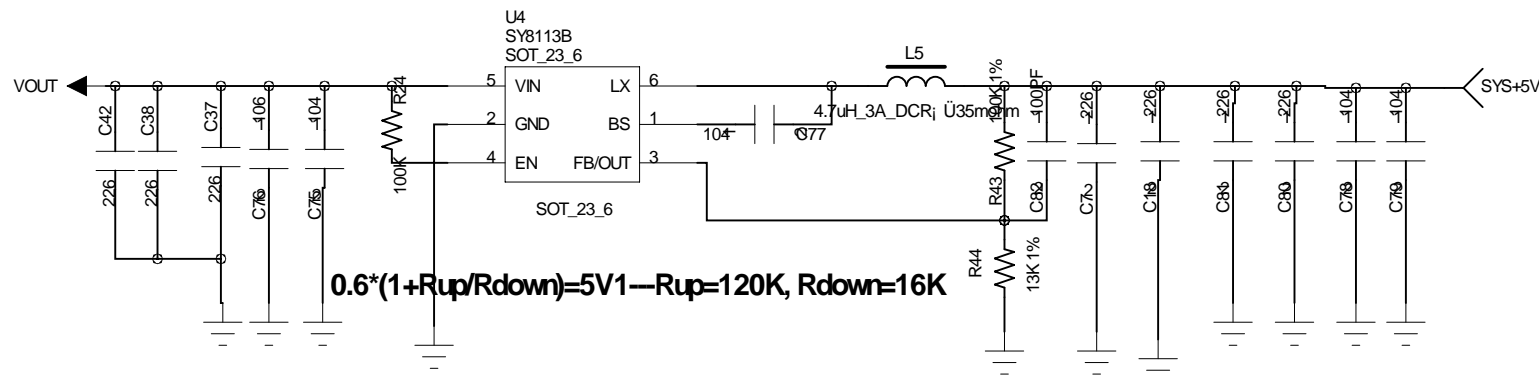
D4 B5819WS D0603

C43 220uF/16V

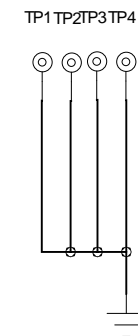
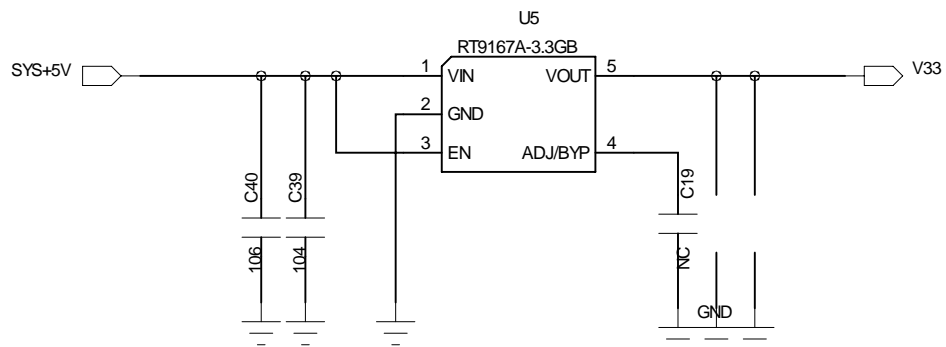
VOUT



## 5.1V/3A



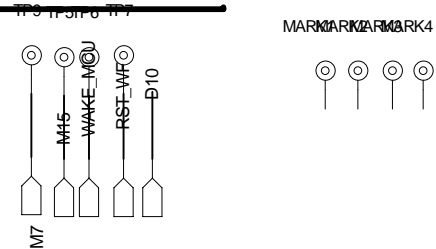
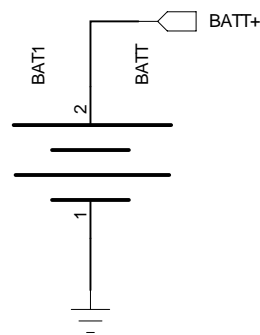
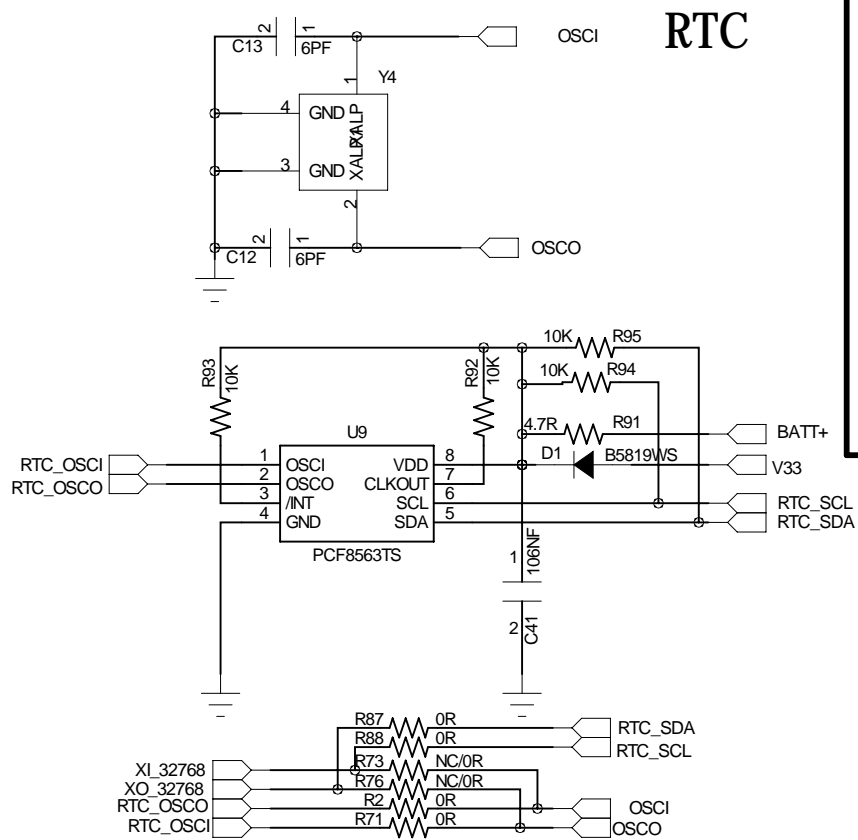
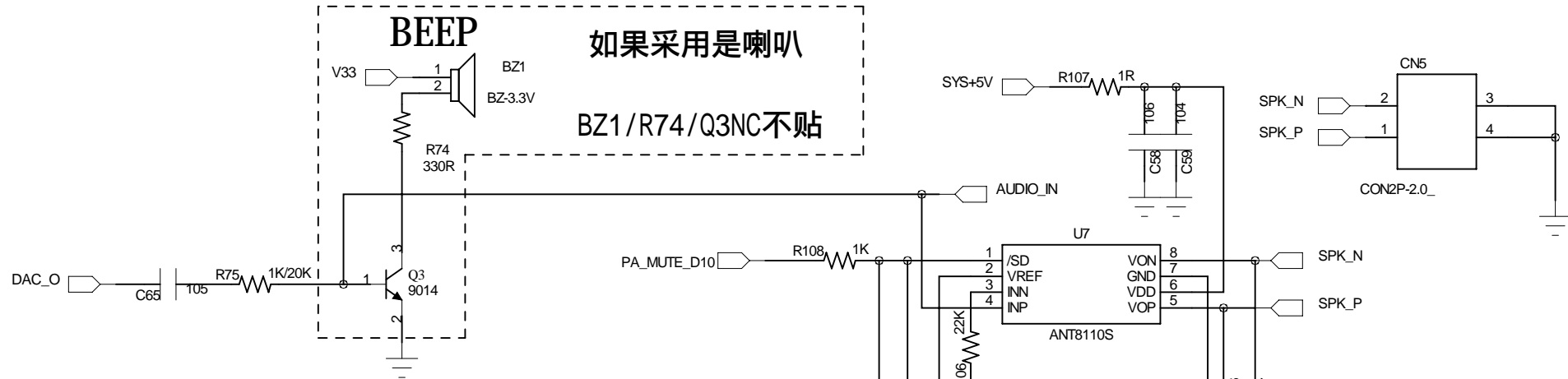
$$0.6 \cdot (1 + R_{up}/R_{down}) = 5V1 \rightarrow R_{up} = 120K, R_{down} = 16K$$



|                 |  |        |        |
|-----------------|--|--------|--------|
| <b>Project:</b> | GT-7.0 "serial screen module circuit diagram |        |        |
| <b>File:</b>    |  |        |        |
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| <b>Project:</b>                         | GT-7.0 "serial screen module circuit diagram |        |        |
| <b>File:</b>                            |  |        |        |
| Date:                                   | 2023-03-27                                   | Rev:   | V1.0   |
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