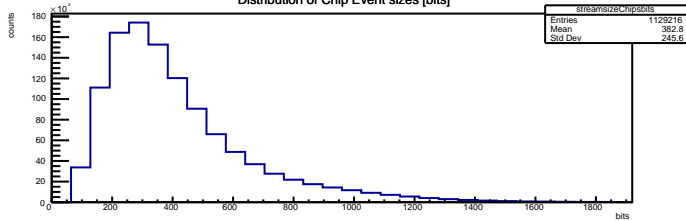
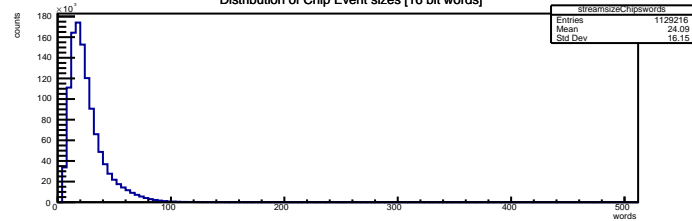


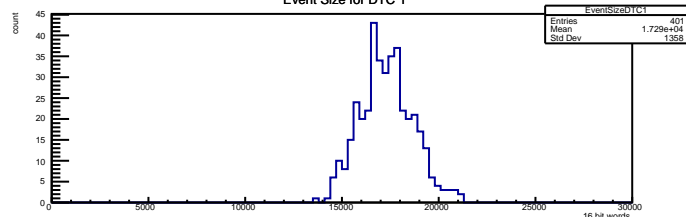
Distribution of Chip Event sizes [bits]



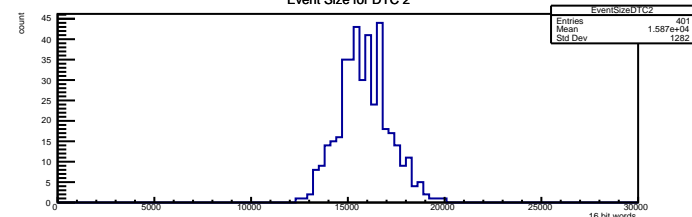
Distribution of Chip Event sizes [16 bit words]



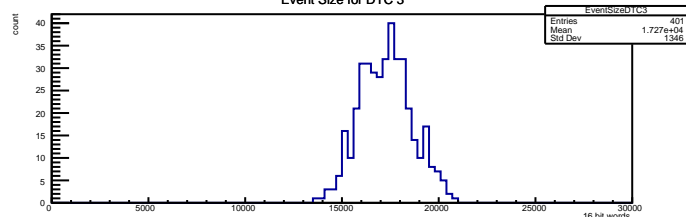
Event Size for DTC 1



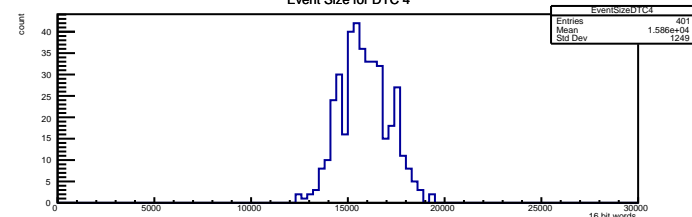
Event Size for DTC 2



Event Size for DTC 3



Event Size for DTC 4



Event Size for DTC 5

