

Nadir Khan

Research Scientist  
FZI Forschungszentrum Informatik,  
Karlsruhe, Germany  
+49 721 9654-160

## To whom it may concern

I am proud to recommend the outstanding student Mr. Asier Garciandia López, for any research or industry position. Mr. Garciandia worked at FZI Forschungszentrum Informatik from March 2019 to August 2019 on his master thesis. He was registered as a regular student at Public University of Navarre, where he received the highest grade for his thesis with an honors mention. The grade would be equivalent to 1.0 according to academic grading in Germany.

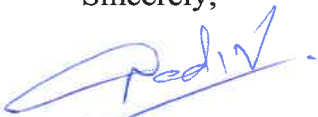
He had an excellent grasp of the very challenging and interdisciplinary material, which draws from computer science, electrical engineering, electronic engineering and mechanical engineering. His Master Thesis entitled Development of “*A Secure Framework with Remote Configuration of Intellectual Property via TLS for FPGA-s*” demonstrated all of the learning outcomes in the fields of FPGA-s, Trusted Execution Environments, Dynamic Partial Reconfiguration and Cryptography. Specifically, he performed the following tasks:

- Implementation of a hypervisor architecture that is loaded into a SoC FPGA.
- Configurations of Intellectual Property using dynamic partial reconfiguration, while ensuring the security using cryptographic concepts, algorithms and protocols.

In his work, I found Mr. Garciandia cheerful, hardworking and respectful of deadlines. He can work independently as well as in a team. He also delivered many novel ideas during our discussions and regular meetings.

Mr. Garciandia is highly motivated and has a pleasing personality with great social skills. He would be a tremendous asset for any organization and has my highest recommendation. If there is any further question with regard to his qualification, please do not hesitate to call or contact me.

Sincerely,



M. Sc. Nadir Khan