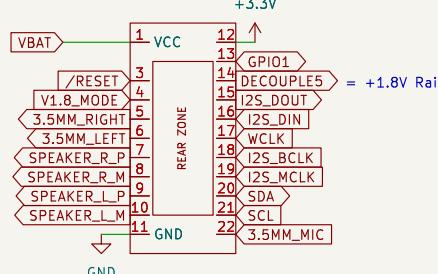
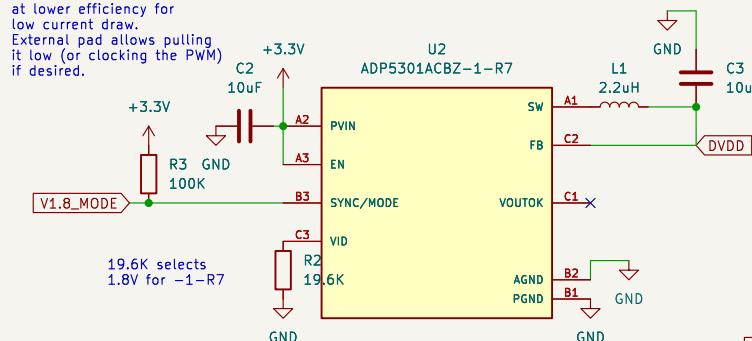
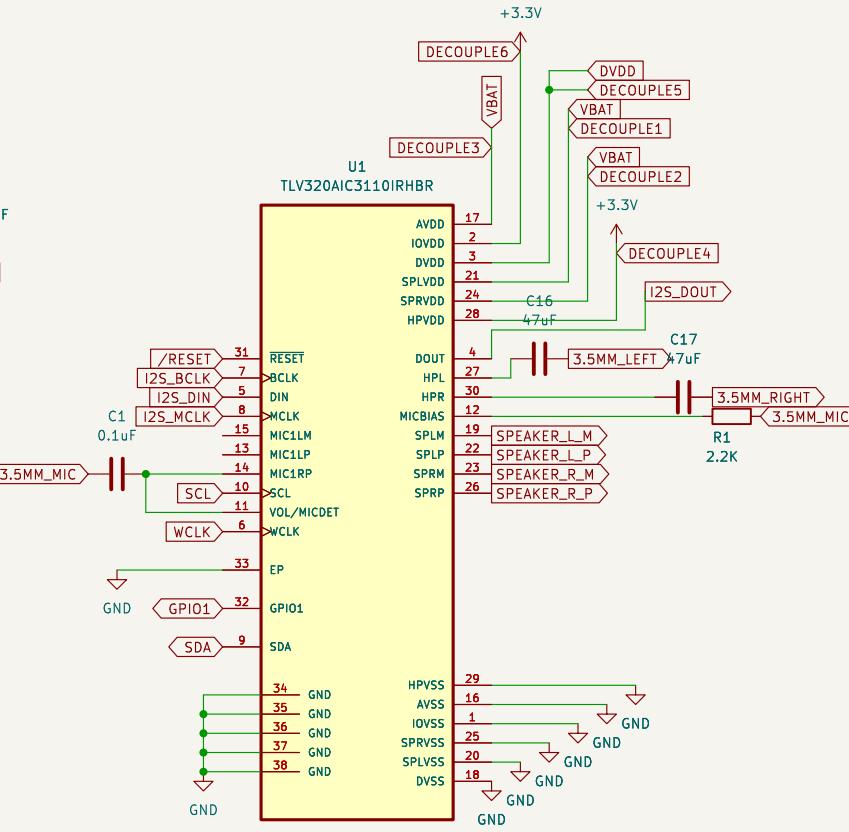
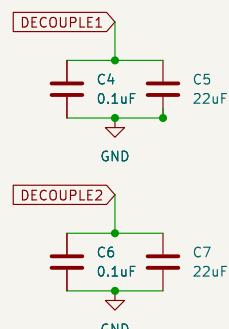


Pull SYNC/MODE high for default 500mA max, but at lower efficiency for low current draw.
External pad allows pulling it low (or clocking the PWM if desired).



3D top side



DVSS & IOVSS tied to digital side of GND plane.

All others to analog
(VBAT) side.

