

Krzysztof Garczyński

embedded software developer

about:

phone: 888-820-151

linkedin profile

github

krzysztof.garczyński
@gmail.com

languages:

english
(fluent)
russian
(basics)

programming:

C, C++, python, Rust
VHDL
embedded linux
kernel
u-boot
android

skills:

Architecture:
ARM, AVR, DSP

Protocols:
DECT, WDCT
I2C, SPI

Others:
git, ClearCase, svn
buildroot, Yocto
electronic design
lauterbach
SCRUM

at work:

faithful
teamworker
"can-do" attitude

after hours:

squash player
rock listener
basketball fanc

career goal

embedded linux and drivers developer, embedded devices

experience

10.2015 - ... embedded linux kernel & u-boot developer

Nokia Networks - Radio Module

- OS/Boot Technical Team Leader,
- Linux Trainings (over 100 people),
- Porting Linux and U-Boot to new SoC,
- Drivers development (Bootloader and OS),
- Specification and manuals for new components,
- Team interface to the Product Owners and Scrum Masters

07.2013
- 10.2015

embedded linux kernel & u-boot developer

Nokia Networks - System Module

- Bootloaders team leader,
- U-boot porting and features development,
- Linux Drivers implementation for new hardware,
- Coaching new members

11.2010
- 07.2013

embedded software developer

Gigaset Communications Sp. z o.o.

- Embedded systems developer (DECT devices),
- Features design, implementation and maintenance,
- Preparing specifications, manuals, workpackages for test department,
- Maintain DSP areas (API, CPU vendors contact) and DECT embedded OS

02.2012
- 01.2013

embedded software developer

Wroclaw University Of Technology, Division of Micro- and Nanostructures Metrology

- Architecture of new measurement embedded platforms for AFM
- Lecturer at "ArmAcademy" workshops for students,
- Students projects supervision and inspection (code reviews)

01.2010
- 09.2010

technical engineer

Wroclaw University Of Technology, Division of Micro- and Nanostructures Metrology

- Embedded software developer of microprocessors (ARM, DSP), microcontrollers (Atmel) and FPGA, CPLD,
- Design and assembly of digital electronics circuits and devices,
- Measurements using atomic force and scanning tunneling microscopes

education

2005–2010 **Faculty of Microsystem Electronics and Photonics**
Wrocław University of Technology
M.Sc. in Electronics
specialization in Microsystem Electronics

trainings

03.2015	Kernel internals and debugging Linux Foundation	description
04.2012	SCRUM for beginners Brass Willow	PL description
04.2012	Linux kernel and drivers development ALX 2012	description
08.2012	Android Applications ALX 2012	description

publications

Garczyński K., Zielony M., Zawierucha P., Kopiec D., Gotszalk T.,
ATmega128 and CPLD based universal driver for Scanning Tunneling Microscopy

Polish Electronics Conference, Darłówko Wschodnie 2010, published: Elektronika, 9/2010

Kopiec D., Józwiak G., Zawierucha P., Garczyński K., Grabiec P., Gotszalk T.,
Calibrating spring constant known mass method

Nano and Micromechanics Conference, Krasieczyn 2010

activities

SPENT member - Stowarzyszenie Polskich Entuzjastów Nanotechnologii ("Polish Nanotechnology Enthusiasts Association")