MANASVI GARG
Gender: M

DOB: 11/12/1996

EDUCATION				
2014-Present (Undergraduate)	B.Tech. (Electrical Engineering) (Specialization in Computer Science)	Dayalbagh Educational Institute, Dayalbagh, Agra	Deemed University	CGPA 6.3316 (Till 6 semesters)
2014	ClassXII	Sri Chaitanya Jr Kalasala, Hyderabad	BIE,HYDERABAD	90.2%
2012	Class X	Nirmala Convent School, Bulandshahr	CBSE	10.0 CGPA

PROJECT'S AND TRAINING

- Internship:**5 months internship at Xtranet Technologies Pvt Ltd**(03 Apr to 18 Aug, 2017).
    - **Major learning objectives** -:Software Testing ,Types of Software Testing , STLC , Documentation and SRS ,ERP Solutions

- 1 month Training: **Learnt C Programming Concepts** (25 May to 27 June, 2016) under RGCSM(Rajiv Gandhi Computer Saksharta Mission)
    - Leant data types ,functions, String handling operations and File Reading operations

- **Industrial Visits:**
    - 2 days industrial visit at Varun Beverages, Kosikala saw various manufacturing methods of drinks and beverages.
    - 2 days' Visit at Creambells IceCream manufacturing unit near Mathura and saw various methods of Icecream manufacturing.

    - 1 day Industrial visit at Atul Generators where we learnt how does a dynamo work and various types of generators

- **Projects:**
    - Software Testing and various types of Testing methods and various types of Software applications during 5 months internship in
      Xtranet Technologies
        - **I. SDLC Models and CMMI II.** Unit Testing
        - **III.** System Testing .
        - **IV.** Integration Testing
        - **V.** Regression Testing
        - **VI. Load Testing**
        - **VII.** Stress Testing
        - **VIII.** Performance Testing
        - **IX.** Acceptance Testing

    - Gas Leakage Automatic Safety System under DETD(Design Engineering and Theme Development Project).

    - POV Led Display by using 7 segment LED display and using a Blue term app to control the working of the device under PMP Project.

- **MAJOR PROJECT:-PRECISION DAIRY FARMING SYSTEM SOFTWARE IN PHP.**

Skills
- Verilog Coding as a hardware description language , Digital Systems
- Microcontroller Programming in assembly language
- Knowledge of C and Data Structures and Designing and Analysis of algorithms .
- Knowledge of PHP & Java Programming and oops concepts.

- Web Development Through JSF framework of java
- Knowledge of SQL, MySql Server, Oracle Server and JDBC
- JavaScript, HTML,CSS
- Ability to perform well as an individual as well as in a team.

INTERESTS

- Coding
- Reading Books
- Volleyball and cricket.
- Travelling
- Music
- Team work

PERSONAL DETAILS

Father's Name: Dr. Anil Kumar Garg
Mother's Name: Mrs. Seema Garg

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