

EELE 367 - Logic Design (Daily Schedule)

Week	Day	Date	Chapter	Topic	Reading & Homework Set	Due Dates
1	W	1/13	-	Course Overview	Syllabus	
	W	1/13	-	No Lab		
	F	1/15		Review of Classical Digital Design	Ch.4, 5, 7	Wednesday, 1/20/2016
2	M	1/18	-	No Class - MLK Day		
	W	1/20	8: VHDL (Part 2)	8.1 - The Process	8.1	Wednesday, 1/27/2016
	W	1/20	-	No Lab		-
	F	1/22	8: VHDL (Part 2)	8.2 - Conditional Programming Constructs	8.2	Wednesday, 1/27/2016
3	M	1/25	8: VHDL (Part 2)	8.2 - Conditional Programming Constructs		Wednesday, 2/3/2016
	W	1/27	8: VHDL (Part 2)	8.3 - Signal Attributes, 8.4 - Test Benches	8.3	Wednesday, 2/3/2016
	W	1/27	8: VHDL (Part 2)	Lab 8(a) - 7 Segment Decoders using if/then		Wednesday, 2/3/2016
	F	1/29	8: VHDL (Part 2)	8.4 - Test Benches	8.4	Wednesday, 2/3/2016
4	M	2/1	8: VHDL (Part 2)	8.5 - Packages	8.5	Wednesday, 2/10/2016
	W	2/3	8: VHDL (Part 2)	8.5 - Packages		Wednesday, 2/10/2016
	W	2/3	8: VHDL (Part 2)	Lab 8(b) - Binary Characters using case		Wednesday, 2/10/2016
	F	2/5	8: VHDL (Part 2)	8.5 - Packages		Wednesday, 2/10/2016
5	M	2/8	9: Behavioral Modeling	9.1 - Modeling Sequential Storage Devices in VHDL	9.1	Wednesday, 2/17/2016
	W	2/10	9: Behavioral Modeling	9.1 - Modeling Sequential Storage Devices in VHDL		Wednesday, 2/17/2016
	W	2/10	9: Behavioral Modeling	Lab 9(a) - Ripple Counter & Character Displays		Wednesday, 2/17/2016
	F	2/12	9: Behavioral Modeling	9.2 - Modeling FSMs in VHDL	9.2	Wednesday, 2/17/2016
6	M	2/15		No Class - President's Day		
	W	2/17	9: Behavioral Modeling	9.3 - FSM Design Examples in VHDL	9.3	Wednesday, 2/24/2016
	W	2/17	9: Behavioral Modeling	Lab 9(b) - Walking 1 FSM on Character Displays		Wednesday, 2/24/2016
	F	2/19	9: Behavioral Modeling	9.3 - FSM Design Examples in VHDL Cont...		Wednesday, 2/24/2016
7	M	2/22	9: Behavioral Modeling	9.4 - Modeling Counters	9.4	Wednesday, 3/2/2016
	W	2/24	9: Behavioral Modeling	9.4 - Modeling Counters Cont...		Wednesday, 3/2/2016
	W	2/24	9: Behavioral Modeling	Lab 9(c) - Counters using 1 Process		Wednesday, 3/2/2016
	F	2/26	9: Behavioral Modeling	9.5 - RTL Modeling	9.5	Wednesday, 3/2/2016
8	M	2/29	9: Behavioral Modeling	Exam Review		-
	W	3/2		Exam #1		
	W	3/2	9: Behavioral Modeling	Lab 9(d) - BCD Counter		
	F	3/4	10: Memory	10.1 - Terminology, 10.2 - Non-Volatile Storage	10.1, 10.2	Wednesday, 3/9/2016
9	M	3/7	10: Memory	10.3 - Volatile Storage	10.3	Wednesday, 3/23/2016
	W	3/9	10: Memory	10.4 - Modeling Memory in VHDL	10.4	Wednesday, 3/23/2016
	W	3/9	10: Memory	Lab 10 - Accessing Memory		Wednesday, 3/23/2016
	F	3/11	10: Memory	10.4 - Modeling Memory in VHDL		Wednesday, 3/23/2016
	M	3/14		No Class - Spring Break		
	W	3/16		No Class - Spring Break		
	W	3/16		No Class - Spring Break		
	F	3/18		No Class - Spring Break		
10	M	3/21	11: Programmable Logic	11.1 - Programmable Arrays	11.1	Wednesday, 3/30/2016
	W	3/23	11: Programmable Logic	11.2 - FPGAs	11.2	Wednesday, 3/30/2016
	W	3/23		Open Lab		-
	F	3/25		No Class - University Day		
11	M	3/28	12: Arithmetic Circuits	12.1 - Adders	12.1	Wednesday, 4/6/2016
	W	3/30	12: Arithmetic Circuits	12.1 - Adders Cont, 12.2 - Subtraction	12.2	Wednesday, 4/6/2016
	W	3/30	12: Arithmetic Circuits	Lab 12(a) - HEX Adders		Wednesday, 4/6/2016
	F	4/1	12: Arithmetic Circuits	12.3 - Multiplication, 12.4 - Division,	12.3, 12.4	Wednesday, 4/6/2016
12	M	4/4	12: Arithmetic Circuits	Review for Exam		-
	W	4/6		Exam #2		
	W	4/6	12: Arithmetic Circuits	Lab 12(b) - BCD Adders		Wednesday, 4/13/2016
	F	4/8	13: Computer Systems	13.1 - Computer Hardware, 13.2 - Computer Software	13.1, 13.2	Thursday, 5/5/2016

13	M	4/11	13: Computer Systems	13.3 - Computer Implementation	13.3	Thursday, 5/5/2016
	W	4/13	13: Computer Systems	13.3 - Computer Implementation		
	W	4/13		Work on Project		
	F	4/15	13: Computer Systems	13.3 - Computer Implementation		
14	M	4/18	13: Computer Systems	13.3 - Computer Implementation		
	W	4/20	13: Computer Systems	13.3 - Computer Implementation		
	W	4/20		Work on Project		
	F	4/22	13: Computer Systems	13.4 - Architectural Considerations	13.4	Thursday, 5/5/2016
15	M	4/25	13: Computer Systems	Work on Project		
	W	4/27	13: Computer Systems	Work on Project		
	W	4/27		Work on Project		
	F	4/29	13: Computer Systems	Work on Project		
16	M	5/2	Finals Week	Projects Due on May 5th at 10am		
	W	5/4	Finals Week			
	W	5/4	Finals Week			
	F	5/6	Finals Week			