NUCLEO-32 STM32G0

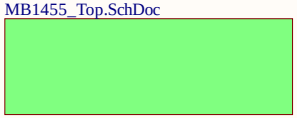
MB1455

Table of contents

Sheet 1: Project overview (this page)

- Sheet 2: Top
- Sheet 3: STM32G0 microcontroller
- Sheet 4: ST-LINK/V2-1
- Sheet 5: Power
- Sheet 6: Mechanical

U_MB1455_Top
MB1455_Top.SchDoc



Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

Notes to generate the board layout.

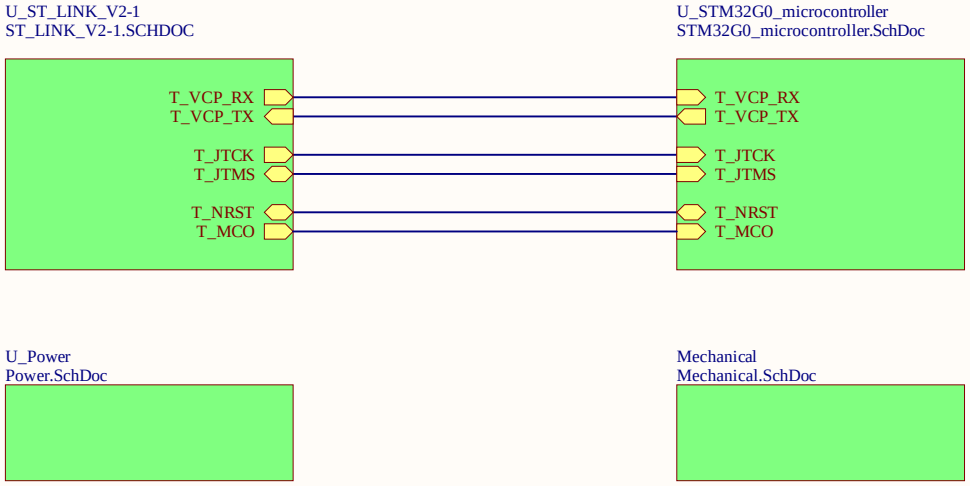
OPEN PLATFORM LICENSE AGREEMENT

The Open Platform License Agreement (“Agreement”) is a binding legal contract between you ("You") and STMicroelectronics International N.V. (“ST”), a company incorporated under the laws of the Netherlands acting for the purpose of this Agreement through its Swiss branch 39, Chemin du Champ des Filles, 1228 Plan-les-Ouates, Geneva, Switzerland.

By using the enclosed reference designs, schematics, PC board layouts, and documentation, in hardcopy or CAD tool file format (collectively, the “Reference Material”), You are agreeing to be bound by the terms and conditions of this Agreement. Do not use the Reference Material until You have read and agreed to this Agreement terms and conditions. The use of the Reference Material automatically implies the acceptance of the Agreement terms and conditions.

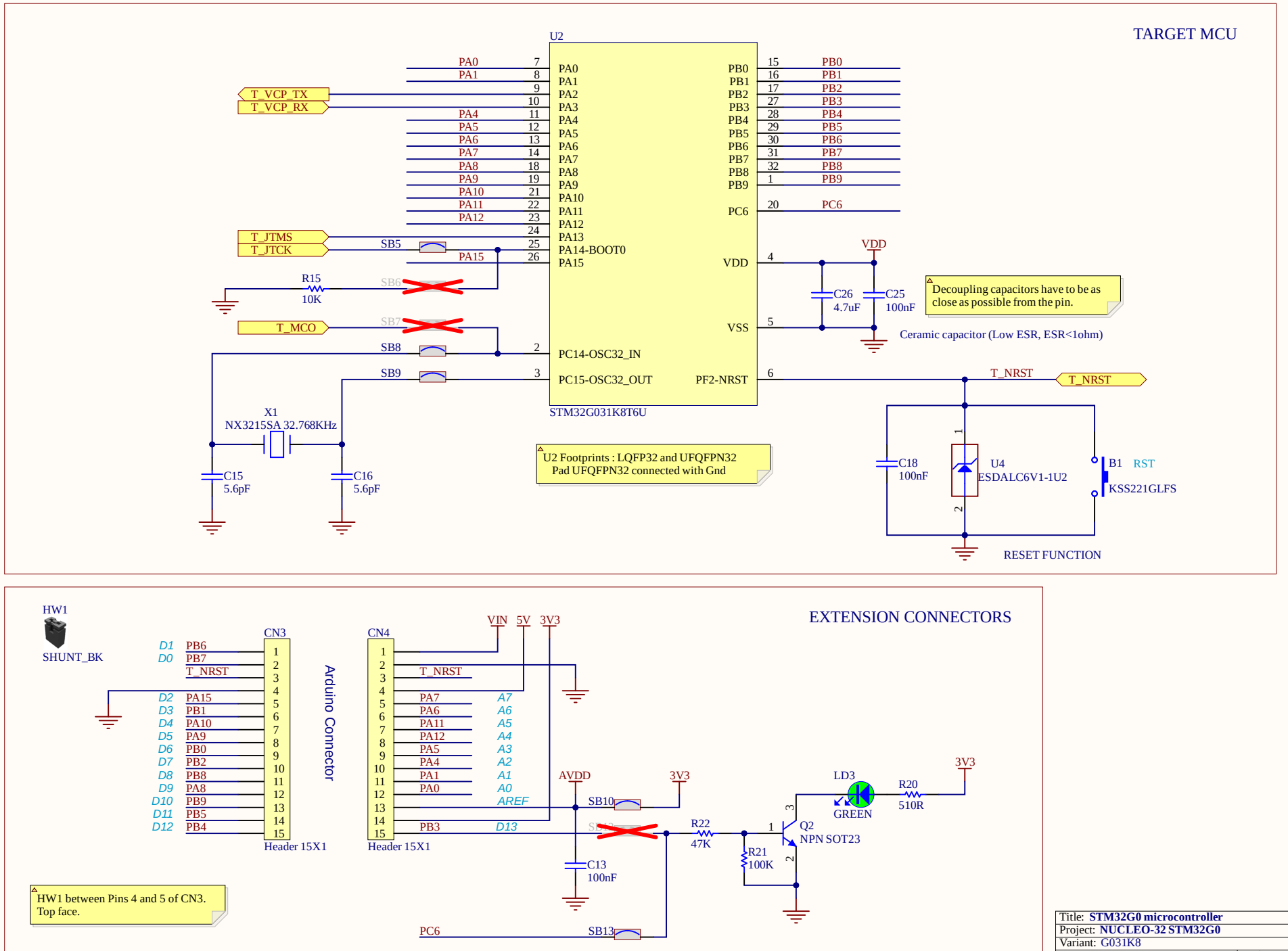
The complete Open Platform License Agreement can be found on www.st.com/opla.

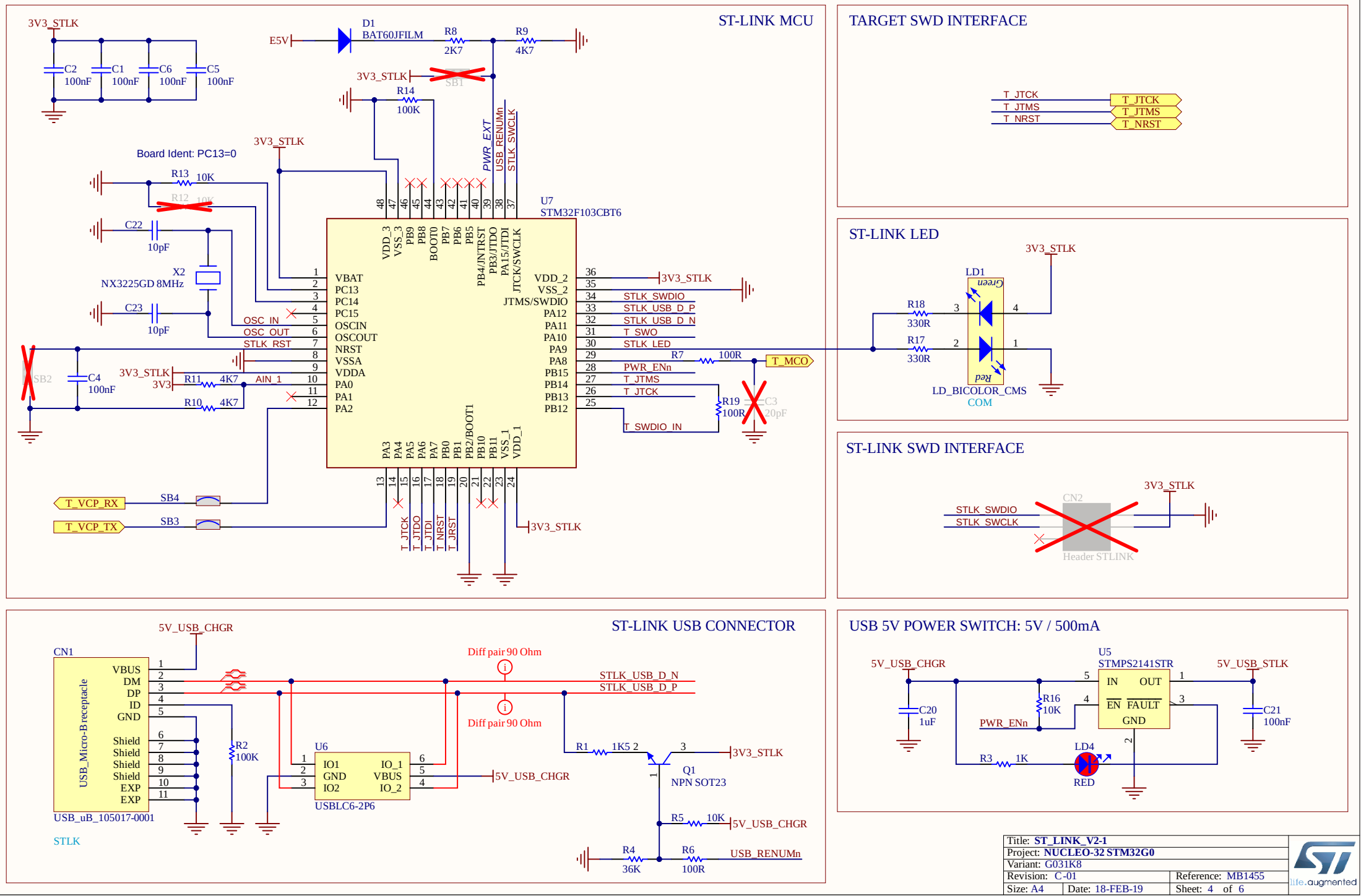
Title: **Project overview**			
Project: **NUCLEO-32 STM32G0**			
Variant: G031K8			
Revision: C-01		Reference: MB1455	
Size: A4	Date: 18-FEB-19	Sheet: 1 of 6	life.augmented



Title: **Top hierarchical view**			
Project: **NUCLEO-32 STM32G0**			
Variant: G031K8			
Revision: C-01		Reference: MB1455	
Size: A4	Date: 18-FEB-19	Sheet: 2 of 6	

life.augmented

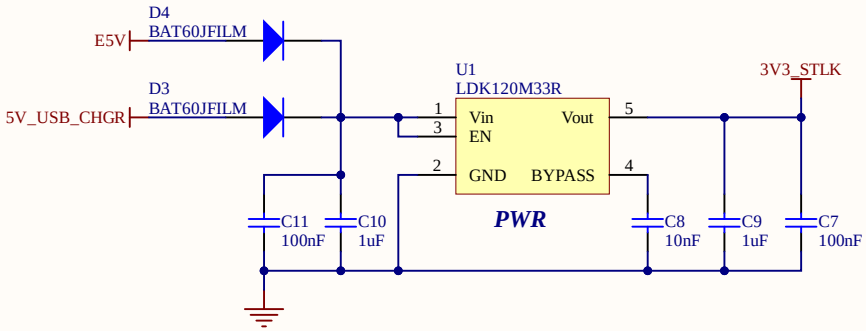

TARGET MCU
U2
STM32G031K8T6U
T_VCP_TX
T_VCP_RX
T_JTMS
T_JTCK
T_MCO
SB5
SB6
SB7
SB8
SB9
R15
10K
X1
NX3215SA 32.768KHz
C15
5.6pF
C16
5.6pF
VDD
C26
4.7uF
C25
100nF
Decoupling capacitors have to be as close as possible from the pin.
Ceramic capacitor (Low ESR, ESR<1ohm)
T_NRST
C18
100nF
U4
ESDALC6V1-1U2
B1
RST
KSS221GLFS
RESET FUNCTION
U2 Footprints : LQFP32 and UFQFPN32
Pad UFQFPN32 connected with Gnd
EXTENSION CONNECTORS
HW1
SHUNT_BK
CN3
Header 15X1
Arduino Connector
CN4
Header 15X1
VIN
5V
3V3
AVDD
SB10
SB13
C13
100nF
PC6
R22
47K
R21
100K
Q2
NPN SOT23
LD3
GREEN
R20
510R
HW1 between Pins 4 and 5 of CN3.
Top face.
Title: STM32G0 microcontroller
Project: NUCLEO-32 STM32G0
Variant: G031K8
Revision: C-01
Reference: MB1455
Size: A4
Date: 18-FEB-19
Sheet: 3 of 6
life.augmented



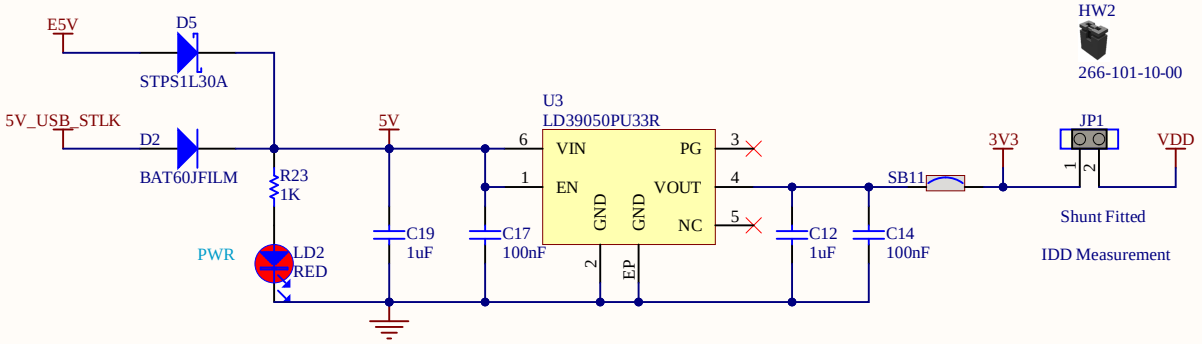
Title: **ST_LINK_V2-1**		
Project: **NUCLEO-32 STM32G0**		
Variant: G031K8		
Revision: C-01		Reference: MB1455
Size: A4	Date: 18-FEB-19	Sheet: 4 of 6

life.augmented

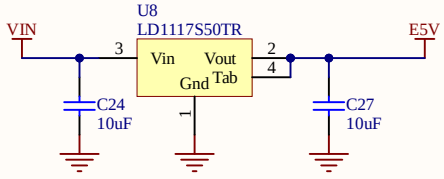
3.3 VOLTS POWER SUPPLY FOR ST-LINK



VDD PWR



VIN / 5V PWR



Title: **Power**			
Project: **NUCLEO-32 STM32G0**			
Variant: G031K8			
Revision: C-01		Reference: MB1455	
Size: A4	Date: 18-FEB-19	Sheet: 5 of 6	

life.augmented

HW3

BOARD REF

MBxxxxy-zz syywwxxxxx

HW4

BOARD CPN

Board CPN

HW5

PCB

MBxxxxy

HW6

LOGO NUCLEO

HW7

LOGO ST

Title: **Mechanical**		
Project: **NUCLEO-32 STM32G0**		
Variant: G031K8		
Revision: C-01		Reference: MB1455
Size: A4	Date: 18-FEB-19	Sheet: 6 of 6

life.augmented