

資工課程簡介

問題分析

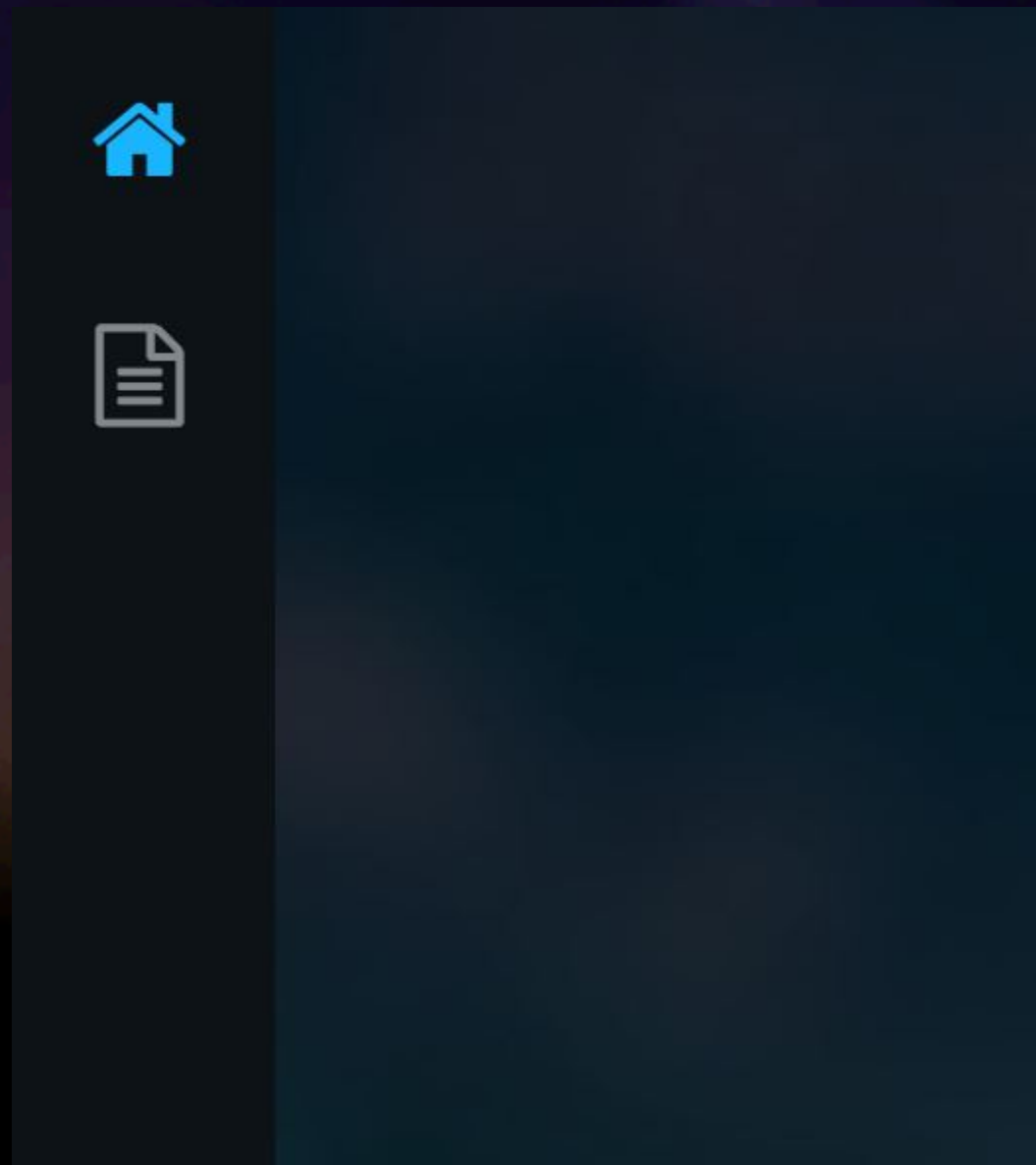
資工二106590003高鵬勛
資工二106590005張正揚

問題一

- 按鈕動畫只有做hover的部分，應該也要做點擊動畫，增加回饋感。

問題二

- ICON下方應有文字標題，讓使用者清楚該ICON之作用。



問題三

- 輸入應有格式限制，防止使用者輸入錯誤。

Get in Touch

Name

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問題四

- RWD表格不應使用水平滑動的方式呈現。

Number Systems, Conversion, Codes and Arithmetic	W2: Number Systems, Conversion, Codes and Arithmetic	W3: Boolean Algebra, Logic Circuits, and Boolean Simplification
Boolean Algebra, Logic Gates, and Boolean algebra Simplification	W5: Karnaugh Map and Quine- McCluskey Minimization and Implementations	W6: Combinational Logic Synthesis and Glitch Elimination
Timing Analysis, Race Conditions, Hazard, Glitches	W8: using MSI, PLD, timing diagram	W9: Latches, Flops, and Counters
Design of Latches, Flip- flops, Register and Counters	W11: Analysis of Synchronous/clocked Sequential Circuits	W12: Analysis of Synchronous Sequential Circuits
Design of Synchronous/clocked Sequential Circuits – state assignment and state reduction	W14: Design of Synchronous/clocked Sequential Circuits – state assignment and state reduction	W15: Design of Synchronous Sequential Circuits – state assignment and state reduction
Design of VHDL and Simulation	W17: VHDL and simulation	W18: Final Project

問題五

- 課程列表應以更為美觀的方式呈現。



問題六

- 提供幫助與說明文件。