计算机体系结构实验

QtSpim 软件

可运行32位MIPS汇编代码的MIPS模拟器





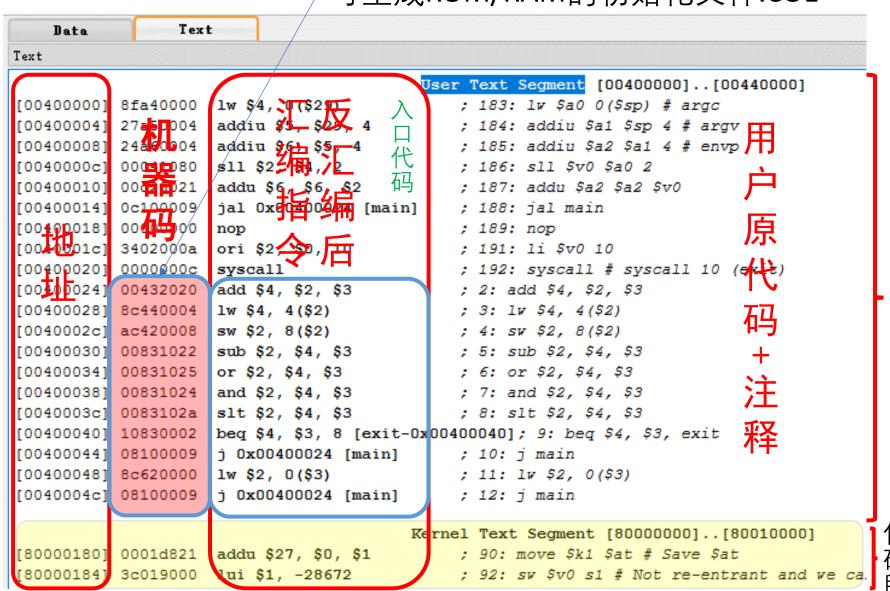
MIPS 仿真器 QtSpim (简称 Spim)

QtSpim: 支持32位MIPS指令集的MIPS微处理器模拟器。



MIPS 仿真器 QtSpim

▶可生成ROM/RAM的初始化文件.COE



例1. Hello world.

```
# Hello world
           .data
                                                          支持MIPS汇编指令程序调试,
     str: .asciiz "Hello world.\n"
           .text
                                                         也支持MIPS宏汇编指令。
           .globl main
                         # execution starts here
 6
    main:
                                                          但不支持在线编辑,也不
                         # put string address into a0
           la $a0,str
                         # system call to print
           li $v0.4
 8
                                                          支持直接装载二进制程序。
           syscall
                         # print the string
 9
                         # system call to exit
           li $v0,10
10
           syscall
                         # exit
              Text
   Data
Text
                        User Text Segment [00400000]..[00440000]
[00400000] 8fa40000 lw $4, 0($29)
                                        ; 183: lw $a0 0($sp) # argc
[00400004] 27a50004 addiu $5, $29, 4
                                       ; 184: addiu $a1 $sp 4 # argv
                                                                                               ×
[00400008] 24a60004 addiu $6, $5, 4
                                        ; 185: addiu $a2 $a1 4 # envp
[0040000c] 00041080 sll $2, $4, 2
                                        ; 186: sll $v0 $a0 2
                                                                               Hello world.
[00400010] 00c23021 addu $6, $6, $2
                                        ; 187: addu $a2 $a2 $v0
[00400014] 0c100009 jal 0x00400024 [main]
                                        ; 188: jal main
[00400018] 00000000 nop
                                        ; 189: nop
[0040001c] 3402000a ori $2, $0, 10
                                        ; 191: li $v0 10
                                        ; 192: syscall # syscall 10 (exit)
[00400020] 0000000c syscall
[00400024] 3c041001 lui $4, 4097 [str]
                                       ; 7: la $a0,str # put string address into a0
                                        ; 8: li $v0,4 # system call to print
[00400028] 34020004 ori $2, $0, 4
                                       ; 9: syscall # print the string
[0040002c] 0000000c syscall
[00400030] 3402000a ori $2, $0, 10
                                       ; 10: li $v0,10 # system call to exit
[00400034] 0000000c syscall
                                       ; 11: syscall # exit
                       Kernel Text Segment [80000000]..[80010000]
[80000180] 0001d821 addu $27, $0, $1
                                        ; 90: move $k1 $at # Save $at
```

; 92: sw \$v0 s1 # Not re-entrant and we can't

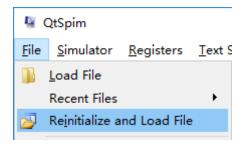
[80000184] 3c019000 lui \$1, -28672

QtSpim 错误调试

```
1 # Hello world
                  包含全角"的错误代码
        .data
        .asciiz "Hello world.\n"
        .text
        .globl main
 6 main:
                    # execution starts here
        la $a0,str
                    # put string address into a0
        li $v0,4
                    # system call to print
                    # print the string
        syscall
10
        li $v0,10
                    # system call to exit
11
        syscall
                    # exit
```

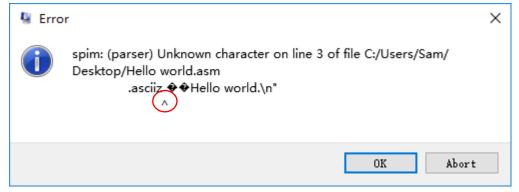
QtSpim装载汇编源程序后, 如果源程序有错, 只报第1个错误。

在QtSpim中打开

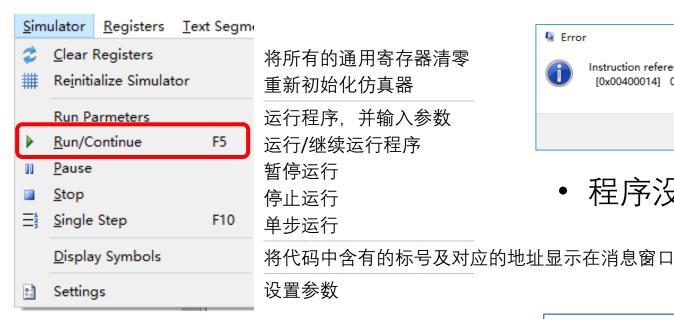


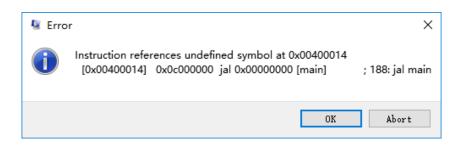
文件夹名称也不能用中文!

- 汉字显示为乱码
- 错误之处用"^"标注
- 右侧代码引号为中文全角, 应改为半角双引号。



QtSpim运行代码 (F5)





• 程序没有定义main标号

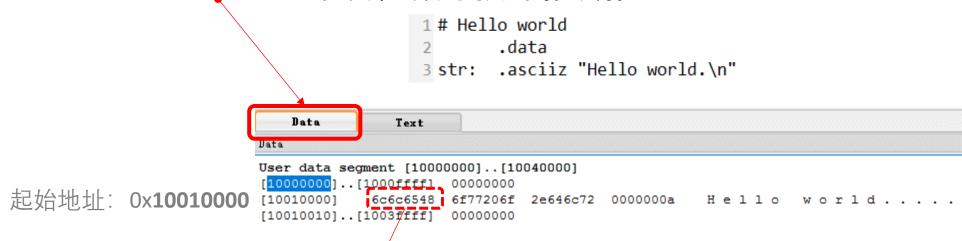
有错误的代码 # Hello world .data str: .asciiz "Hello world.\n" .text .globl top # execution starts here top: la \$a0,str # put string address into a0 li \$v0,4 # system call to print syscall # print the string li \$v0,10 # system call to exit # exit syscall



```
# Hello world
.data
str: .asciiz "Hello world.\n"
.text
.globl main
main: # execution starts here
la $a0,str # put string address into a0
li $v0,4 # system call to print
syscall # print the string
li $v0,10 # system call to exit
syscall # exit
```

QtSpim查看程序内存映像-1

• 通过数据窗口查看用户数据段内存映像

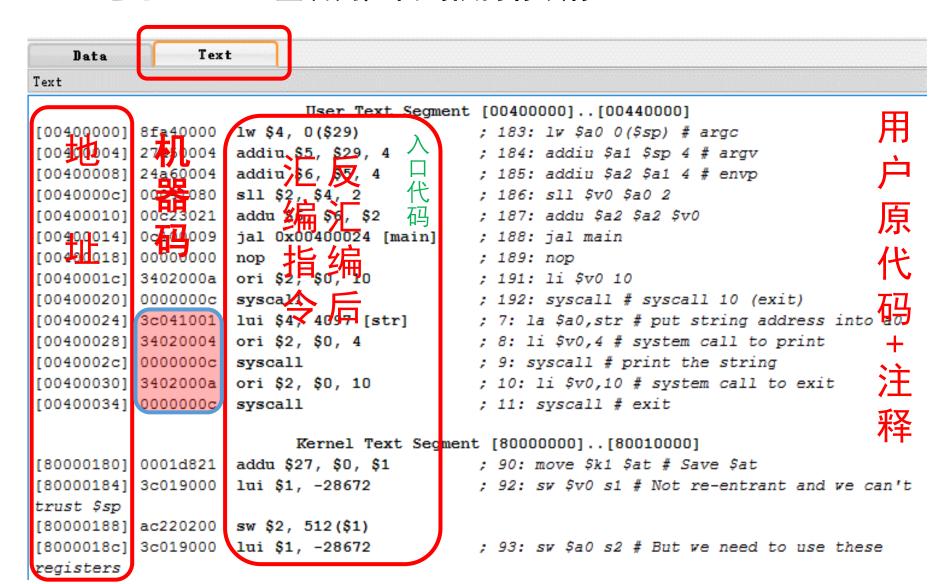


| 变量名 | 地址 | 数据 | 定义值 |
|-------|------------|--------------|-----|
| | 0x10010010 | 0x 48 | Н |
| | | 0x 65 | e |
| | | 0x 6c | l |
| c.t.v | | 0x 6c | l , |
| str | | 0x 6f | 0 |
| | | 0x 20 | |
| | | 0x 77 | W |
| | | 0x 6f | 0 |

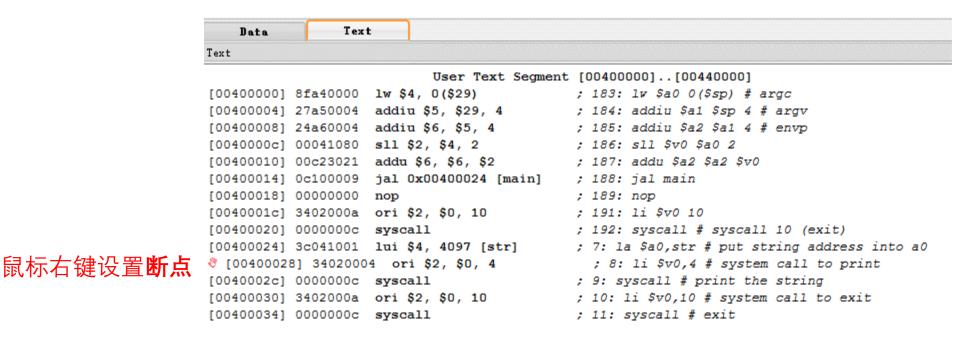
【注意】MIPS微处理器原本采用大字节顺序存放数据,但由于仿真器运行在PC(Intel微处理器)上,因此实际数据的存储采用小字节顺序。

QtSpim查看程序内存映像-2

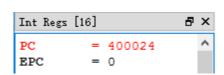
通过代码窗口查看用户代码段内存映像



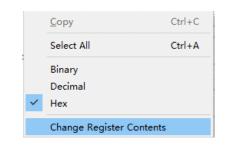
QtSpim断点、调试

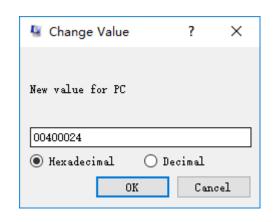


观察寄存器的值



改变寄存器的值





ROM/RAM初始化文件.COE的制作

用ultraEdit编辑汇编源程序代码。(见左下角图)

或Notepad++

用QtSpim装载test.asm,同时测试功能是否正常。(见上页图)

复制QtSpim中的用户代码段,拷贝到ultraEdit中,并设置为**列模式**,

提取机器码。•

2 # add, sub, and, or, slt, addi, lw, sw, beq, j

beg \$4, \$0, around # should be taken

initialize \$2 = 5

initialize \$3 = 12

initialize \$7 = 3

\$4 <= 3 or 5 = 7

\$5<=12 and 7 = 4

shouldn't happen

\$7= 1 + 11 = 12

shouldn't be taken

\$5 = 4 + 7 = 11

\$4=12<7 = 0

\$4 = 3<5 =1

\$7=12-5 = 7

\$2 = [80] = 7

should be taken

shouldn't happen

write mem[84] = 7

[80] = 7

test.asm x

1 #Test the MIPS processor

addi \$2, \$0, 5

or \$4, \$7, \$2

and \$5, \$3, \$4

add \$5, \$5, \$4

beq \$5, \$7, end

slt \$4, \$3, \$4

addi \$5, \$0, 0

add \$7, \$4, \$5

sub \$7, \$7, \$2

sw \$7, 68(\$3)

lw \$2, 80(\$0)

addi \$2, \$0, 1

sw \$2, 84(\$0)

14 around: slt \$4, \$7, \$2

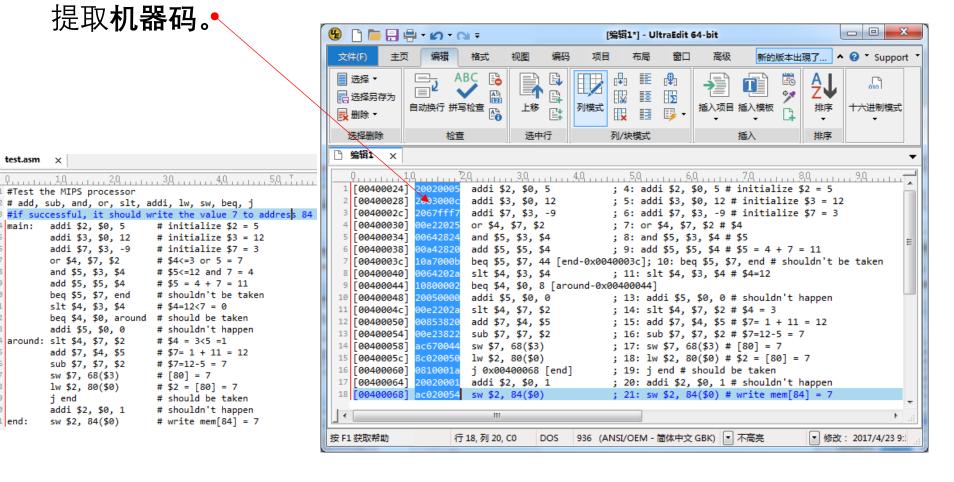
i end

18

19

addi \$3, \$0, 12

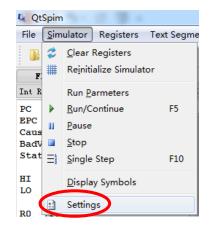
addi \$7, \$3, -9

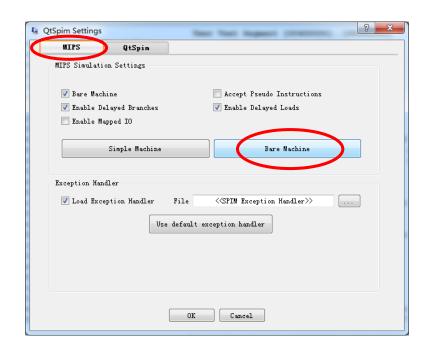


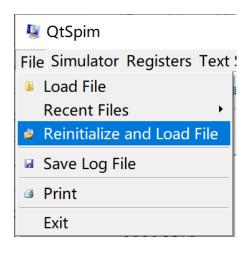
ROM/RAM初始化文件.COE的制作-2

4 有条件跳转指令机器码如 beq、bne等指令

方法一: 设置QtSpim参数为 Bare Machine







方法二: 自行修改为: 条件跳转指令地址 - 1

参见: 教材P396附录B 注脚

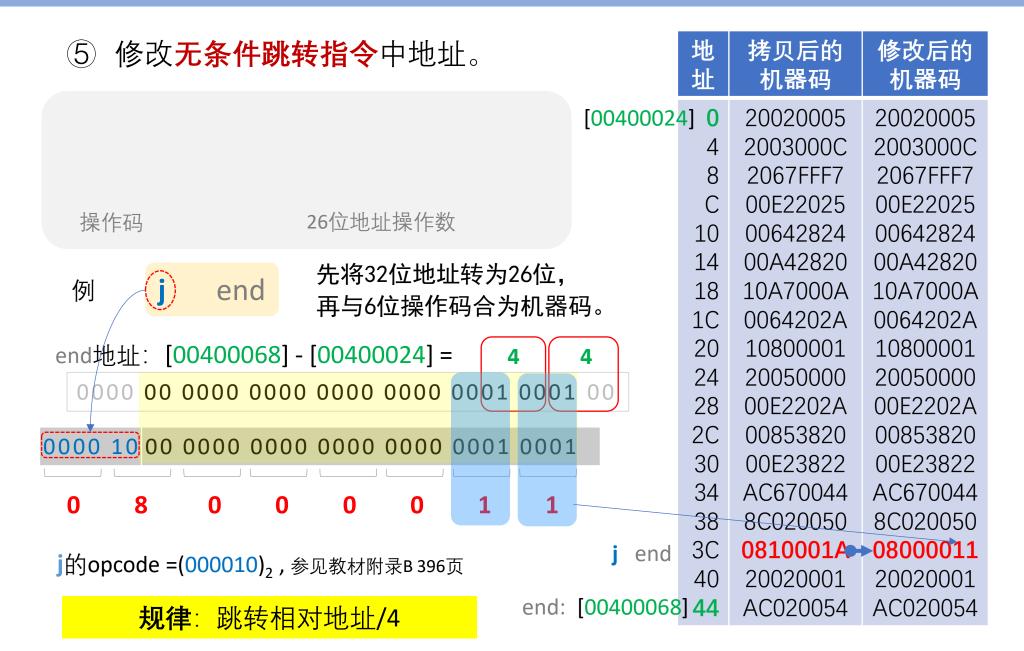
方法一设置QtSpim参数为 Bare Machine

Simple Machine

Bare Machine

| 1 | User Text | Segment [0 | 0400000][0044000 | 1 | User Text | Segment [0 | 0400000][00440000] |
|----|------------|------------|-----------------------------------|----|------------|------------|---------------------------|
| 2 | [00400000] | 8fa40000 | lw \$4, 0(\$29) | 2 | [00400000] | 8fa40000 | lw \$4, 0(\$29) |
| 3 | [00400004] | 27a50004 | addiu \$5, \$29, 4 | 3 | [00400004] | 27a50004 | addiu \$5, \$29, 4 |
| 4 | [00400008] | 24a60004 | addiu \$6, \$5, 4 | 4 | [00400008] | 24a60004 | addiu \$6, \$5, 4 |
| 5 | [0040000c] | 00041080 | sll \$2, \$4, 2 | 5 | [0040000c] | | sll \$2, \$4, 2 |
| 6 | [00400010] | 00c23021 | addu \$6, \$6, \$2 | 6 | [00400010] | 00c23021 | addu \$6, \$6, \$2 |
| 7 | [00400014] | 0c100009 | jal 0x00400024 [n | 7 | [00400014] | 0c100009 | jal 0x00400024 [main] |
| 8 | [00400018] | 00000000 | nop | 8 | [00400018] | 00000000 | nop |
| 9 | [0040001c] | 3402000a | ori \$2, \$0, 10 | 9 | [0040001c] | | ori \$2, \$0, 10 |
| 10 | [00400020] | 0000000c | syscall | 10 | [00400020] | | syscall |
| 11 | [00400024] | 20020005 | addi \$2, \$0, 5 | 11 | [00400024] | 20020005 | addi \$2, \$0, 5 |
| 12 | [00400028] | 2003000c | addi \$3, \$0, 12 | 12 | [00400028] | | addi \$3, \$0, 12 |
| 13 | [0040002c] | 2067fff7 | addi \$7, \$3, -9 | 13 | [0040002c] | 2067fff7 | addi \$7, \$3, - 9 |
| 14 | [00400030] | 00e22025 | or \$4, \$7, \$2 | 14 | [00400030] | 00e22025 | or \$4, \$7, \$2 |
| 15 | [00400034] | 00642824 | and \$5, \$3, \$4 | 15 | [00400034] | 00642824 | and \$5, \$3, \$4 |
| 16 | [00400038] | 00a42820 | add \$5, \$5, \$4 | 16 | [00400038] | | add \$5, \$5, \$4 |
| 17 | [0040003c] | 10a7000b | beq \$5, \$7, 44 [€ | 17 | [0040003c] | | beq \$5, \$7, 40 [end-(|
| 18 | [00400040] | 0064202a | slt \$4, \$3, \$4 | 18 | [00400040] | | slt \$4, \$3, \$4 |
| 19 | [00400044] | 10800002 | beq \$4, \$0, 8 [ar | 19 | [00400044] | | beq \$4, \$0, 4 [around |
| 20 | [00400048] | 20050000 | addi \$5, \$0, 0 | 20 | [00400048] | | addi \$5, \$0, 0 |
| 21 | [0040004c] | 00e2202a | slt \$4 , \$7 , \$2 | 21 | [0040004c] | 00e2202a | slt \$4, \$7, \$2 |
| 22 | [00400050] | 00853820 | add \$7, \$4, \$5 | 22 | [00400050] | 00853820 | add \$7, \$4, \$5 |
| 23 | [00400054] | 00e23822 | sub \$7, \$7, \$2 | 23 | [00400054] | | sub \$7, \$7, \$2 |
| 24 | [00400058] | ac670044 | sw \$7, 68(\$3) | 24 | [00400058] | | sw \$7, 68(\$3) |
| 25 | [0040005c] | 8c020050 | lw \$2, 80(\$0) | 25 | [0040005c] | 8c020050 | lw \$2, 80(\$0) |
| 26 | [00400060] | 0810001a | j 0x00400068 [enc | 26 | [00400060] | 0810001a | j 0x00400068 [end] |
| 27 | [00400064] | 20020001 | addi \$2, \$0, 1 | 27 | [00400064] | 20020001 | addi \$2, \$0, 1 |
| 28 | [00400068] | ac020054 | sw \$2, 84(\$0) | 28 | [00400068] | ac020054 | sw \$2, 84(\$0) |
| 29 | | | | 29 | | | |

ROM/RAM初始化文件.COE的制作-3



ROM/RAM初始化文件.COE的制作-4

- ⑥ 添加coe文件头描述语句。 并在每行机器指令后加","号 最后一行结尾加";"号
- ⑦ 保存为test.coe文件。

```
MEMORY INITIALIZATION RADIX=16;
    MEMORY INITIALIZATION VECTOR=
                                  ▶ 16进制
    20020005,
    2003000c,
   2067fff7,
   00e22025,
    00642824,
    00a42820,
   10a7000a,
  0064202a,
  10800001,
   20050000,
  00e2202a,
14 00853820,
15 00e23822,
16 ac670044,
17 8c020050,
18 08000011,
   20020001,
    ac020054;
```

Ultraedit软件加亮显示MIPS汇编文件

• 下载相应的格式显示配置文件(.uew)

http://www.ultraedit.com/downloads/extras/wordfiles.html#wordfiles

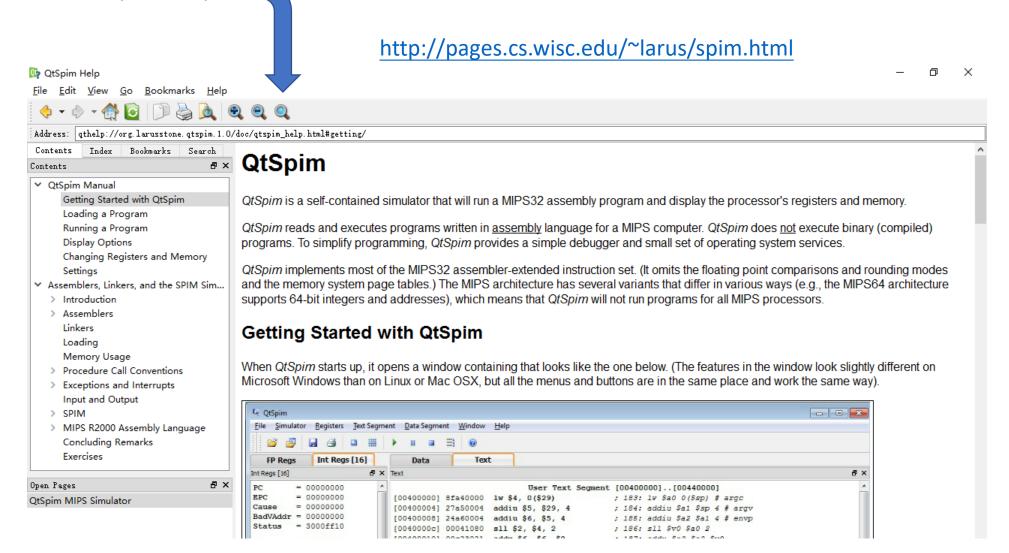
• 将该文件放到路径: 【注】IDMComp为隐藏子目录

C:\Users\你的用户名\AppData\Roaming\IDMComp\UltraEdit\wordfiles

```
MIPS Assembler
                              🤚 风格建设者
          格式化
                               HTML / XML
 Hello world.asm
   # Hello world
         .data
   str: .asciiz "Hello world.\n"
         .text
         .globl main
                    # execution starts here
   main:
        la $a0,str # put string address into a0
                    # system call to print
        li $v0,4
        syscall
                    # print the string
                    # system call to exit
10
        li $v0,10
        syscall
                    # exit
```

参考资料





| 类型 | 指令 | 指令举例 | 含义 | 备注 |
|--------|-----------|---------------------|----------------------------------|-----------------|
| 算术运算 | 加法 | add \$s1,\$s2,\$s3 | \$s1=\$s2+\$s3 | 三个寄存器操作数 |
| | 减法 | sub \$s1,\$s2,\$s3 | \$s1=\$s2-\$s3 | 三个寄存器操作数 |
| | 加立即数 | addi \$s1,\$s2,20 | \$s1=\$s2+20 | 用来加立即数 |
| 数据传送 | 读取字 | lw \$s1,20(\$s2) | \$s1=mem[\$s2+20] | 从内存读字到寄存器 |
| | 存储字 | sw \$s1,20(\$s2) | mem[\$2+20] =\$s1 | 从寄存器写字到内存 |
| | 读取半字 | lh \$s1,20(\$s2) | \$s1=mem[\$s2+20] | 从内存读半字到寄存器 |
| | 读取无符号半字 | lhu \$s1,20(\$s2) | \$s1=mem[\$s2+20] | 从内存读半字到寄存器 |
| | 存储半字 | sh \$s1,20(\$s2) | mem[\$s2+20] =\$1 | 从寄存器写半字到内存 |
| | 读取字节 | lb \$s1,20(\$s2) | \$s1=mem[\$s2+20] | 从内存读字节到寄存器 |
| | 读取无符号字节 | lbu \$s1,20(\$s2) | \$s1=mem[\$s2+20] | 从内存读字节到寄存器 |
| | 存储字节 | sb \$s1,20(\$s2) | mem[\$s2+20] =\$s1 | 从寄存器写字节到内存 |
| | 读取链接字 | 11 \$s1,20(\$s2) | \$s1= mem[\$s2+20] | 读字作为原子交换的第一半 |
| | 条件存储字 | sc \$s1,20(\$s2) | mem[\$s2+20] =s\$1;\$s1=0或1 | 写字作为原子交换的第二半 |
| | 读取立即数到高半字 | lui \$s1,20 | \$s1=20*2 ¹⁶ | 读取一个常数到高16位 |
| 逻辑操作 | 与 | and \$s1,\$s2,\$s3 | \$s1=\$s2&\$s3 | 三个寄存器,位与 |
| | 或 | or \$s1,\$s2,\$s3 | \$s1=\$s2 \$s3 | 三个寄存器,位或 |
| | 或非 | nor \$s1,\$s2,\$s3 | \$s1=~(\$s2 \$s3) | 三个寄存器,位或非 |
| | 与立即数 | andi \$s1,\$s2,20 | \$s1=\$s2&20 | 寄存器与立即数位与 |
| | 或立即数 | ori \$s1,\$s2,20 | \$s1=\$s2 20 | 寄存器与立即数位或 |
| | 逻辑左移 | sll \$s1,\$s2,10 | \$s1=\$s2<<10 | 左移常数次 |
| | 逻辑右移 | srl \$s1,\$s2,10 | \$s1=\$s2>>10 | 右移常数次 |
| 条件跳转 | 相等转移 | beq \$s1,\$s2,25 | If (\$s1=\$s2) goto PC+4+25*4 | 相等测试,转移 |
| | 不相等转移 | bne \$s1,\$s2,25 | If (\$s1!=\$s2) goto PC+4+25*4 | 不相等测试,转移 |
| | 小于设置 | slt \$s1,\$s2,\$s3 | If(\$s2<\$s3) \$s1=1 else \$s1=0 | 比较小于设置\$s1=1 |
| | 低于设置 | sltu \$s1,\$s2,\$s3 | If(\$s2<\$s3) \$s1=1 else \$s1=0 | 比较低于设置\$s1=1 |
| | 小于常数设置 | slti \$s1,\$s2,20 | If(\$s2<20) \$s1=1 else \$s1=0 | 和常数比较小于设置\$s1=1 |
| | 低于常数设置 | sltiu \$s1,\$s2,20 | If(\$s2<20) \$s1=1 else \$s1=0 | 和常数比较低于设置\$s1=1 |
| 无条件跳转 | 直接跳转 | j 2500 | goto 2500*4 | 跳转到目标地址 |
| | 间接跳转 | jr \$ra | goto \$ra | 用在分支和子程序返回 |
| | 跳转并链接 | jal 2500 | \$ra=PC+4; goto 2500*4 | 用在子程序调用 |
| 系统功能调用 | 系统功能调用 | syscall | | 实现人机对话 |