0

DIV
Divide
Source A   FanucLoopVFD_N0
45:I.OutputFreq
0
Source B   60

Dest   Fanuc_Loop_Spee
d
0

1

DIV
Divide
Source A   ABBLoopVFD_N046
:I.OutputFreq
0
Source B   60

Dest   ABB_Loop_Speed
0

2

EQU
Equal
Source A   HMI_Disp_No
600
Source B   100

EnableBits.0   EnableBits.1   EnableBits.2
(L)   (U)   (L)

3

EQU
Equal
Source A   HMI_Disp_No
600
Source B   200

EnableBits.0   EnableBits.1   EnableBits.2
(L)   (L)   (U)

4

EQU
Equal
Source A   HMI_Disp_No
600
Source B   300

EnableBits.0   EnableBits.1   EnableBits.2
(L)   (L)   (L)

5

EQU
Equal
Source A   HMI_Disp_No
600
Source B   400

EnableBits.0   EnableBits.1   EnableBits.2
(U)   (L)   (L)

6

EQU
Equal
Source A   HMI_Disp_No
600
Source B   500

EnableBits.0   EnableBits.1   EnableBits.2
(L)   (L)   (L)

HMI - Ladder Diagram
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AdvManLab:MainTask:MainProgram
10/5/2017 9:06:07 PM

Total number of rungs in routine: 8
C:\Users\VRMILLING\Documents\control_team_manuals\Logic W2017\AdvManLabCurrent.ACD

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```
        ┌─────EQU─────────┐                          EnableBits.0   EnableBits.1   EnableBits.2
        │ Equal           │                               (L)            (L)            (L)
        │ Source A  HMI_Disp_No │
        │               600 │
        │ Source B      600 │
        └─────────────────┘
```

(End)