

INSTRUCCION	Nombre	OPcode	Function	PcSrc	RegDst	ALUSrc	ALUOp	MemRead	MemWrite	Branch	RegWrite	MemtoReg
R-Type		0		00->sum 01->offset 10->jump 11->rs	01->rd 00->rt 10->gpr31	00->rt, rs 10->offset, rs 11->offset, rt						00->mem 01->alu 10-> return addr
SLL	Shift Word Left Logical	000000	000000	00	01	11	0000	0	0	0	1	01
SRL	Shift Word Right Logical	000000	000010	00	01	11	0000	0	0	0	1	01
SRA	Shift Word Right Arithmetic	000000	000011	00	01	11	0000	0	0	0	1	01
SLLV	Shift Word Left Logical Variable	000000	000100	00	01	00	0000	0	0	0	1	01
SRLV	Shift Word Right Logical Variable	000000	000110	00	01	00	0000	0	0	0	1	01
SRV	Shift Word Right Arithmetic Variable	000000	000111	00	01	00	0000	0	0	0	1	01
ADDU	Add Unsigned Word	000000	100001	00	01	00	0000	0	0	0	1	01
SUBU	Subtract Unsigned Word	000000	100011	00	01	00	0000	0	0	0	1	01
AND	And	000000	100100	00	01	00	0000	0	0	0	1	01
OR	Or	000000	100101	00	01	00	0000	0	0	0	1	01
XOR	Exclusive OR	000000	100110	00	01	00	0000	0	0	0	1	01
NOR	Not Or	000000	100111	00	01	00	0000	0	0	0	1	01
SLT	Set On Less Than	000000	101010	00	01	00	0000	0	0	0	1	01
I-Type												
LB	Load Byte	100000	-	00	00	10	0001	1	0	0	1	00
LH	Load Halfword	100001	-	00	00	10	0001	1	0	0	1	00
LW	Load Word	100011	-	00	00	10	0001	1	0	0	1	00
LWU	Load Word Unsigned	100111	-	00	00	10	0001	1	0	0	1	00
LBU	Load Byte Unsigned	100100	-	00	00	10	0001	1	0	0	1	00
LHU	Load Halfword Unsigned	100101	-	00	00	10	0001	1	0	0	1	00
SB	Store Byte	101000	-	00	-	10	0001	0	1	0	0	-
SH	Store Halfword	101001	-	00	-	10	0001	0	1	0	0	-
SW	Store Word	101011	-	00	-	10	0001	0	1	0	0	-
ADDI	Add Immediate Word	001000	-	00	00	10	0001	0	0	0	1	01
ANDI	AND IMMEDIATE	001100	-	00	00	10	0010	0	0	0	1	01
ORI	Or Immediate	001101	-	00	00	10	0011	0	0	0	1	01
XORI	Exclusive OR Immediate	001110	-	00	00	10	0100	0	0	0	1	01
LUI	Load Upper Immediate	001111	-	00	00	10	0101	0	0	0	1	01
SLTI	Set on Less Than Immediate	001010	-	00	00	10	0111	0	0	0	1	01
BEQ	Branch on Equal	000100	-	01	-	00	1001	0	0	1	0	-
BNE	Branch on Not Equal	000101	-	01	-	00	1011	0	0	1	0	-
J	Jump	000010	-	10	-	-	-	0	0	0	0	-
JAL	Jump And Link	000011	-	10	10	-	-	0	0	0	1	10
J-Type												
JR	Jump Register	000000	001000	11	-	-	-	0	0	0	0	-
JALR	Jump And Link Register	000000	001001	11	01	-	-	0	0	0	1	10