



SYMBIOSIS INTERNATIONAL (DEEMED UNIVERSITY)

(Established under section 3 of the UGC Act, 1956)

Re-accredited by NAAC with 'A+' Grade | Awarded Category - I by UGC

Founder: Prof. Dr. S. B. Mujumdar, M. Sc., Ph. D. (Awarded Padma Bhushan and Padma Shri by President of India)

Department: ENTC

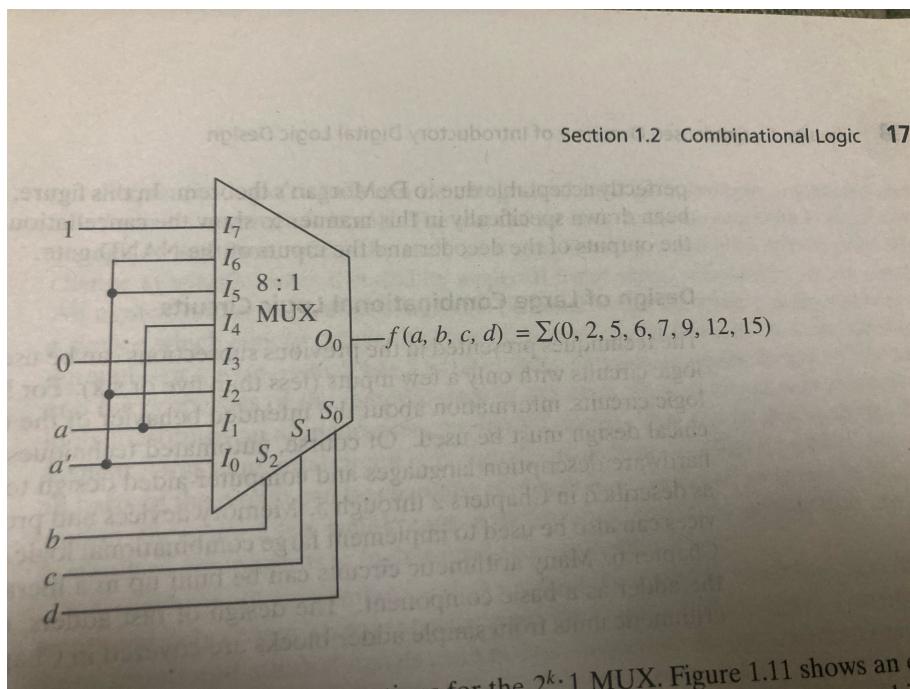
Batch: 2022-26

Course name: VLSI Design

Year / Sem: V

Assignment No1

1. Write Verilog code to implement the following logic.



Also write the test bench for the same.

2.

Write a Verilog code for the 2:8 decoder using behavioural and structural modelling styles. Write the test bench for the same