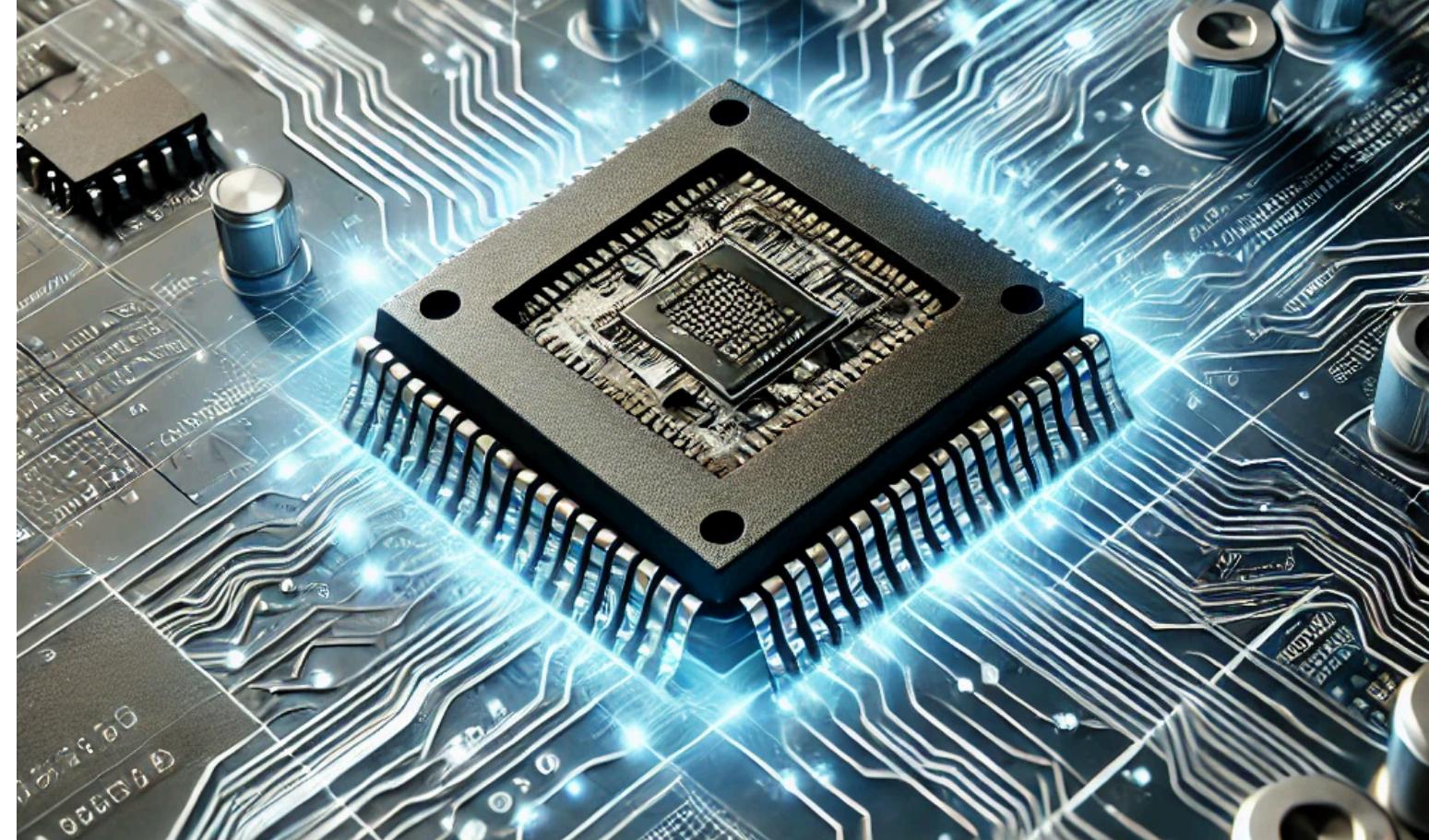


ALTERA CPLD'S:

VERSATILE PROGRAMMABLE LOGIC DEVICES



★ WHAT ARE CPLD'S

A Complex Programmable Logic Device is a programmable logic device with a fixed architecture, allowing for specific logic functions and predictable timing. It retains its configuration without power and is commonly used in simple control logic and signal routing applications.

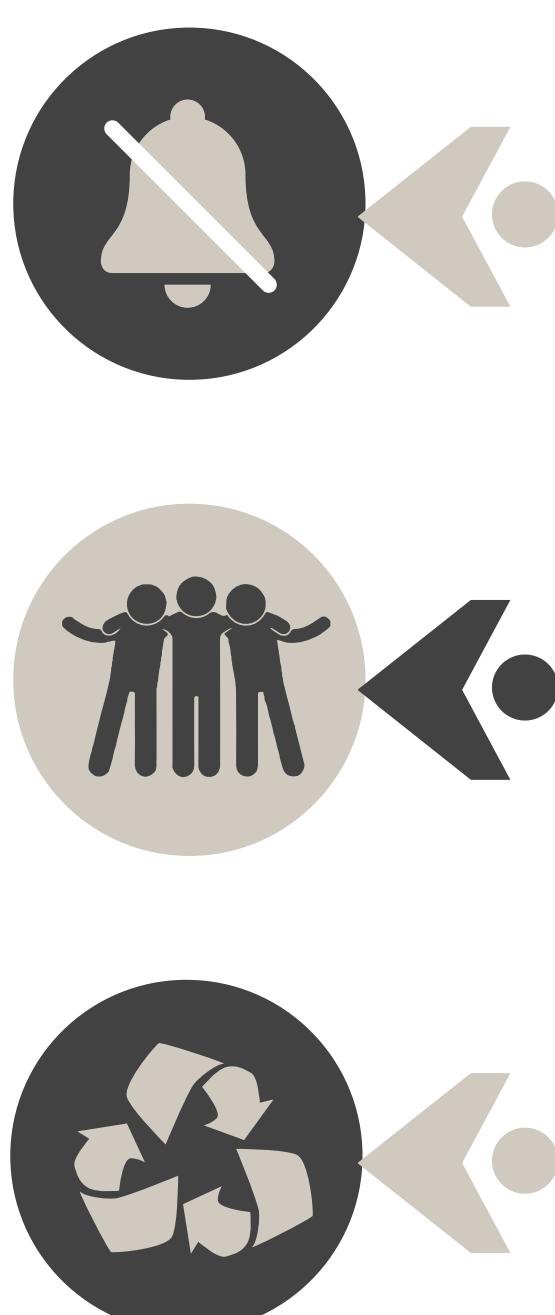
1M+
PRODUCTION
All over the world

Integration Capabilities: Altera CPLDs can integrate various functions, such as logic processing, state machines, and interfacing, into a single device, reducing the need for multiple components and streamlining circuit design.

Design Flexibility: They support a wide range of design applications, from simple logic gates to complex state machines, allowing engineers to implement tailored solutions for specific requirements without extensive redesign efforts.

ROBOUST: DESIGN TOOL SUPPORT

Altera CPLDs benefit from comprehensive development environments like Intel Quartus Prime, facilitating efficient design entry, simulation, and programming which streamlines the design process and enhances overall performance and reliability.



RECONFIGURABILITY:

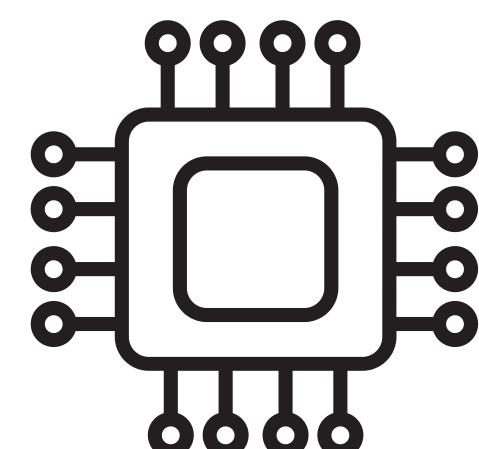
Altera CPLDs can be easily programmed and reprogrammed using hardware description languages (HDLs) like Verilog and VHDL, allowing for flexibility in design and functionality.

RELIABLE OPERATION:

With a fixed architecture and non-volatile memory, Altera CPLDs offer predictable timing and reliable operation, making them suitable for applications requiring consistent performance.

ENERGY EFFICIENCY:

They are designed for energy efficiency, making them ideal for embedded systems and applications where power usage is a critical factor.



HIGH LOGIC DENSITY

Altera CPLDs offer a high density of logic elements in a compact form factor, allowing for complex designs while minimizing board space.