Course	21CCC2041 Course	COMPILER DESIGN	Course	_	PROFESSIONAL CORE	L	Т	Р	С
Code	Name	COMPLEX DESIGN	Category	C	PROFESSIONAL CORE	2	0	2	3

Pre-requisite Courses	Nil	Co- requisite Courses	Nil	Progressive Courses	Nil
Course Offeri	ing Department	School of Computing	Data Book / Codes / Standards		Nil

Course Learning Rationale (CLR): The purpose of learning this course is to:			Program Outcomes (PO)											Prog			
CLR-1:	P-1: Outline the implementation of Lexical Analyzer				3	4	5	6	7	8	9	10	11	12		ic ies	
CLR-2:	To learn the various parsing	techn <mark>iques</mark>	agb	1	ъ	SI					Nork		e,				
CLR-3:					ent	stigations oblems	age	70			, K		Finance	guir			
CLR-4:	LR-4: To learn the implementation of code generator			Analysis	elopme	estig	ool Usage	rand	∞ >		Tear	ig	≅	ä			
CLR-5:	ELR-5: Identify the various methods for Code Optimizer				ign/deve	ict inve	, I		vironment &		ividual &	ommunication	Project Mgt.	ong Le	_		_
Course (	Outcomes (CO):	At the end of this course, learners will be able to:	Engine	Problem	Design	Condu of com	Modern	The en	Enviro Sustai	Ethics	Individ	Comn	Projec	Life Lo	PSO-	PSO-2	PSO-3
CO-1:	Acquire knowledge of Lexica	al Analyzer from a specification of a language's lexical rules	3	2	-	- 1	2	- 1	-	-	-	-	-	-	-	1	-
CO-2:	<b>D-2:</b> Apply different parsing algorithms to develop the parsers for a given grammar				-		2	-	-	-	-	-	-	-	-	1	-
CO-3:	O-3: Gain knowledge to translate a system into various intermediate codes			2	-	-	2		-	-	-	-	-	-	-	1	-
CO-4:	<b>0-4:</b> Analyze the methods of implementing a Code Generator for compilers				-	3	-	-	-	-	-	-	-	-	-	1	-
CO-5:	Design the methods of developing a Code Optimizer				2	3	-	-		-	_	-	-	-	-	1	-

Unit-1 - Introduction 12 Hour

Compilers-Phases of Compiler-Cousins of the Compiler-Grouping of Phases-Compiler construction tools- Lexical Analysis-Role of Lexical Analyzer-Input Buffering -Specification of Tokens -LEX -Finite Automata-Regular Expressions to Automata -Minimizing DFA. 12 Hour

Unit-2 - Top Down Parsing

Role of Parser-Grammars-Error Handling-Context-Free Grammars-Writing a grammar- Elimination of Ambiguity-Left Recursion- Left Factoring-Top Down Parsing — Recursive Descent Parser- Predictive Parser-LL(1) Parser- Computation of FIRST-Computation of FOLLOW-Construction of a predictive parsing table-Predictive Parsers LL(1) Grammars- Predictive Parsing Algorithm- Problems related to Predictive Parser Error Recovery in Predictive Parsing-.

Unit-3 - Bottom-Up Parsing 12 Hour

Bottom Up Parsing-Reductions-Handle Pruning-Shift Reduce Parser-Problems related to Shift Reduce Parsing-Operator Precedence Parser, LEADING, TRAILING -LR Parser- LR Parsers- Need of LR Parsers-LR (0)Item-Closure of Item Sets- Construction of SLR Parsing Table -Problems related to SLR-Construction of Canonical LR(1)- Problems related to CLR - LALR Parser — Problems related to LALR-YACC.

Unit-4 - Code Generation 12 Hour

Intermediate Code Generation- prefix – postfix notation- Quadruple - triple - indirect triples Representation- Syntax tree- Evaluation of expression - Three-address code- Synthesized attributes - Inherited attributes Intermediate languages – Declarations- Assignment Statements- Boolean Expressions- Case Statements- Back patching – Procedure calls- Code Generation- Issues in the design of code generator- The target machine – Runtime Storage management- A simple Code generator- Code Generation Algorithm- Register and Address Descriptors.

Unit-5 - Code Optimization 12 Hour

Code optimization - Principal Sources of Optimization- Function Preserving Transformation- Loop Optimization- Peephole optimization — DAG- Basic Blocks- Flow Graphs- Global Data Flow Analysis — Efficient Data Flow Algorithm- Runtime Environments- Source Language issues- Storage Organization- Activation Records- Storage Allocation strategies.

Lab Experiments	
Lab 1 - Implementation of Lexical Analyzer	Lab9 Computation of LR (0) items
Lab 2 conversion from Regular Expression to NFA	Lab 10-Intermediate code generation – Postfix, Prefix
Lab 3 Conversion from NFA to DFA	Lab 11 Intermediate code generation – Quadruple, Triple, Indirect triple
Lab 4 Elimation of Ambiguity, Left Recursion and Left Factoring	Lab 12: A simple code Generator
Lab 5 -FIRST AND FOLLOW computation	Lab 13 Implementation of DAG
Lab 6 Predictive Parsing Table	Lab 14: Implementation of Global Data Flow Analysis
Lab 7 - Shift Reduce Parsing	Lab 15: Implement any one storage allocation strategies (heap, stack, static)
Lab 8- Computation of LEADING AND TRAILING	4) VA

Learning	1.	Alfred V. Aho, Monica S. Lam, Ravi Sethi, Jeffrey D. Ullman, Compilers: Principles, Techniques and Tools, Second Edition, Pearson Education, 2011.	David Galles, "Modern Compiler Design", Pearson Education, Reprit 2012.  Raghavan V., "Principles of CompilerDesign", Tata McGraw Hill Education Pvt. Ltd.,
Resources	2.	S. Godfrey Winster, S. Aruna Devi, R. Sujatha, "Compiler Design", Yesdee Publishing Pvt. Ltd, 2016.	2010.
	3.	K .Muneeswaran,"CompilerDesign", Oxford Higher Education, Fourth Edition, 2015.	

rning Assessn	nent -							
	Bloom's Leve <mark>l of Thin</mark> king	CLA-1 Avera	Formative CLA-1 Average of unit test (45%)		g Learning LA-2 5%)	Summative Final Examination (40% weightage)		
		Theory	Practice	Theory	Practice	<u>The</u> ory	Practice	
Level 1	Remember	15%			15%	15%	-	
Level 2	Understand	25%	7 - F 1	and the second	20%	25%	-	
Level 3	Apply	30%		100	25%	30%	-	
Level 4	Analyze	30%		A STATE OF THE PARTY.	25%	30%	-	
Level 5	Evaluate	The state of			10%	-	-	
Level 6	Create	/	- 1	-	5%	1	-	
	Total -	10	0 %	10	00 %	10	00 %	

Course Designers									
Experts from Industry	Experts from Higher Technical Institutions	Internal Experts							
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	<ol> <li>Dr. M. Shyamala Devi, Professor, Department of Computer Science and Engineering, Vel Tech Rangarajan Dr. Sagunthala R &amp; D Institute of Science and Technology, Chennai.</li> </ol>	2. Dr. Godfrey Winster S SRMIST							