

kernel maxwell_partial_rcv

maxwell_partial_rcv.B0

maxwell_partial_rcv.B1

loop

Store Info

Width: 256 bits

Type: Burst-coalesced write-ack non-aligned

Stall-free: No

Start Cycle: 56

Latency: 52

RD

ST

RD

ST

RD

ST

RD

ST

loop end

