

Readme

Insmmod the kernel file i2c_flash.ko

User side file is main_2.c

The user side program tests the all the commands in following steps

- 1) Flash erase entire chip – fill it with ones
- 2) Read the entire chip and print it
- 3) Do FLASHGETS, print busy status
- 4) Do FLASHSETP , set address to page 502
- 5) Write 20 pages , it overlaps after 512, each page has 64 ascii characters from no. 48 to 112
- 6) Do FLASHSETP, set address to page 495, reading starts here
- 7) Read 30 pages, overlaps after 512
- 8) Do FLASHGETP, get current address after reading
- 9) Print Read data

Sleeps (delays) have been included in between to facilitate viewing.

All 4 IOCTL commands tested.

Read, Write multiple pages along with overlap tested.

Electrical connections

EEPROM connections

Pin1(A0), Pin 2(A1) , Pin 4(Vss) to Gnd.

Pin 3(A2): to Vcc

Pin 5(SDA): to SDA of Galileo. Also Pin 5 to Resistor that goes to Vcc(Not Shown).

Pin 6(SCL): to SCL of Galileo. Also Pin 6 to Resistor that goes to Vcc. (Not Shown)

Pin 7(WP): (Write Protection) Not Connected

Pin 8(Vcc): to Vcc.

BUSY_LED

Also Pin 5 to Resistor that goes to Vcc. Pin 8(PWM) to resistor to LED(Anode) to Gnd (Cathode).(Not shown)

See Schematic below-

Resistors and led not shown

