AGNUS CARD CONNECTOR

SUPER FAT LADY

Probably not a good idea to change J101.

A500+ JP3 removed (swaps upper & lower ram banks)
A500+ JP4 removed (bypasses U32 for 512k system)

RAM CONTROL LOGIC AND ADDRESS BUS BUFFERS

CHIP RAM "ONBOARD"

CHIP RAM "TRAPDOOR"

28 MHZ CLOCK

N.B. no data sheet for X1.
Removed XCLK option.

Remember to read the readme file!

AGNUS DECOUPLING

LOGIC DECOUPLING

BOARD DECOUPLING

RAM DECOUPLING — "ONBOARD"

RAM DECOUPLING — "TRAPDOOR"

OPTIONAL TERMINATION

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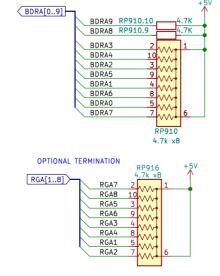   CERN—OHL—S) license applies, subject to conditions 1—3 above.
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DISCLAIMER:
This is an experimental hobbyist project which may not work. There are probably design flaws and faults. Constructors will require equipment including soldering and rework, test gear, oscilloscope, and advanced soldering, electronics, and troubleshooting skills.

If you choose to construct this project please assume there will be no support available to help you get it working. You are on your own. If you can't get it working or break your Amiga chipset or something else do not blame anyone but yourself. You were warned!

No person or organisation associated with this project accepts any liability or responsibility for damages or injury as a result of someone using this board.

You agree to these terms if you have the board fabricated or you obtain a fabricated board.

That said, remember it is a hobby and the whole point is to enjoy making it and to get satisfaction from having made a unique piece of hardware. Remember to have fun. If you start cracking the shits walk away and finish it another day.