

Theoretical Study of the Impact of Channel Estimation Errors on the Performance of IDMA Detectors

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Abstract—In this paper, the performance of an Interleave-Division-Multiple-Access (IDMA) detector is considered when channel estimation errors are present. This detector can be quite effective under severe channel estimation errors when channel estimator statistics are considered. The detector is generalized for QPSK signaling. It is shown that the overall effect of the CSI mismatch can be seen as an additive noise term, without approximation. Moreover, the term is constant for all users, paths and for in-phase and quadrature components. The performances of the detector are studied by Monte-Carlo simulations. From the results, it is shown that the improvement depends on the severity of the mismatch, the number of paths and the number of users. A 10% capacity increase can be obtained with respect to the number of users.

Index Terms—IDMA, multi-user detection, iterative decoding, channel estimation error, channel state information.

I. INTRODUCTION

Interleave Division Multiple Access (IDMA) is a promising wideband multiuser method for future wireless communication [1]. The method separates the users by assigning them different independent interleavers. IDMA systems have high spectral efficiency, are resistant to and exploit multipath and delays and is suited for very large number of users. Also, IDMA has been combined with OFDM [2] and MIMO systems [3]. Moreover, it allows simple chip-by-chip detection and parallel iterative interference cancellation (PIC). However, knowledge of the channel state information (CSI) is required. In [4]-[6], channel estimation for IDMA has been considered.

Detection of IDMA signals has been widely studied, mostly assuming perfect CSI. However, this hypothesis is not generally true. Hence, [5,7,8] have considered the effects of imperfect CSI. For this, channel error statistics are needed and can be obtained from the estimator. However, the method

proposed in [7] needs long-term channel statistics which are not readily available. Otherwise, the approach in [8] does not require these statistics but is incomplete. A more appropriated method is shown in [5] that does not need long-term channel statistics as in [7], is more precise than [8] and is less complex than both. The CSI estimation errors of all users and all paths can be treated as additive noise. However, the performance of this detector has not been reported since the objective of [5] was to present a channel estimation method. In this paper we propose to generalize this technique to QPSK signaling and analytically determine the performance of this detector.

The rest of this paper is organized as follows. In Section 2, a brief description of a conventional IDMA system is given. In Section 3, the generalized IDMA receiver is developed. In Section 4, numerical results are shown. Finally, Section 5 draws conclusions and discusses other implications of the results.

II. IDMA SYSTEM DESCRIPTION

A. Transmitter Structure

Transmitters used in IDMA systems are shown in Fig. 1. First, a low rate forward error correction (FEC) code is applied to the data sequence. Typically, data is convolutionally encoded and then spread by direct sequence spreading. The spread sequence is interleaved at the chip level and QPSK modulated. If the error control code is identical for every user, then the interleavers must be different in order to allow user separation. Independent interleavers produce signals which appear random. Implementing the interleaver before modulation makes the interleaver twice as effective, as the inphase and quadrature components of the sequence become independent. The transmitter output of user k is hence

$$x_k(j) = x_k^{Re}(j) + ix_k^{Im}(j) \quad (1)$$

where $i = \sqrt{-1}$ and where $x_k^{Re}(j)$ is independent of $x_k^{Im}(j)$.

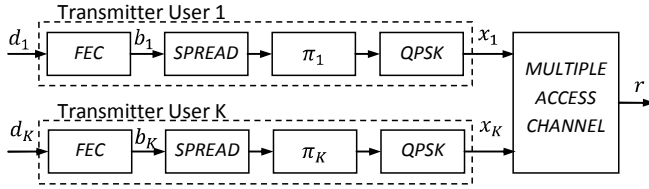


Fig. 1. IDMA transmitters for K users and Multiple Access Channel Model

B. Complex Multiple-Access Channel

The outputs of the transmitters are convoluted and summed by a complex, multipath, multiple-access channel. It is assumed for simplicity that signals are fully synchronized. However, different delays between users can be included in the impulse response of the channels. Hence, the signal at the receiver is:

$$r^{Re}(j) = \sum_{k,l} (h_{k,l}^{Re} x_k^{Re}(j-l) - h_{k,l}^{Im} x_k^{Im}(j-l)) + n^{Re}(j) \quad (2)$$

$$r^{Im}(j) = \sum_{k,l} (h_{k,l}^{Re} x_k^{Im}(j-l) + h_{k,l}^{Im} x_k^{Re}(j-l)) + n^{Im}(j) \quad (3)$$

where $h_{k,l}^{Re}$ is the real part of the channel gain for l^{th} path of the k^{th} user. The real part of the noise is $n^{Re}(j)$. The additive noise is considered white, Gaussian with the following statistics:

$$E[n^{Re}(j)] = E[n^{Im}(j)] = 0 \quad (4)$$

$$V[n^{Re}(j)] = V[n^{Im}(j)] = \sigma_n^2/2 \quad (5)$$

C. Conventional IDMA Receiver

A conventional IDMA receiver uses chip-by-chip detection and parallel interference cancellation (PIC) (Fig. 2.) For QPSK signaling, it also separates the detection of the real and imaginary parts of the chip. First, the Elementary Signal Estimator (ESE) gives the estimation of the chips. It makes use of the channel statistics to produce log-likelihood ratios of the signals (LLRs.) The LLRs of different paths are resolved independently and then combined.

To produce better estimates, interference statistics are generated. Next, these LLRs are demultiplexed for the In-Phase and Quadrature components, deinterleaved and despread (RAKE) for each user. Then, a soft-input soft-output decoder is used to produce the extrinsic LLRs of the coded bits. Afterwards, a spreader, interleaver and multiplexer for QPSK signaling are used. The results are fed to the PIC. After some iterations of PIC, the decoders produce the uncoded data estimates.

For a more detailed treatment of the conventional IDMA receiver, see [1].

III. CHANNEL ESTIMATION ERROR RESISTANT ELEMENTARY SIGNAL ESTIMATOR

This section shows the calculation of the signal statistics when the channel estimation errors are considered, without prior knowledge of the channel distribution.

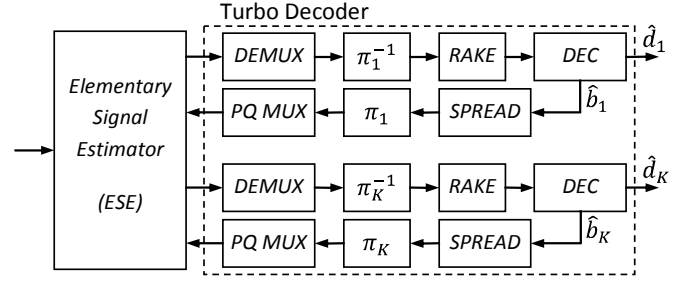


Fig. 2. Conventional IDMA receiver with multi user detection and parallel interference cancellation.

A. Channel Estimator Performances

A channel estimator can generally give an error variance of its estimates. When complex multipath is considered, an estimate of each path is given with associated error variance for both real and imaginary components. These are not necessarily equal. Hence, we have the channel estimates:

$$\hat{h}_{k,l}^{Re}(j) = h_{k,l}^{Re}(j) + e_{k,l}^{Re}(j), \quad \hat{h}_{k,l}^{Im}(j) = h_{k,l}^{Im}(j) + e_{k,l}^{Im}(j) \quad (6)$$

where $\hat{h}_{k,l}^{Re}$ is the channel estimate of the real part of the l^{th} path of the k^{th} user and $e_{k,l}^{Re}$ is its corresponding error with the following statistics:

$$E(e_{k,l}^{Re}) = E(e_{k,l}^{Im}) = 0 \quad (7)$$

$$Var(e_{k,l}^{Re}) = \sigma_{\hat{h}_{k,l}^{Re}}^2, \quad Var(e_{k,l}^{Im}) = \sigma_{\hat{h}_{k,l}^{Im}}^2 \quad (8)$$

B. Channel Error Effect

Here, it is shown that the overall effect of the combination of every user, every path and chip component errors can be treated as a single equivalent noise term. This noise term can hence be included in the thermal additive noise.

First, the channel impulse response in (2) is substituted by its estimated version:

$$r^{Re}(j) = \sum_{k,l} ((\hat{h}_{k,l}^{Re} - e_{k,l}^{Re}) x_k^{Re}(j-l) - (\hat{h}_{k,l}^{Im} - e_{k,l}^{Im}) x_k^{Im}(j-l)) + n^{Re}(j). \quad (9)$$

Next, it is reformulated in order to include the channel error into the noise term:

$$r^{Re}(j) = \sum_{k,l} (\hat{h}_{k,l}^{Re} x_k^{Re}(j-l) - \hat{h}_{k,l}^{Im} x_k^{Im}(j-l)) + n^{Re'}(j) \quad (10)$$

where:

$$n^{Re'}(j) = - \sum_{k,l} (e_{k,l}^{Re} x_k^{Re}(j-l) - e_{k,l}^{Im} x_k^{Im}(j-l)) + n^{Re}(j). \quad (11)$$

Equivalent equations can be found for the imaginary components of the signal. The equivalent noise has the following statistics:

$$E[n^{Re'}(j)] = E[n^{Im'}(j)] = 0 \quad (12)$$

$$V[n^{Re'}(j)] = V[n^{Im'}(j)] = \sum_{k,l} (\sigma_{h_{k,l,Re}}^2 + \sigma_{h_{k,l,Im}}^2) + \sigma_n^2 \quad (13)$$

$$E[n^{Re'}(j)] = E[n^{Im'}(j)] = 0, \quad (14)$$

$$Cov[n^{Re'}(j), n^{Im'}(j)] = 0. \quad (15)$$

Hence, the modified noise term is identical for every user, every path and chip component.

C. Statistics of the Received Signal

Statistics of the received signals with consideration of channel estimator performances have the same structure as the one shown in [5]. The only differences are that the estimated multipath components are used instead of the exact value and that the additive noise term is included.

$$E[r^{Re}(j)] = \sum_{k,l} (\hat{h}_{k,l}^{Re}(j)E[x_k^{Re}(j-l)] - \hat{h}_{k,l}^{Im}(j)E[x_k^{Im}(j-l)]) \quad (16)$$

$$E[r^{Im}(j)] = \sum_{k,l} (\hat{h}_{k,l}^{Re}(j)E[x_k^{Im}(j-l)] + \hat{h}_{k,l}^{Im}(j)E[x_k^{Re}(j-l)]) \quad (17)$$

$$V[r^{Re}(j)] = \sum_{k,l} (\hat{h}_{k,l}^{Re^2}(j)V[x_k^{Re}(j-l)] + \hat{h}_{k,l}^{Im^2}(j)V[x_k^{Im}(j-l)]) + V[n^{Re'}(j)] \quad (18)$$

$$Cov[r^{Re}(j), r^{Im}(j)] = \sum_{k,l} (\hat{h}_{k,l}^{Re}(j)\hat{h}_{k,l}^{Im}(j)(V[x_k^{Re}(j-l)] - V[x_k^{Im}(j-l)])) \quad (19)$$

D. Statistics of Real and Imaginary Parts of Chip Signals

Each component of the chip signals is independent of each other due to the interleavers. This independence allows the receiver to resolve each component individually and use interference cancellation for the other component. The component statistics are computed by removing the corresponding contribution to the received signal statistics:

$$E[\zeta_{k,l,Re}^{Re}(j)] = E[r^{Re}(j)] - \hat{h}_{k,l}^{Re}(j)E[x_k^{Re}(j-l)] \quad (20)$$

$$V[\zeta_{k,l,Re}^{Re}(j)] = V[r^{Re}(j)] - \hat{h}_{k,l}^{Re^2}(j)V[x_k^{Re}(j-l)] \quad (21)$$

$$Cov[\zeta_{k,l,Re}^{Re}, \zeta_{k,l,Re}^{Im}] = Cov[r^{Re}(j), r^{Im}(j)] - (\hat{h}_{k,l}^{Re}(j)\hat{h}_{k,l}^{Im}(j)V[x_k^{Re}(j-l)]) \quad (22)$$

where $\zeta_{k,l,Re}^{Re}(j)$ is the real part of the interference of the real component of the l^{th} path of the k^{th} user. The other statistics calculated in the same manner are: $[r^{Im}(j)]$, $V[r^{Im}(j)]$, $E[\zeta_{k,l,Im}^{Re}(j)]$, $E[\zeta_{k,l,Im}^{Im}(j)]$, $V[\zeta_{k,l,Im}^{Re}(j)]$, $V[\zeta_{k,l,Im}^{Im}(j)]$, $Cov[\zeta_{k,l,Im}^{Re}, \zeta_{k,l,Im}^{Im}]$.

E. Complex Elementary Signal Estimator

The elementary signal estimators are calculated using the statistics above. First, the interference is removed for each user, path and component:

$$s_{k,l,Re}^{Re}(j) = r^{Re}(j-l) - E[\zeta_{k,l,Re}^{Re}(j)] \quad (23)$$

The expression of the LLRs for each path is given in (26) and simplified in (27). The matrix $\Sigma_{k,l,Re}$ used in (24) is the covariance matrix of the interference the desired signal.

$$\Sigma_{k,l,Re} = \begin{bmatrix} E[\zeta_{k,l,Re}^{Re}(j)] & Cov[\zeta_{k,l,Re}^{Re}, \zeta_{k,l,Re}^{Im}] \\ Cov[\zeta_{k,l,Re}^{Re}, \zeta_{k,l,Re}^{Im}] & E[\zeta_{k,l,Re}^{Im}(j)] \end{bmatrix} \quad (24)$$

The LLRs of the different paths of each signal are combined:

$$e_{ESE}(x_k^{Re}(j)) = \sum_l e_{ESE}(x_k^{Re}(j))_l \quad (25)$$

Similar equations are developed for imaginary components. Finally, the chip signals statistics can be computed:

$$E[x_k^{Re}(j)] = \tanh(e_{ESE}(x_k^{Re}(j))/2) \quad (28)$$

$$V[x_k^{Re}(j)] = 1 - E[x_k^{Re}(j-l)]^2 \quad (29)$$

IV. SIMULATION RESULTS

We present the performance of IDMA systems with severe channel estimation errors. The simulations are also done to show the impact of considering the channel estimation error on the detector performance. The received power of all users is normalized and the interleavers are generated randomly as well as the channels. A convolutional code of type (2,1,3) is used.

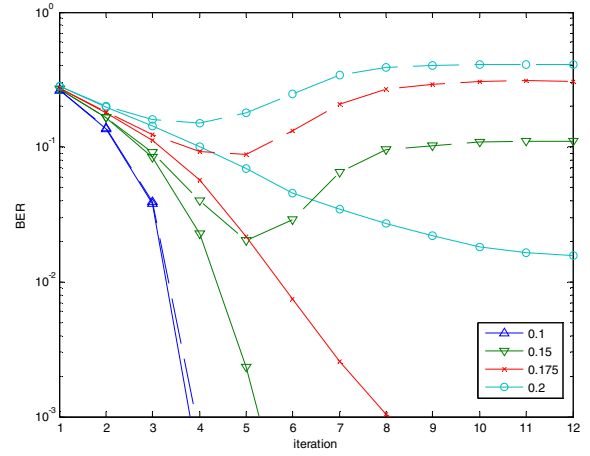


Fig. 3. BER vs number of iteration for channel error standard deviation from 0.1 to 0.2, single path, without noise, 24 users. Solid lines include channel estimation error treatment while dashed lines omit it.

$$e_{ESE}(x_k^{Re}(j))_l = \log \left[\frac{\exp\left(-\frac{1}{2} [s_{k,l,Re}^{Re} - \hat{h}_{k,l}^{Re} \quad s_{k,l,Re}^{Im} - \hat{h}_{k,l}^{Im}] \Sigma_{k,l,Re}^{-1} \begin{bmatrix} s_{k,l,Re}^{Re} - \hat{h}_{k,l}^{Re} \\ s_{k,l,Re}^{Im} - \hat{h}_{k,l}^{Im} \end{bmatrix}\right)}{\exp\left(-\frac{1}{2} [s_{k,l,Re}^{Re} + \hat{h}_{k,l}^{Re} \quad s_{k,l,Re}^{Im} + \hat{h}_{k,l}^{Im}] \Sigma_{k,l,Re}^{-1} \begin{bmatrix} s_{k,l,Re}^{Re} + \hat{h}_{k,l}^{Re} \\ s_{k,l,Re}^{Im} + \hat{h}_{k,l}^{Im} \end{bmatrix}\right)} \right] \quad (26)$$

$$e_{ESE}(x_k^{Re}(j))_l = 2 \frac{h_{k,l}^{Re} s_{k,l,Re}^{Re} V[\zeta_{k,l,Re}^{Im}] + h_{k,l}^{Im} s_{k,l,Re}^{Im} V[\zeta_{k,l,Re}^{Re}] - (h_{k,l}^{Re} s_{k,l,Re}^{Im} + h_{k,l}^{Im} s_{k,l,Re}^{Re}) Cov[\zeta_{k,l,Re}^{Re}, \zeta_{k,l,Re}^{Im}]}{V[\zeta_{k,l,Re}^{Re}] V[\zeta_{k,l,Re}^{Im}] - Cov[\zeta_{k,l,Re}^{Re}, \zeta_{k,l,Re}^{Im}]^2} \quad (27)$$

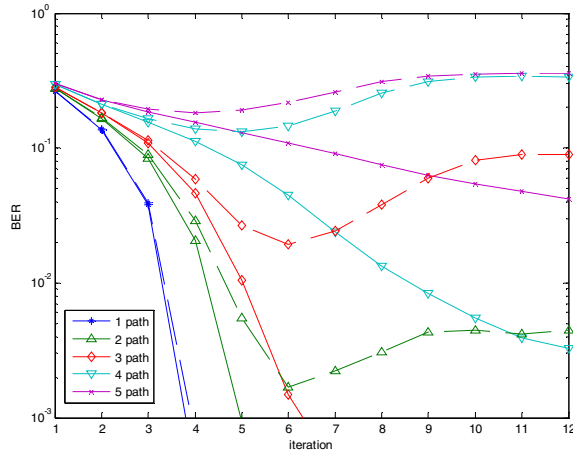


Fig. 4. BER vs number of iteration for different number of paths, channel error standard deviation of 0.1, without noise, 24 users. Solid lines include channel estimation error treatment while dashed lines omit it.

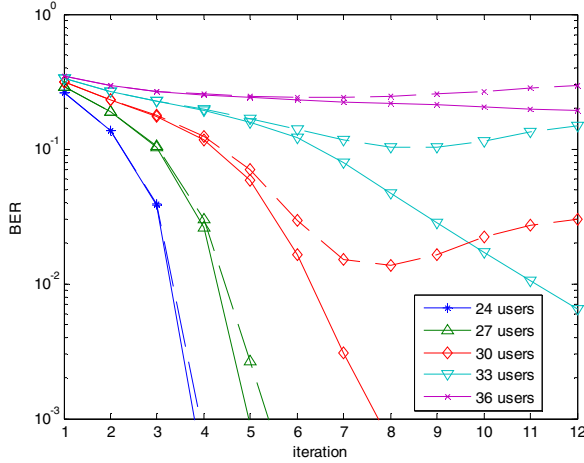


Fig. 5. BER vs number of iteration for different number of users, channel error standard deviation of 0.1, without noise, single path. Solid lines include channel estimation error treatment while dashed lines omit it.

Fig. 3 compares the performances of IDMA whether or not the channel estimation error is considered for different level of errors. It can be seen that IDMA performs much better when channel estimation errors are considered. Not only is the BER systematically lower, the BER decreases monotonically with the number of iterations.

Fig. 4 shows the effect of channel estimation error for multipath channels. The estimation error standard deviation is 0.1 for every path considered. The decrease in the BER when the receiver considers channel estimation errors is greater for a multipath channel.

Fig. 5 shows the performance of the decoder when the number of users is increased. It can be seen that the performance in case of 33 users considering the estimation error are better than a system of 30 users that does not take channel estimation errors into account. We hence have found a gain in the number of users capacity of approximately 10%.

Fig. 6 shows the performance of IDMA with channel estimation error in the presence of noise. The algorithm is shown to converge when noise is added.

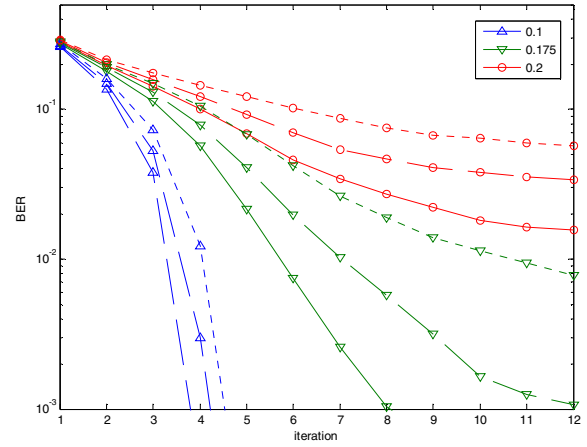


Fig. 6. BER vs number of iteration for different SNR and channel error standard deviation, 24 users, single path. Solid lines are at 20 dB, large dashed lines at 3dB and small dashed lines at 0dB.

V. CONCLUSION

IDMA detector in presence of channel estimation errors and QPSK signaling has been proposed. This detector can tolerate high channel estimation error levels. The advantages of considering the error have been shown. The modification needed for this consideration has been shown to be equivalent to additive noise. This term is equivalent for every user, every path and for in-phase and quadrature components.

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