AR #22088 - 7.1i MAP - "WARNING:PhysDesignRules:367 - The signal <DESIGN_MODULE/TXN> is incomplete"

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7.1i MAP - "WARNING:PhysDesignRules:367 - The signal <	DESIGN_MODULE/	TXN> is incomplet	Feedback Pr
Description		AR#	22088
Description Keywords: TXP		Part	FPGA-RocketIO
When I implement a Virtex-4 MGT design, the following warning is issued by MAP against RocketIO TX serial pins:	g is issued by	Last Modified	2006-08-21 00:00:00.0
		Status	Active
"WARNING:PhysDesignRules:367 - The signal <design_moi incomplete."<="" td=""><td>OULE/TXN> is</td><td>Keywords</td><td>TXP</td></design_moi>	OULE/TXN> is	Keywords	TXP
The signal does not drive any load pins in the design.			
Solution			
	safely ignored		
If all signals are completely routed at PAR, this warning can be	Salety Ignoreu.		

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